

# UNIT - IV

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- ❖ **Intermediate Code Generation (ICG):** Intermediate languages – Graphical representations, Three Address code, Quadruples, Triples.
- ❖ **Code Optimization:** Principal sources of optimization, Optimization of Basic blocks.

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- Intermediate languages
  - Implementation of 3-address statements
  - Translation of simple statements and control flow statements.

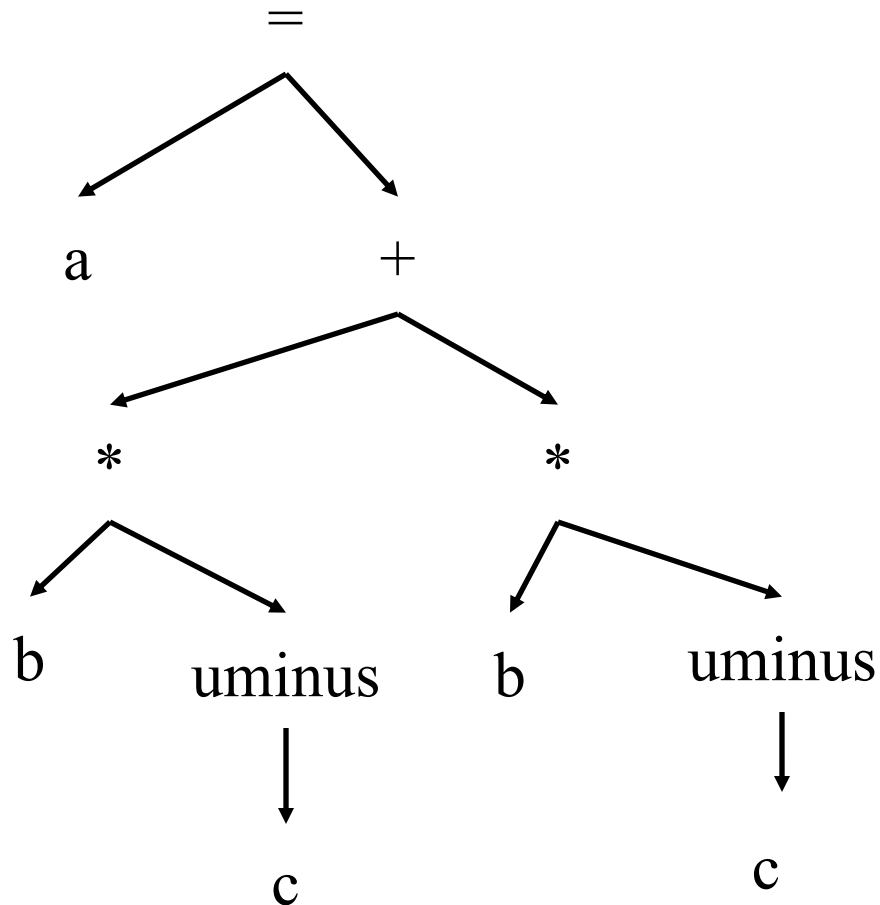
# Intermediate Code Generation

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- ❖ The given program in a source language is converted to an equivalent program in an intermediate language by the intermediate code generator.
- ❖ Intermediate language can be many different languages, and the designer of the compiler decides this intermediate language.
  - ❑ syntax trees (abstract syntax trees)
  - ❑ postfix notation
  - ❑ three-address code

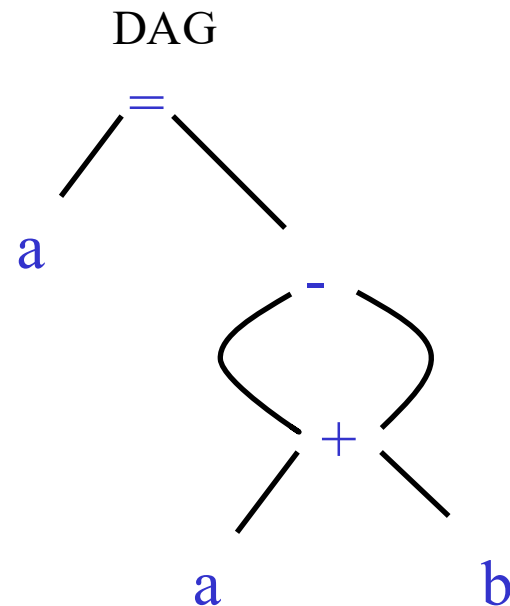
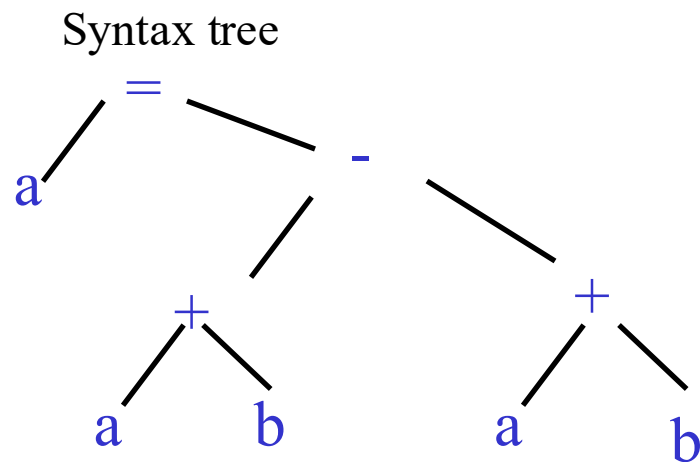
# Syntax Trees

- Consider the assignment **a=b\*-c+b\*-c**



# DAG (Directed Acyclic Graph )

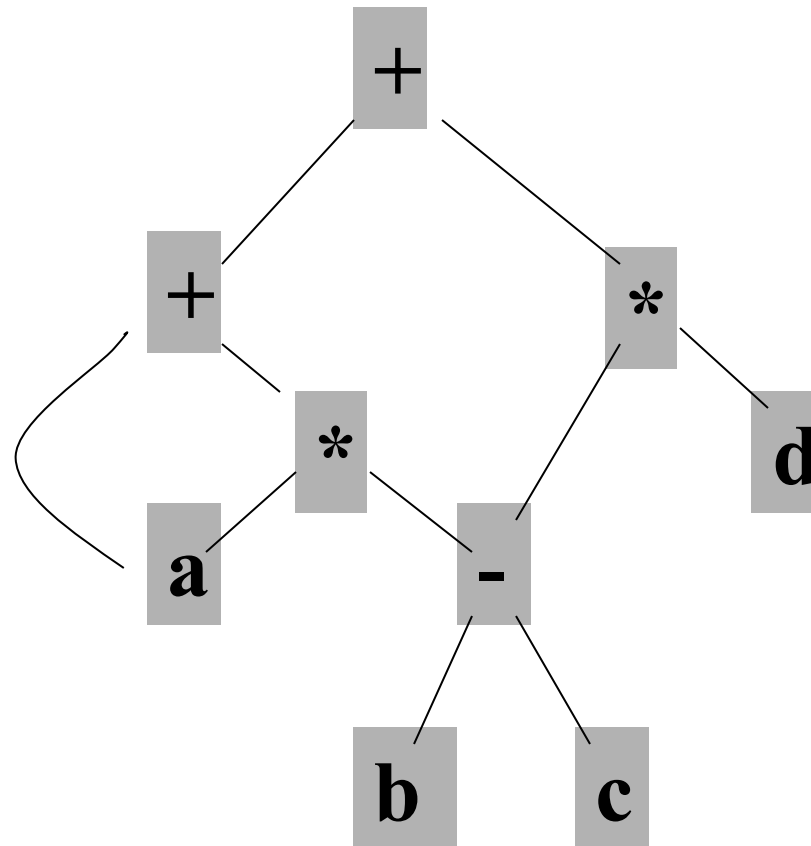
- ❖ A **dag** is similar to syntax tree , and it identifies the common sub expressions in the expression.
- ❖ A node in a dag representing a common sub expression has more than one parent .
- ❖ In a syntax tree, the common sub expressions would be represented as duplicated subtree.
- ❖
- ❖ Ex : consider an expression :  $a = (a+b) - (a+b)$



# Directed Acyclic Graphs for Expressions

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$a + a * (b - c) + (b - c) * d$



# Postfix notation

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Postfix notation is a linear representation of a syntax trees.

Consider an expression :  $a = b * -c + b * -c$

postfix notation :  **$a \ b \ c \ - \ * \ b \ c \ - \ * \ + \ =$**

# Constructing Syntax Trees for Expressions

1. Syntax trees are constructed using following functions:
  - i. *mknode(op , left, right) : creates an operator node with label op and two fields containing pointers to left child and right child.*
  - ii. *mkleaf (id ,entry) : creates an identifier node with label id and a field containing entry, a pointer to the symbol table.*
  - iii. *mkleaf (num , val) : creates a number node with label num and a field containing val , the value of the number*
2. Each function returns a pointer to a newly created node



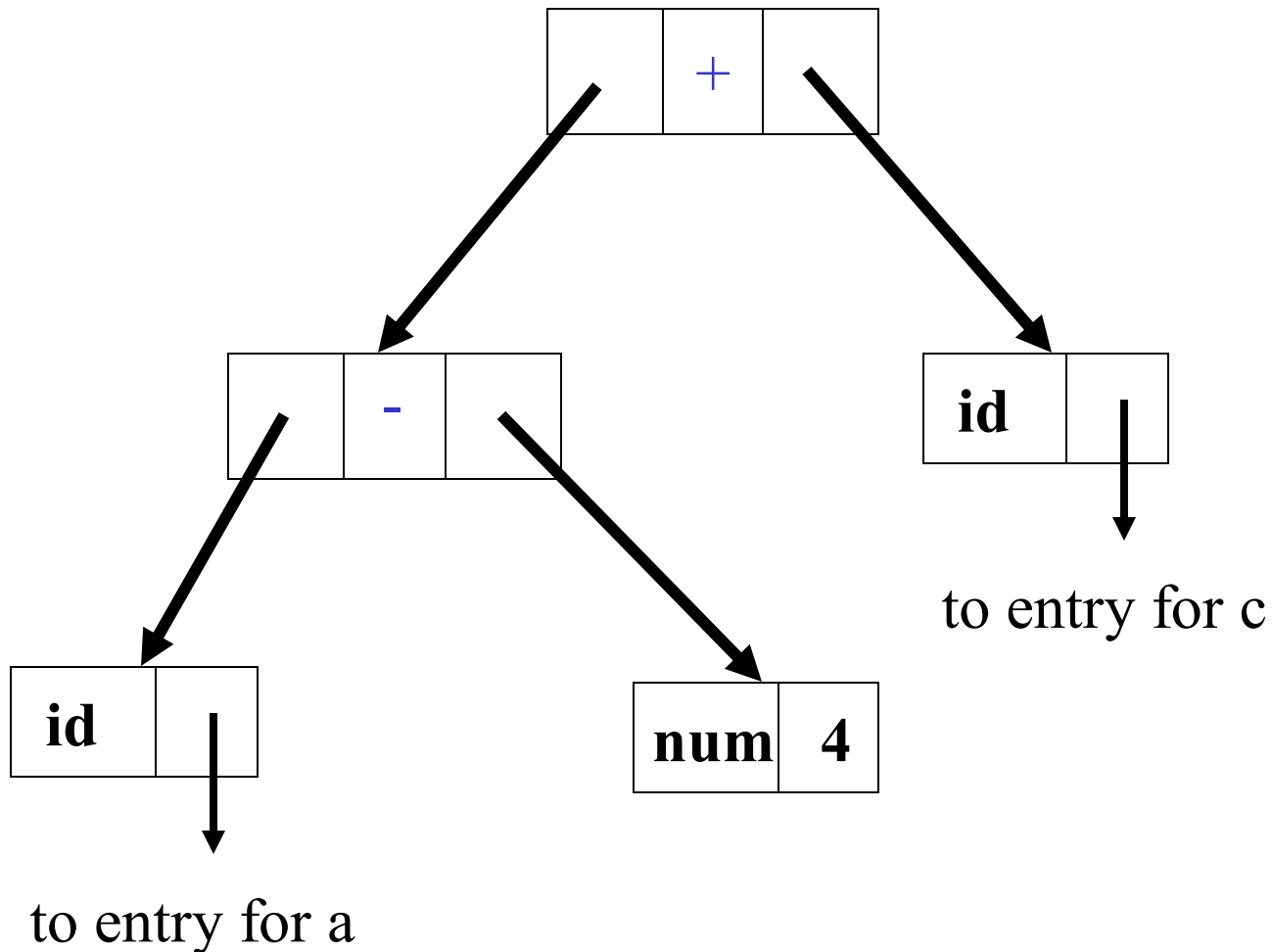
The following sequence of function calls creates the syntax tree  
for the expression : **a-4+c**

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- ❖ 1) **p1= mkleaf ( id , entrya);**
  - ❖ 2) **p2=mkleaf(num , 4);**
  - ❖ 3) **p3=mknnode('-', p1 ,p2);**
  - ❖ 4) **p4=mkleaf(id , entryc);**
  - ❖ 5) **p5=mknnode('+', p3 , p4);**
- 
- ❖ **In this sequence , p1 ,p2 , .....p5 are pointers to nodes**
  - ❖ **entrya and entryc are pointers to symbol table entries for identifiers a and c**
  - ❖ **The syntax tree is constructed bottom up.**

# Draw the Syntax Tree

**a-4+c**



# SDD for Syntax Trees

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## PRODUCTION

$E \rightarrow E_1 + T$

$E \rightarrow E_1 - T$

$E \rightarrow T$

$T \rightarrow (E)$

$T \rightarrow \text{id}$

$T \rightarrow \text{num}$

## SEMANTIC RULE

$E.nptr = mknnode('+', E_1.nptr, T.nptr)$

$E.nptr = mknnode('-', E_1.nptr, T.nptr)$

$E.nptr = T.nptr$

$T.nptr = E.nptr$

$T.nptr = mkleaf(id, id.entry)$

$T.nptr = mkleaf(num, num.val)$

The synthesized attribute *nptr* for E and T keeps track of the pointers returned by the function calls

# Three Address Code

- ❖ **Three address code is a linear representation of syntax tree**
- ❖ **Statements of general form     $x = y \text{ op } z$**
- ❖ **Ex:     $x = y + z * w$   
should be represented as**

$t_1 = z * w$   
 $t_2 = y + t_1$   
 $x = t_2$
- ❖ **Where  $t_1$  and  $t_2$  are compiler generated temporary names**

# Example of 3-address code

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**a := b \* -c + b \* -c**

```
t1 = - c
t2 = b * t1
t3 = - c
t4 = b * t3
t5 = t2 + t4
a = t5
```

Code for the syntax tree

```
t1 = - c
t2 = b * t1
t5 = t2 + t2
a = t5
```

Code for the DAG

# Types of Three-Address Statements.

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<i>Assignment Statement:</i>	$x = y \text{ op } z$
<b>Assignment Statement:</b>	$x = \text{op } z$ (op is unary operator)
<b>Copy Statement:</b>	$x = z$
<b>Unconditional Jump:</b>	goto L
<b>Conditional Jump:</b>	if x relop y goto L
<b>Stack Operations:</b>	Push/pop

## **Procedure (function):**

param  $x_1$   
param  $x_2$   
...  
param  $x_n$   
call p,n

## **Index Assignments:**

$x = y[i]$   
 $x[i] = y$

## **Address and Pointer Assignments:**

$x = \&y$   
 $x = *y$   
 $*x = y$

# Implementations of 3-address statements

- ❖ In compiler, The Three Address statements are implemented using following representations:
- ❖ **Quadruples**
- ❖ **Triples**
- ❖ **Indirect triples**
- ❖ **A quadruple is a record with four fields , op , arg1, arg2 and result.**

<b>op</b>	<b>arg1</b>	<b>arg2</b>	<b>result</b>
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# Implementations of 3-address statements using quadruple

## ❖ 3-address code

$t_1 = -c$   
 $t_2 = b * t_1$   
 $t_3 = -c$   
 $t_4 = b * t_3$   
 $t_5 = t_2 + t_4$   
 $a = t_5$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>	<i>result</i>
(0)	<b>uminus</b>	<b>c</b>		<b>t<sub>1</sub></b>
(1)	<b>*</b>	<b>b</b>	<b>t<sub>1</sub></b>	<b>t<sub>2</sub></b>
(2)	<b>uminus</b>	<b>c</b>		<b>t<sub>3</sub></b>
(3)	<b>*</b>	<b>b</b>	<b>t<sub>3</sub></b>	<b>t<sub>4</sub></b>
(4)	<b>+</b>	<b>t<sub>2</sub></b>	<b>t<sub>4</sub></b>	<b>t<sub>5</sub></b>
(5)	<b>=</b>	<b>t<sub>5</sub></b>		<b>a</b>

**Temporary names must be entered into the symbol table as they are created.**



# Implementations of 3-address statements

## ❖ Triples

$t_1 = -c$

$t_2 = b * t_1$

$t_3 = -c$

$t_4 = b * t_3$

$t_5 = t_2 + t_4$

$a = t_5$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	<b>uminus</b>	<b>c</b>	
(1)	<b>*</b>	<b>b</b>	<b>(0)</b>
(2)	<b>uminus</b>	<b>c</b>	
(3)	<b>*</b>	<b>b</b>	<b>(2)</b>
(4)	<b>+</b>	<b>(1)</b>	<b>(3)</b>
(5)	<b>assign</b>	<b>a</b>	<b>(4)</b>

**Temporary names are not entered into the symbol table.**

# Other types of 3-address statements

- ❖ e.g. ternary operations like

$x[i] = y$

$x = y[i]$

- ❖ require two or more entries. e.g.

$x[i] = y$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	[ ] =	x	i
(1)	<b>assign</b>	(0)	y

$x = y[i]$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	[ ] =	y	i
(1)	<b>assign</b>	x	(0)

# Implementations of 3-address statements

## ❖ Indirect Triples

	<i>statement</i>
(0)	<b>(14)</b>
(1)	<b>(15)</b>
(2)	<b>(16)</b>
(3)	<b>(17)</b>
(4)	<b>(18)</b>
(5)	<b>(19)</b>

*triple*

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(14)	<b>uminus</b>	<b>c</b>	
(15)	<b>*</b>	<b>b</b>	<b>(14)</b>
(16)	<b>uminus</b>	<b>c</b>	
(17)	<b>*</b>	<b>b</b>	<b>(16)</b>
(18)	<b>+</b>	<b>(15)</b>	<b>(17)</b>
(19)	<b>assign</b>	<b>a</b>	<b>(18)</b>

# Syntax-Directed Translation into 3-address code.

## 1. Assignment statements:

- ❖ The following grammar is defined for assignment statements:

$S \rightarrow id = E$

$E \rightarrow E1 + E2 \mid E1 * E2 \mid -E1 \mid (E1) \mid id$

In order to write syntax directed definition for the above grammar, the following attributes are defined for the non terminal E:

- ❑  $E.place$ : the name that will hold the value of E
  - Identifier will be assumed to already have the place attribute defined.
- ❑  $E.code$ : hold the three address code statements that evaluate E (this is the 'translation' attribute).
- ❖ The function ***newtemp*** returns sequence of temporary variables.
- ❖ The function ***gen*** generates a single three address statement.

Ex:  $gen(x := y + z)$  represent the 3-address statement  $x := y + z$

# SDD for Syntax Trees using function calls

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## PRODUCTION

$S \rightarrow \text{id} := E$

$E \rightarrow E_1 + E_2$

$E \rightarrow E_1 * E_2$

$E \rightarrow -E_1$

$E \rightarrow ( E_1 )$

$E \rightarrow \text{id}$

## SEMANTIC RULE

$E.nptr := mknode('assign',$   
 $\quad mkleaf(id, id.place), E.nptr )$

$E.nptr := mknode('+', E_1.nptr , E_2.nptr)$

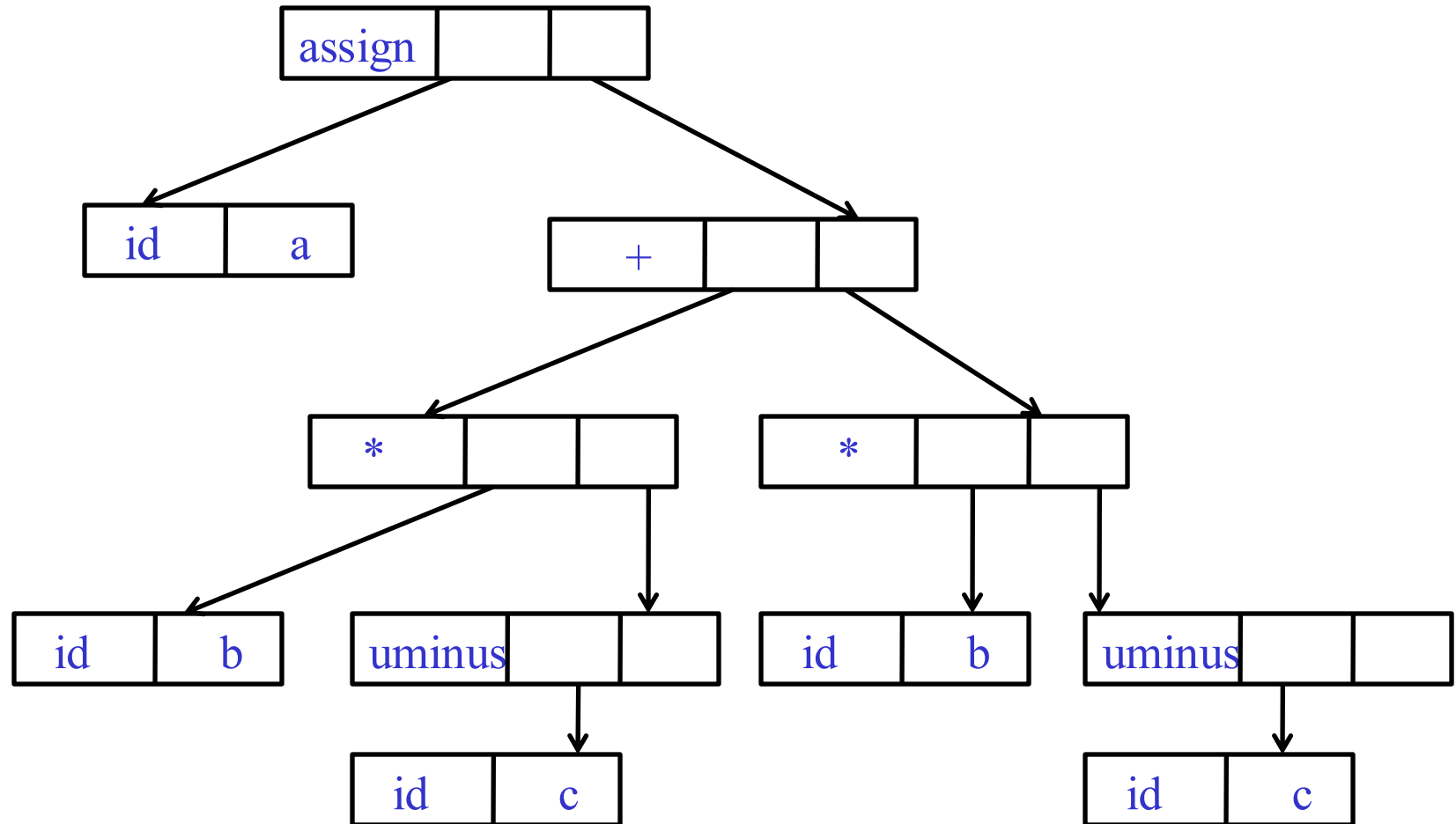
$E.nptr := mknode('*', E_1.nptr , E_2.nptr)$

$E.nptr := mknode('uminus', E_1.nptr )$

$E.nptr := E_1.nptr$

$E.nptr := mkleaf(id, id.place)$

# Syntax Tree for $a = b * -c + b * -c$ using function calls



# Syntax-Dir. Definition for 3-address code

<u>PRODUCTION</u>	<u>SEMANTIC RULE</u>
$S \rightarrow \text{id} := E$	$S.code := E.code    \text{gen}(\text{id.place} '=' E.place)$
$E \rightarrow E_1 + E_2$	$E.place := \text{newtemp} ;$ $E.code := E_1.code    E_2.code$ $   \text{gen}(E.place ':=' E_1.place '+' E_2.place)$
$E \rightarrow E_1 * E_2$	$E.place := \text{newtemp} ;$ $E.code := E_1.code    E_2.code$ $   \text{gen}(E.place ':=' E_1.place '*' E_2.place)$
$E \rightarrow - E_1$	$E.place := \text{newtemp} ;$ $E.code := E_1.code$ $   \text{gen}(E.place '=' 'uminus' E_1.place)$
$E \rightarrow ( E_1 )$	$E.place := E_1.place ; E.code = E_1.code$
$E \rightarrow \text{id}$	$E.place := \text{id.entry} ; E.code = ''$

# Boolean Expressions:

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- ❖ Boolean expressions are composed of the Boolean operators (*and*, *or*, and *not*) applied to the elements that are Boolean variables or relational expressions.
- ❖  $E \rightarrow E \text{ or } E \mid E \text{ and } E \mid \text{not } E \mid (E) \mid \text{id1 rel op id2} \mid \text{true} \mid \text{false}$



# Methods of implementing Boolean expressions

- ❖ There are two principal methods of representing the value of a Boolean expression.
- ❖ The first method is to encode true and false numerically and to evaluate a Boolean expression analogously to an arithmetic expression.
- ❖ The second principle method is by flow of control , that is , representing the value of a Boolean expression by a position reached in a program. Here we have adopted the first method

consider the conditional statement :                      Numerical representation:

if  $a < b$  1 else 0

Ex:

100: if  $a < b$  goto 103

101:  $t1 := 0$

102: goto 104

103:  $t1 := 1$

104:

---

Translation scheme for producing 3-address code for above example

**E**     $\rightarrow$     *id1 relop id2*

```
{E.place := newtemp;  
emit ('if' id1.place relop.op id2.place 'goto' nextstat+3)  
emit (E.place ':=' '0')  
emit ('goto' nextstat+2)  
emit (E.place ':=' '1') }
```

Assume that

*emit* places three Address statements into an output file

*nextstat* gives the index of the three address statement in the output sequence

*emit* increments *nextstat* after producing each three address statement

## Semantic Actions for producing Three Address Codes for Boolean Expressions:

**E → E1 *or* E2**

```
{
    E.place := newtemp();
emit (E.place ':=' E1.place 'or' E2.place); }
```

**E → E1 *and* E2**

```
{
    E.place := newtemp();
emit (E.place ':=' E1.place 'and' E2.place); }
```

**E → *not* E**

```
{
    E.place := newtemp();
emit ( E.place ':=' 'not' E.place); }
```

---

**E**  $\rightarrow$  ( E1 )

{ E.place := E1.place }

**E**  $\rightarrow$  *id1 relop id2*

{ E.place := newtemp;  
emit ('if' *id1.place relop.op id2.place* 'goto' nextstat+3)  
emit (E.place ':= ' '0')  
emit ('goto' nextstat+2)  
emit (E.place ':= ' '1') }

**E**  $\rightarrow$  *true*

{ E.place := newtemp;  
emit (E.place ':= ' '1') }

**E**  $\rightarrow$  *false*

{ E.place = newtemp;  
emit (E.place ':= ' '0') }

## Flow of control statements:

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In second method the boolean expressions are evaluated using flow of control statements ;if-then ,if –else ; while-do.

The following grammar is used to produce such statements:-

$S \rightarrow \text{if } E \text{ then } S1$

$S \rightarrow \text{if } E \text{ then } S1 \text{ else } S2$

$S \rightarrow \text{while } E \text{ do } S1$

## Functions and Attributes used in the translation of control Statements :-

Flow of control statements may be converted to three address code by use of the following functions:-

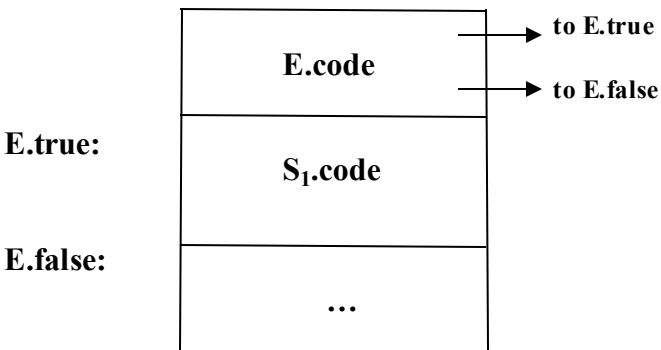
- ❖ newlabel – returns a new symbolic label each time it is called.
- ❖ gen ( ) – “generates” the code (string) passed as a parameter to it.

The following attributes are associated with the non-terminals for the code generation:-

- ❖ code – contains the generated three address code.
- ❖ true – contains the label to which a jump takes place if the Boolean expression associated (if any) evaluates to “true”.
- ❖ false – contains the label to which a jump takes place if the Boolean expression (if any) associated evaluates to “false”.
- ❖ begin – contains the label / address pointing to the beginning of the code chunk for the statement “generated” (if any) by the non-terminal.
- ❖ next - contains the label / address pointing to the end of the code chunk for the statement “generated” (if any) by the non-terminal



The simulation of the flow of control branching for each statement is depicted pictorially as follows



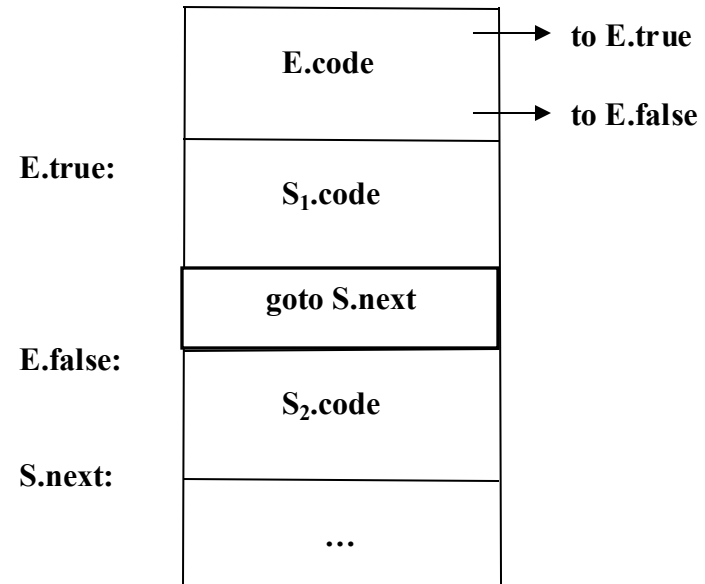
if - then

***$S \rightarrow \text{if } E \text{ then } S1$***

**E.true := newlabel ;**  
**E.false := S.next ;**  
**S1.next := S.next ;**  
**S.code := E.code || gen(E.true ':')**  
**|| S1.code**

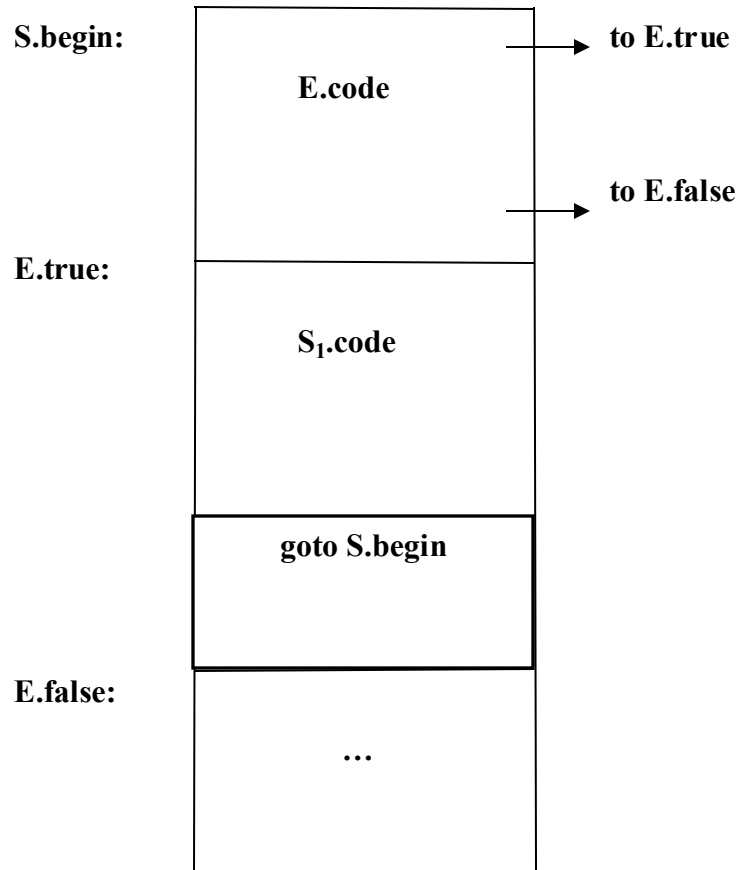
**$S \rightarrow \text{if } E \text{ then } S1 \text{ else } S2$**

**E.true := newlabel ;  
E.false := newlabel ;  
S1.next := S.next ;  
S2.next := S.next ;  
S.code := E.code || gen(E.true ':')  
|| S1.code || gen('goto' S.next) ||  
gen(E.false ':') || S2.code**





# $S \rightarrow \text{while } E \text{ do } S1$



## $S \rightarrow \text{while } E \text{ do } S1$

```
S.begin := newlabel ;  
E.true := newlabel ;  
E.false := S.next ;  
S1.next := S.begin ;  
S.code := gen(S.begin ':') || E.code ||  
gen(E.true ':') || S1.code || gen('goto'  
S.begin)
```

**while - do**