

Chitresh Sharma

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Objective

Aspiring VLSI and Embedded Systems Engineer with a strong foundation in Digital Electronics, HDL (Verilog), and FPGA prototyping, supported by hands-on internship experience at ECIL. Passionate about contributing to RTL design, simulation, verification, and embedded system development while leveraging analytical, programming, and teamwork skills to deliver efficient hardware solutions.

Experience

Electronics Corporation of India Limited (ECIL), Hyderabad – VLSI Internship **July 2025**

- Completed a project on “**SHA-256 Implementation with Auto Padding and MicroBlaze Core Processor**” in the Corporate Research and Development division.
 - Worked on AC 701 (Artix – 7) FPGA-based digital design, RTL module development, and hardware-software integration.
 - Gained exposure to Verilog HDL, Xilinx Vivado, simulation environments, IP Core and Block Designs.
 - Performance during training was rated as “**Excellent**”.

Education

Guru Nanak Institutions Technical Campus, B.Tech ECE **2022 - Present**

Current CGPA: 9.24, Expected Graduation in 2026

Little Flower Junior College, Intermediate - TSBIE **2020 - 2022**

Major: MPC (Mathematics, Physics & Chemistry) | Percentage: 96.7%

Oakwood International School, Schooling - CBSE 2012 - 2022

Percentage: 91%

SKILLS & ABILITIES

- VLSI & Digital Design: Verilog, RTL Design, FPGA (Xilinx, Vivado)
 - Programming: Embedded C, TCL Scripting Language, C, Python, HTML
 - Tools: KiCad(PCB Design), GitHub, ClickUp, MS Office, Canva, Figma, Lightroom, Photoshop
 - Core Strengths: Problem Solving, Team Coordination, Documentation, Event Planning

Leadership

Shutter Clicks (Photography Club) | President

2024 - Present

- Directed photography and videography coverage for 10+ college events, improving event visibility and student engagement by 30%.
- Orchestrated targeted Instagram and Facebook ad campaigns for 10+ college events, achieving an average click-through rate of 1.5% and driving a 40% increase in event registrations.

Yuvnix (College Cultural Event) | Head Organizer

2022 - 2023

- Successfully organized two high-scale campus events with 1200+ attendees.
- Facilitated communication and collaboration between 12 student committees, streamlining event preparations that enhanced overall organization and resulted in zero major issues during the two-day cultural festival.

Achievements & Certifications

- Awarded 1st Topper of the Batch out of 60+ students (CGPA: 9.24) during Annual Day Celebrations, Guru Nanak Institutions Technical Campus – *April 2025*
- Led a 8-member media team as Coordinator for Photography, Videography, and Social Media during the 2nd National-Level Hackathon Hack Arena with 350+ participants – *November 21–23, 2024*
- Earned NPTEL Elite Certificate in “The Joy of Computing Using Python” with a score of 68%, ranking in the top 25% out of 12,432 participants – *January to April 2024*
- Received Best Class Representative Award among class reps for excellence in student coordination and communication – *April 2024*

Language Proficiency

English (Fluent), Hindi (Fluent), Telugu (Conversational)

Declaration

I hereby declare that all the information furnished above is true and correct to the best of my knowledge and belief. I bear responsibility for the correctness of the above-mentioned particulars.

Place: Hyderabad

Date: 12 November 2025

Signature: Chitresh Sharma