

CHITRESH SHARMA

Aspiring VLSI and Embedded Systems Engineer

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 Hyderabad, Telangana - 500060

 23 - 11 - 2004

 chitresh-portfolio

 chitresh-sharma-electronics

 Chitreshsharma23

 Male

EDUCATION

B.Tech ECE

Guru Nanak Institutions
Technical Campus

2022 – Present

Current CGPA: 9.24, Expected
Graduation in 2026

Intermediate - TSBIE

Little Flower Junior College

2020 – 2022

Major: MPC (Mathematics, Physics &
Chemistry) | Percentage: 96.7%

Schooling - CBSE

Oakwood International School

2012 – 2022

Percentage: 91%

EXPERIENCE

VLSI Internship

Electronics Corporation of India Limited (ECIL), Hyderabad

07/2025 | Hyderabad

Completed a project on "SHA-256 Implementation with Auto Padding and MicroBlaze Core Processor" in the Corporate Research and Development division Worked on AC 701 (Artix -7) FPGA-based digital design, RTL module development, and hardware-software integration.

ACHIEVEMENTS & CERTIFICATIONS

1st Topper of the Batch out of 60+ students (CGPA: 9.24)

Guru Nanak Institutions Technical Campus

01/03/2025

NPTEL Elite Certificate in "The Joy of Computing Using Python"

NPTEL

01/03/2024

Best Class Representative Award

01/03/2024

Coordinator for Photography, Videography, and Social Media

2nd National-Level Hackathon Hack Arena

21/11/2024

LEADERSHIP

President

2024 – Present

Directed photography and videography coverage for 10+ college events, improving event visibility and student engagement by 30%.

Head Organizer (Yuvnix)

2022 – 2024

Successfully organized two high-scale campus events with 1200+ attendees.

LANGUAGES

English



Hindi



Telugu



Russian



DECLARATION

I declare that the above details are true and correct, and I take responsibility for their accuracy.

Place: Hyderabad Date: 20 December 2025