

CHITRESH SHARMA

Aspiring VLSI Engineer | HR & Operations Professional

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 Hyderabad, Telangana - 500060

 23 - 11 - 2004

 chitresh-portfolio

 chitresh-sharma-electronics

 Chitreshsharma23

 Male

EDUCATION

B.Tech ECE

Guru Nanak Institutions
Technical Campus

2022 – Present

Current CGPA: 9.24, Expected
Graduation in 2026

Intermediate - TSBIE

Little Flower Junior College

2020 – 2022

Major: MPC (Mathematics, Physics
& Chemistry) | Percentage: 96.7%

Schooling - CBSE

Oakwood International
School

2012 – 2022

Percentage: 91%



SKILLS & ABILITIES

VLSI & Digital Design

Verilog, RTL Design, Xilinx Artix-7
FPGA (Vivado Tool Chain)

Programming

Embedded C, TCL Scripting
Language, C, Python, HTML, CSS,
SQL

Core Strengths

Problem Solving, Team
Coordination, Documentation,
Stakeholder Communication,
Process Coordination, Compliance
& Quality Review, Recruitment
Support

Tools

GitHub, ClickUp, MS Office, Google
Workspace, KiCad, Records
Management



LANGUAGES

English | Hindi | Telugu |
Russian

EXPERIENCE

VLSI Internship

Electronics Corporation of India Limited (ECIL), Hyderabad

07/2025 | Hyderabad

Completed a **SHA-256 implementation with auto padding and MicroBlaze processor** in the Corporate R&D division, involving **RTL design and FPGA (Artix-7 AC701) integration**. Ensured **secure data processing, quality validation, anomaly detection, and accurate documentation** during hardware-software integration.

HR & Operations Intern

Learning Destiny Pvt. Ltd.

08/2025 – 10/2025 | Hyderabad

Supported **recruitment processes**, including intern and student record management. Assisted with **HR documentation, reporting, and data organization**. Coordinated with internal teams to ensure smooth daily operations and communication. Contributed to operational efficiency through structured workflow support



LEADERSHIP

Photography Club President (Shutter Clicks)

2024 – Present

Moderated and reviewed digital content for 10+ events to ensure brand safety and policy compliance, increasing engagement by 30%

Head Organizer (Yuvnix)

2022 – 2024

Managed **scaled operations and policy enforcement** for events with 1,200+ attendees to ensure a secure and compliant environment.



ACHIEVEMENTS & CERTIFICATIONS

1st Topper of the Batch out of 60+ students (CGPA: 9.24)

Guru Nanak Institutions Technical Campus

01/03/2025

NPTEL Elite Certificate in "The Joy of Computing Using Python"

NPTEL

01/03/2024

Best Class Representative Award

01/03/2024

Coordinator for Photography, Videography, and Social Media

2nd National-Level Hackathon Hack Arena

21/11/2024