

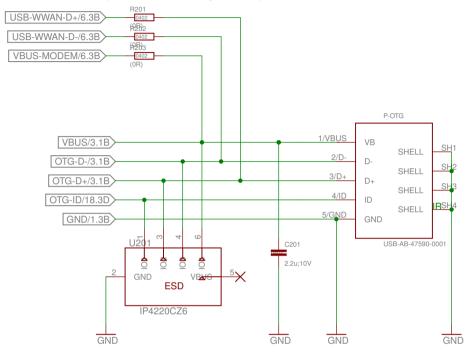
CAM2-GPIO/18.2D

place in scan matrix? would need 3-4 wires to UPPER board instead of 2!
No. VOL+ or VOL- can either be connected to GPIOs
or drive two FETs that sit in the keyboard matrix
in any case it is sufficient to connect GPIO-VOL+ and VOL- to two pins on the B2B connector

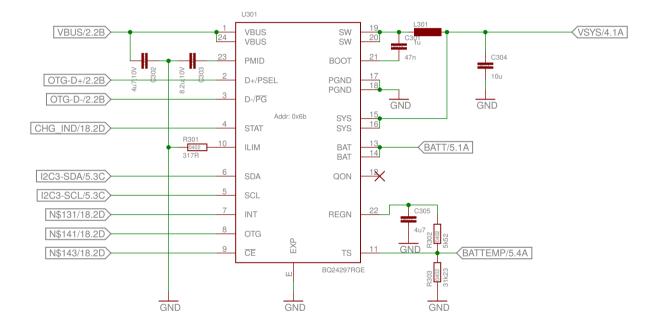
GPIO-VOL-/18.2D

Buttons		
TITLE: sedtest		
Document Number:		REV: V2b
Date: 09.09.15 04:52	Sheet: 1	/35

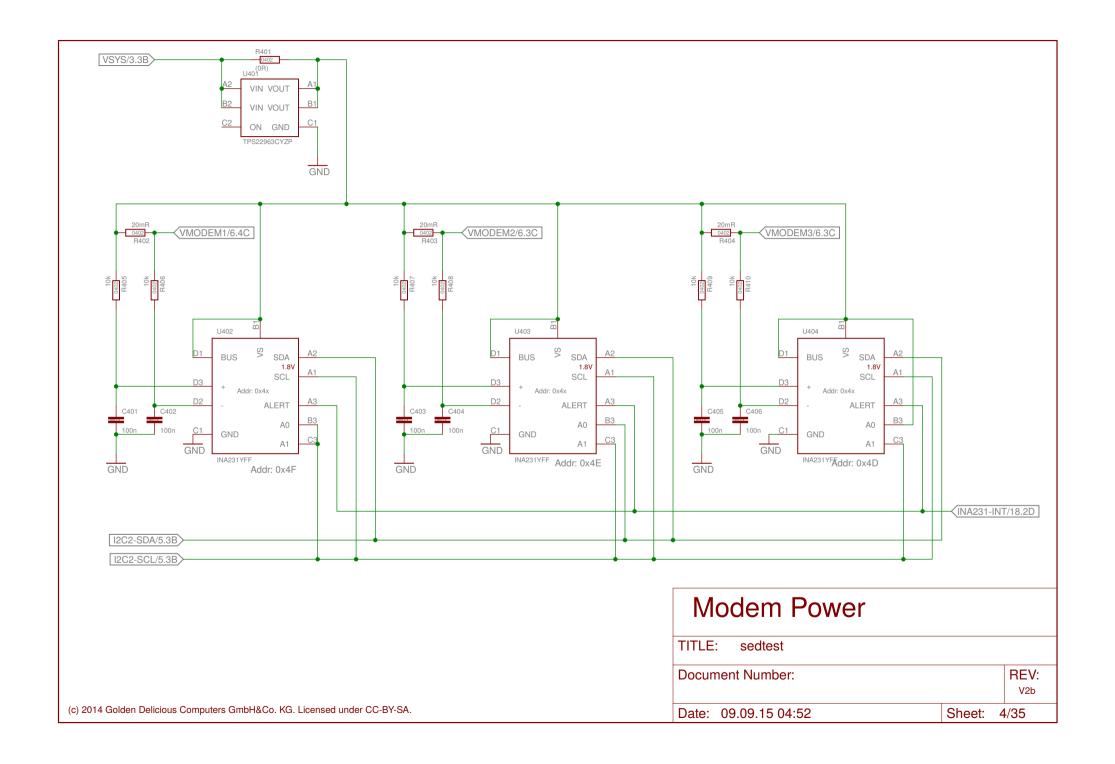


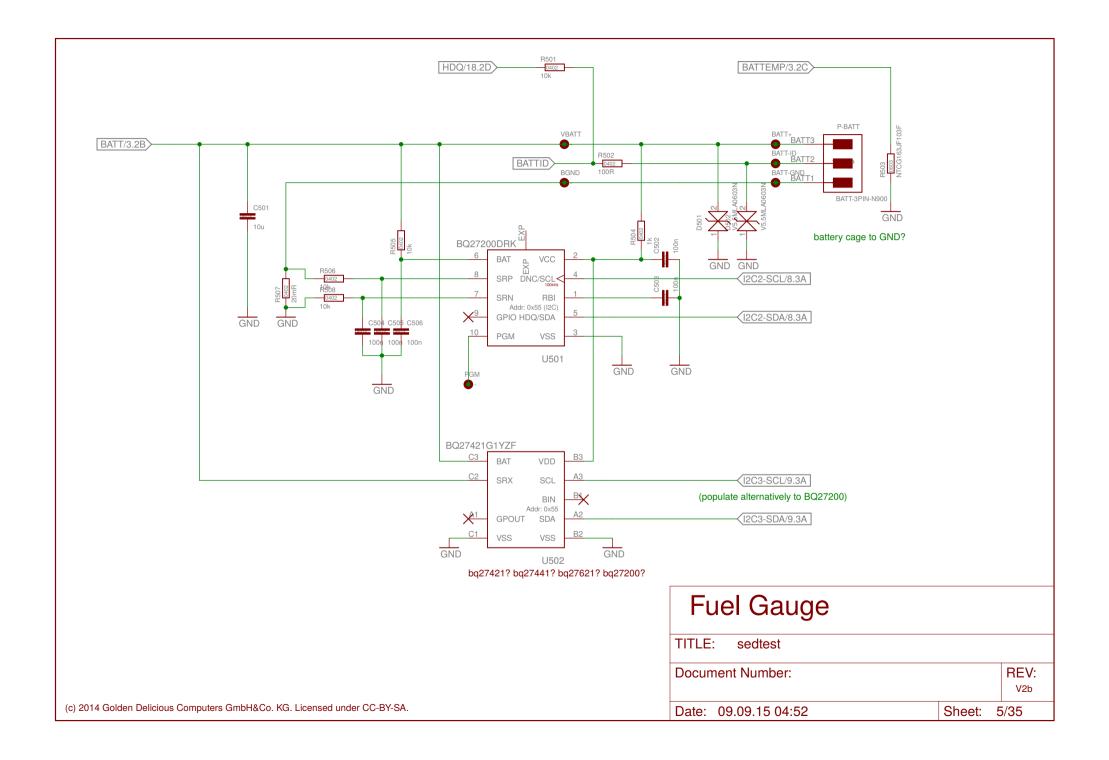


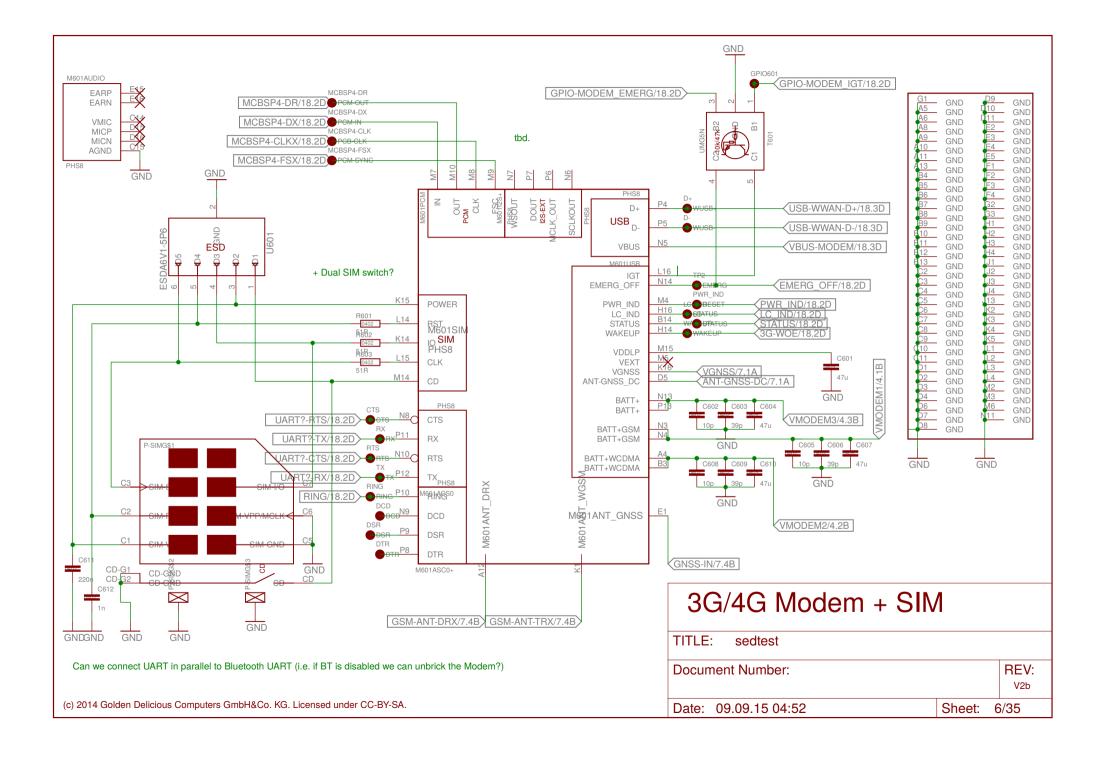
OTG			
TITLE: s	edtest		
Document I	Number:		REV: V2b
Date: 09.0	9.15 04:52	Sheet: 2	2/35

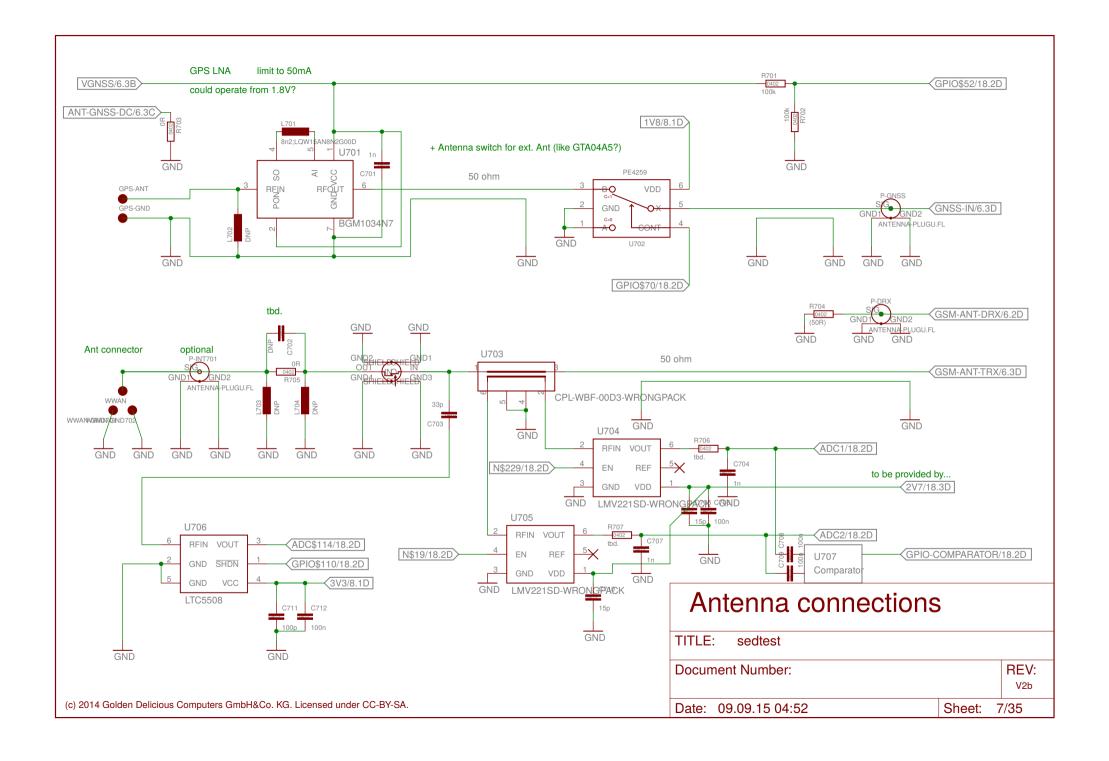


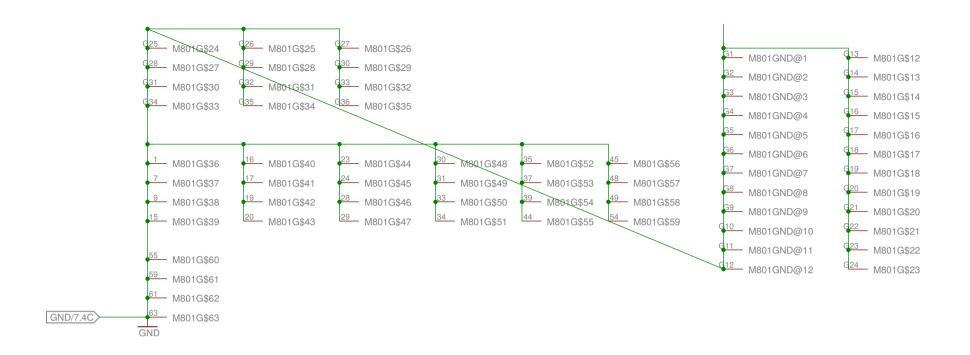
Charger/OTG-Booster			
TITLE: sedtest			
Document Number:		REV: V2b	
Date: 09.09.15 04:52	Sheet:	3/35	

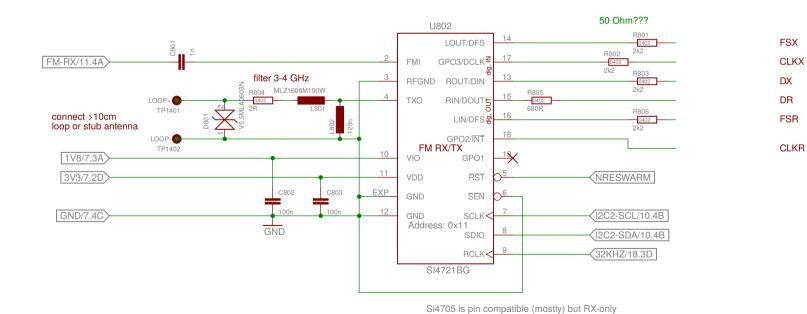


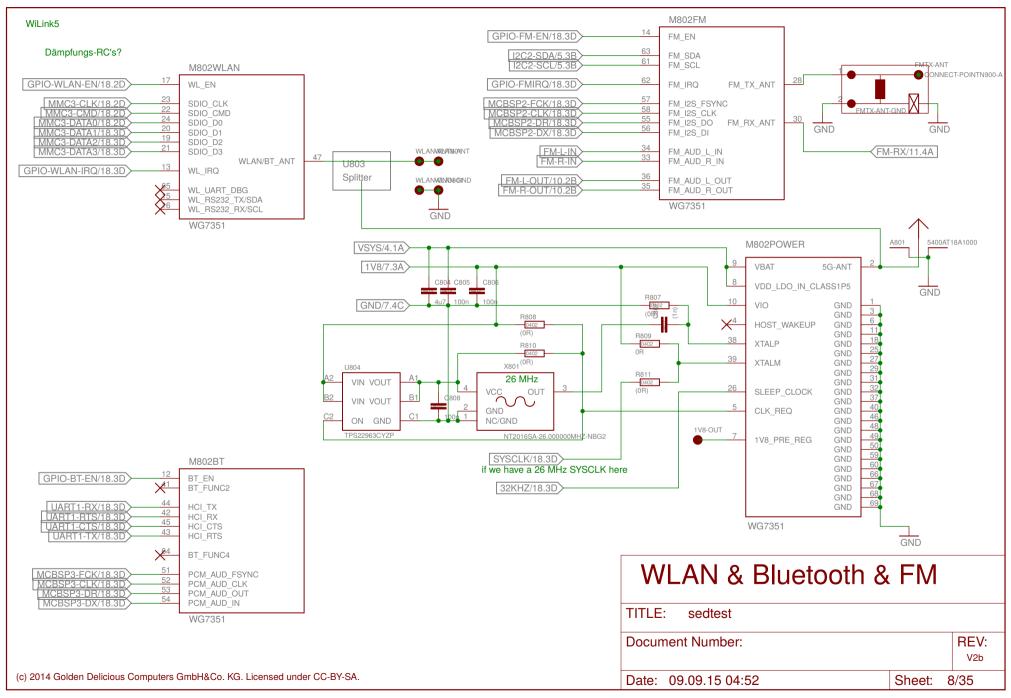


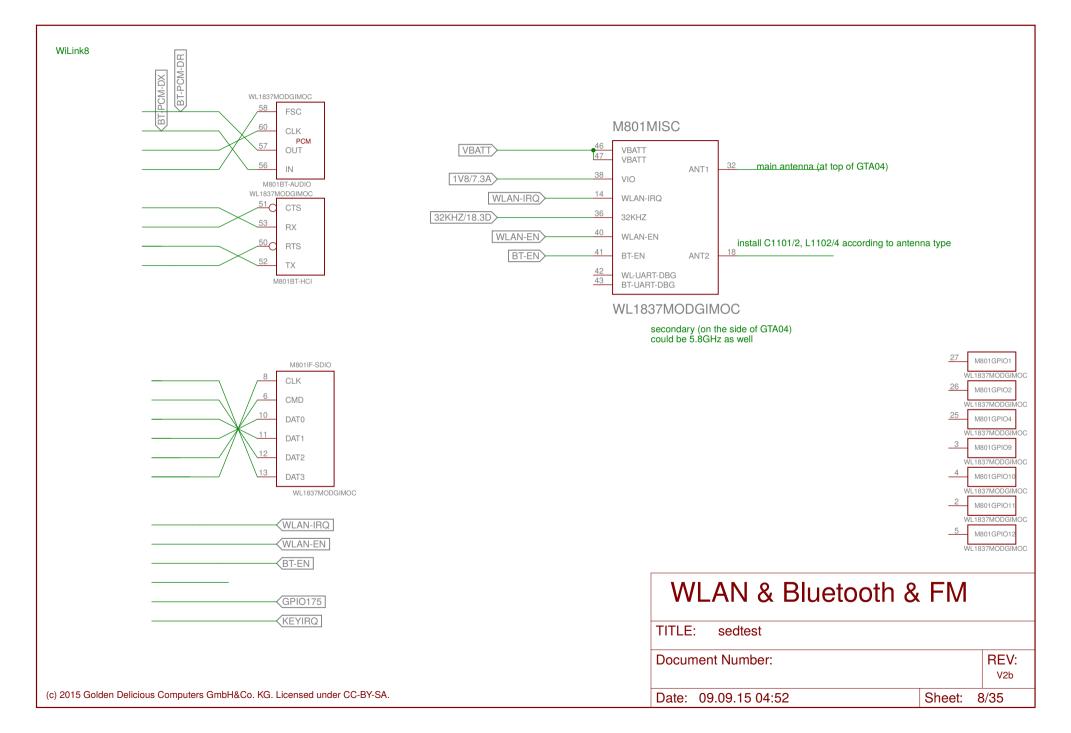




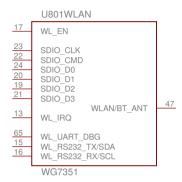


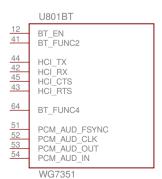


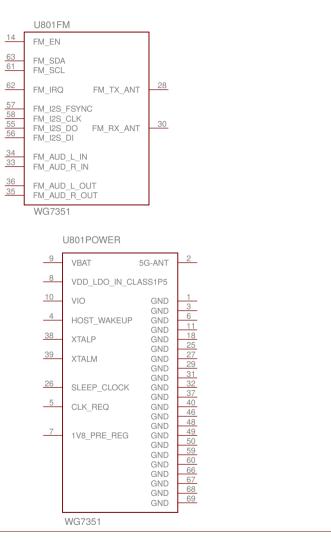






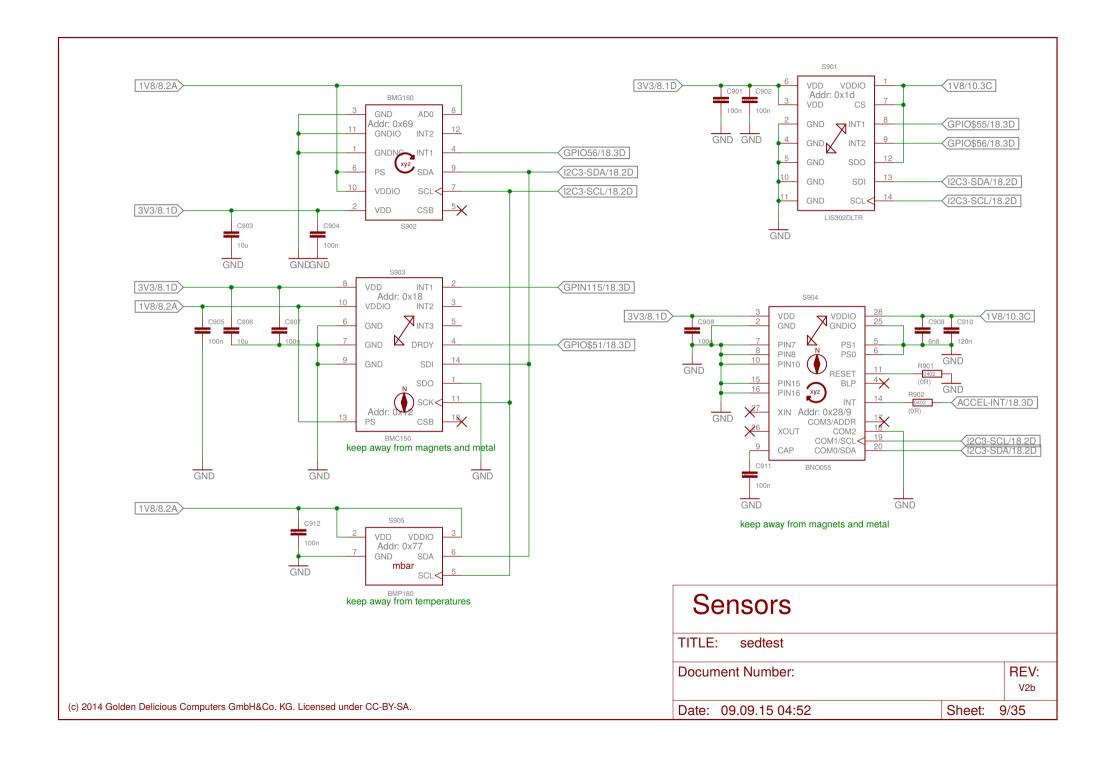


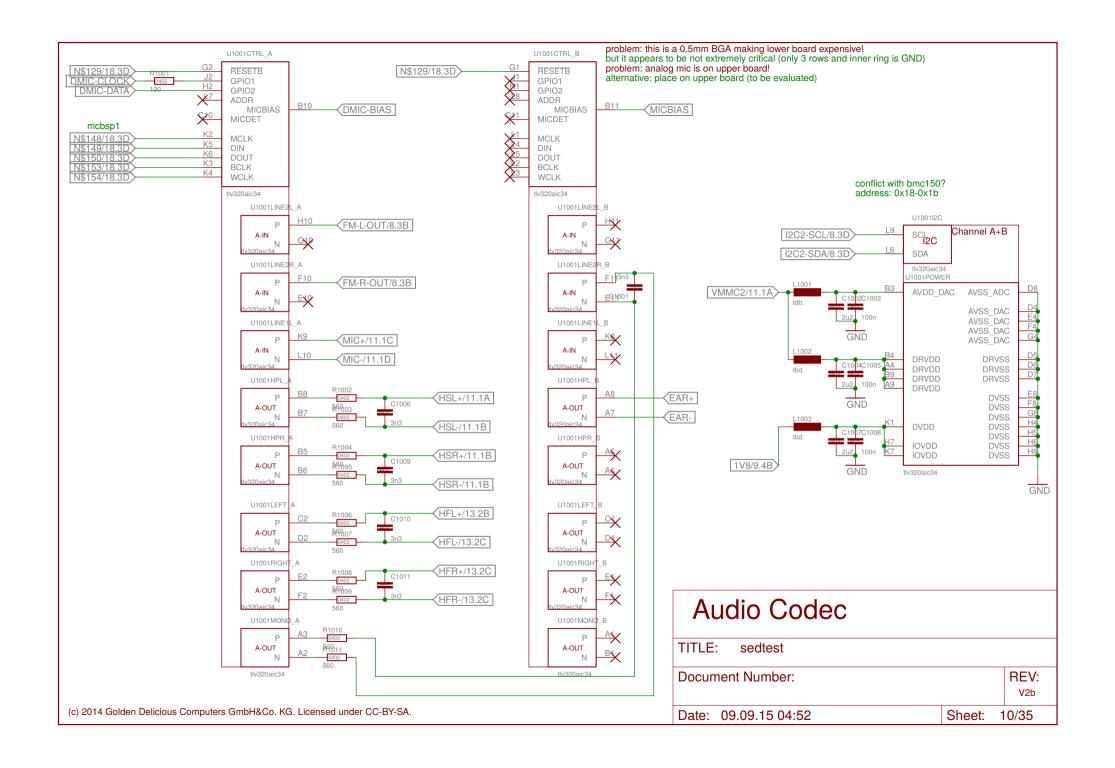


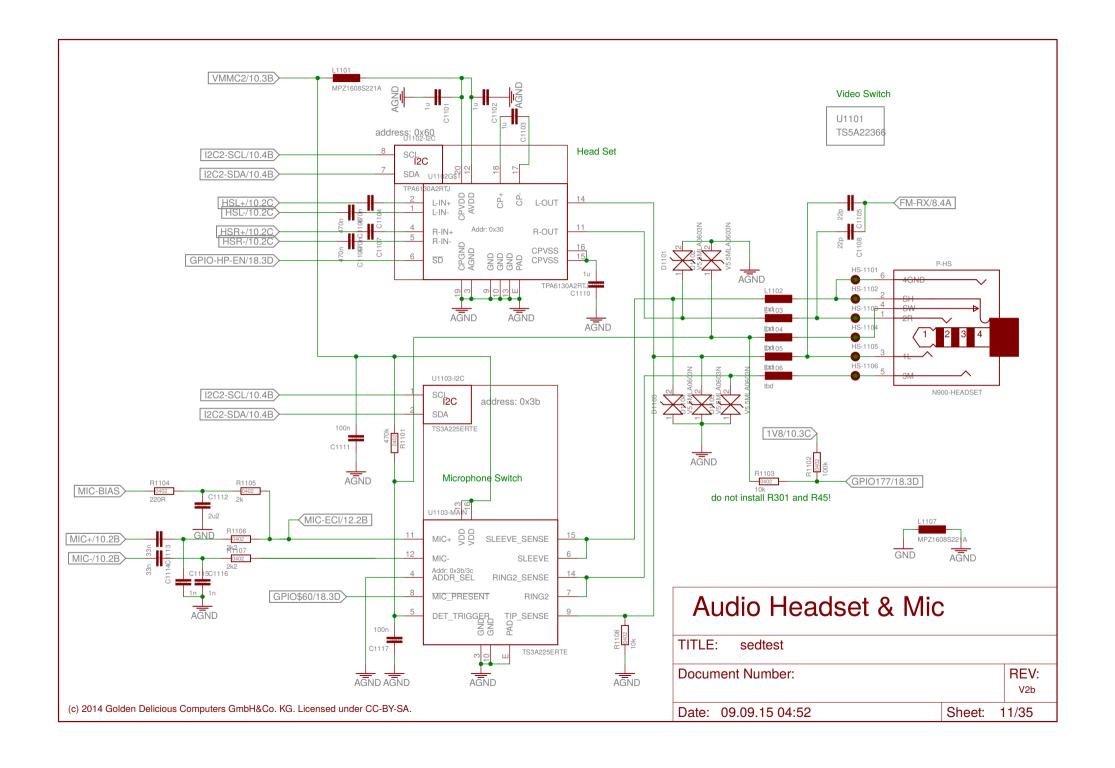


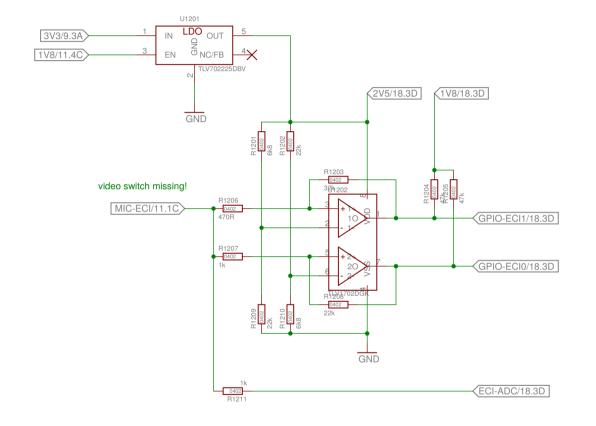
### WLAN & Bluetooth & FM

TITLE:	sedtest		
Documer	t Number:		REV: V2b

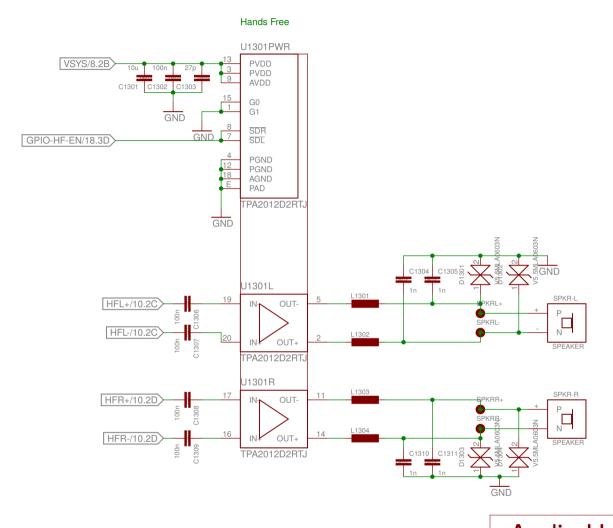




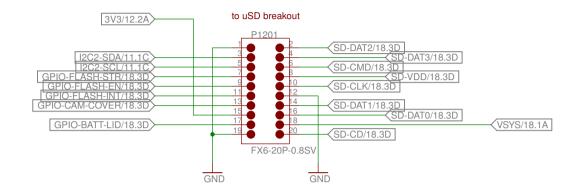


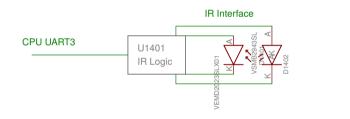


ECI		
TITLE: sedtest		
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## Audio Handsfree TITLE: sedtest Document Number: REV: V2b Date: 09.09.15 04:52 Sheet: 13/35



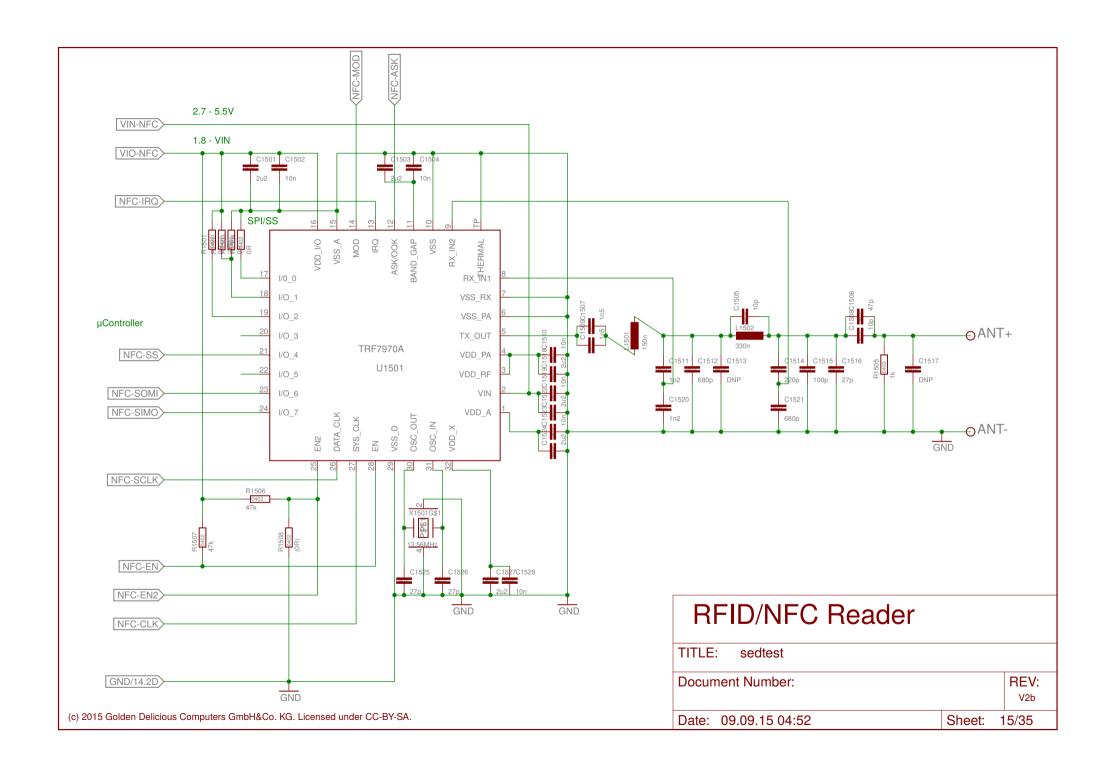


Hackerbus???



Misc (lower)	
TITLE: sedtest	
Document Number:	REV: V2b
Date: 09.09.15 04:52	Sheet: 14/35

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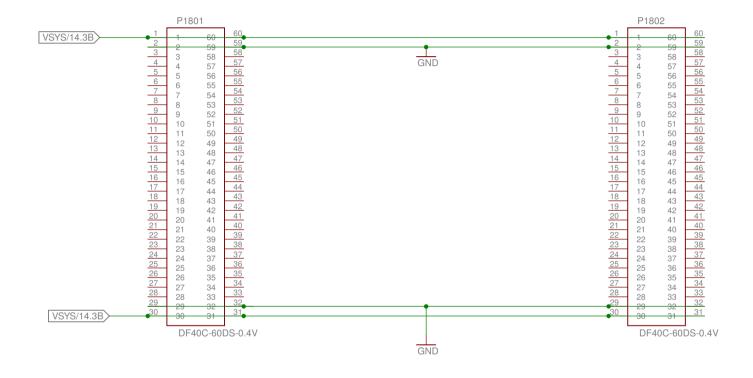


	Hackerbus	
	TIGONOIDUS	
	TITLE: sedtest	
	Document Number:	REV:
( ) 2045 O. H D. Italia - O Lan O H. 100 - 1/0   Land Land - CO. 2040		V2b
(c) 2015 Golden Delicious Computers GmbH&Co. KG. Licensed under CC-BY-SA.	Date: 09.09.15 04:52 Shee	et: 16/35

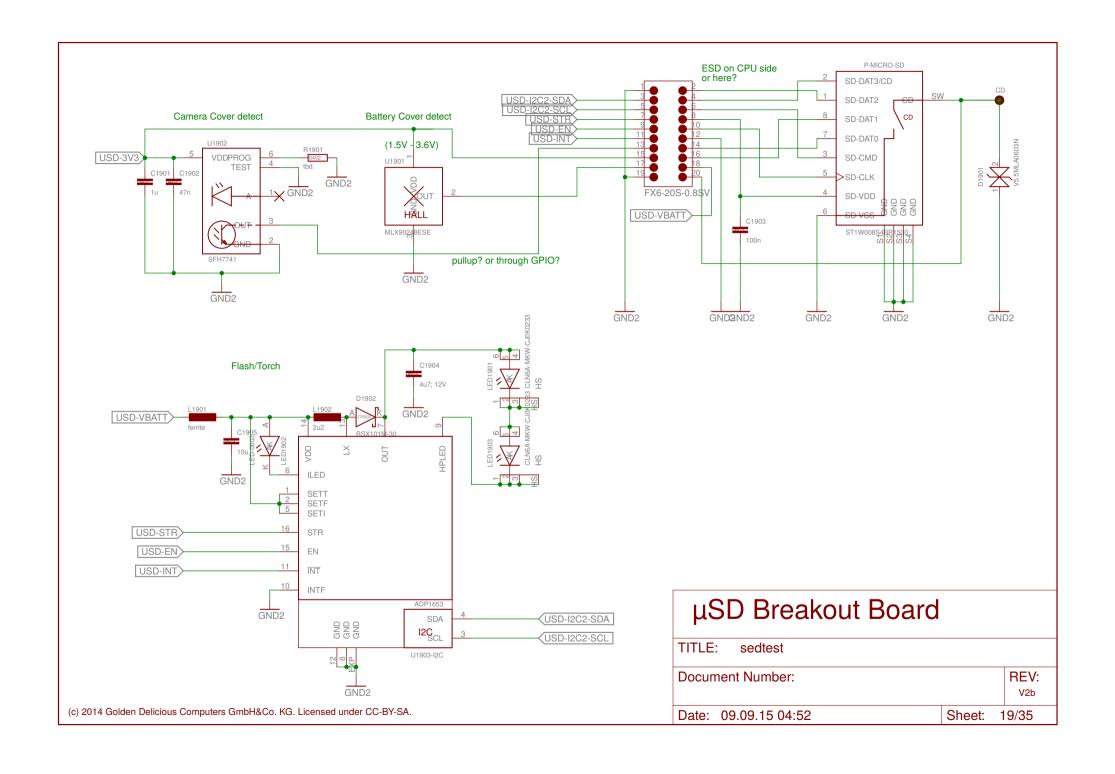
	Infrarad	
	Infrared	
	TITLE: sedtest	
	Document Number:	REV:
(c) 2015 Golden Delicious Computers GmbH&Co. KG. Licensed under CC-BY-SA.		V2b
(c) 2013 Guiden Delicious Computers Gillon&Co. No. Licensed under CC-BY-SA.	Date: 09.09.15 04:52 Shee	et: 17/35

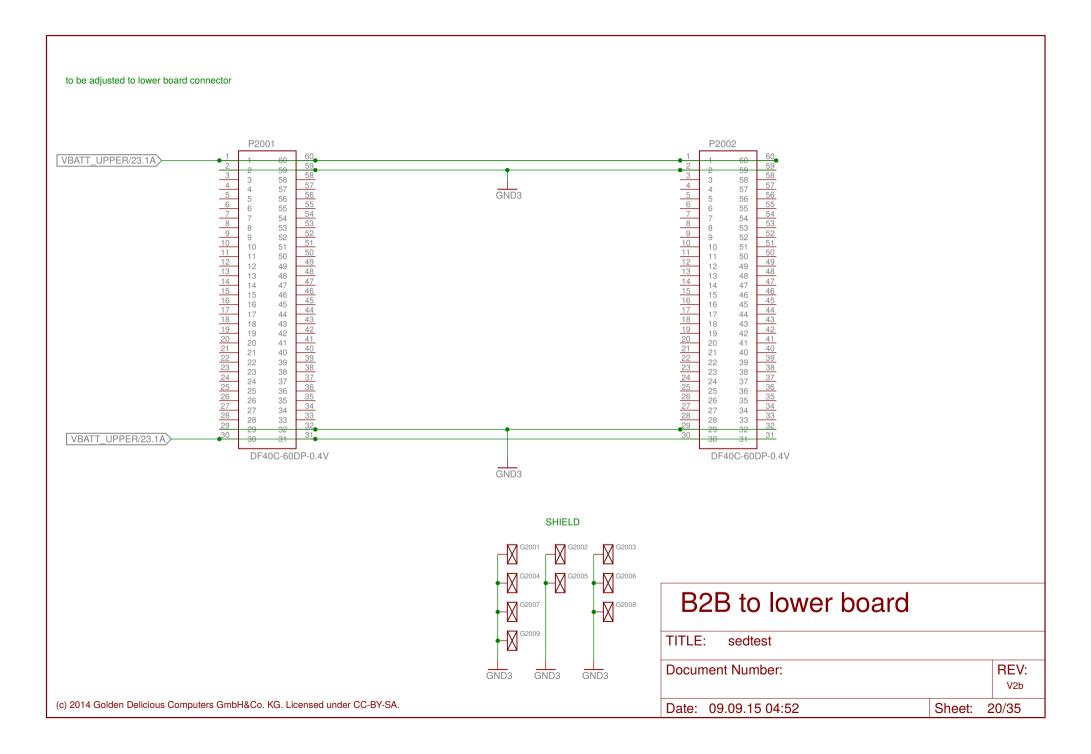
ca. 130 signals (to be counted exactly after definition	on of upper/lower split)
,	MMC3-DATA1/8.1A
	IVIIVICS-DATAT/O.TA
	MMC3-DATA2/8.1A
	MMC3-DATA3/8.1A
	GPIO-WLAN-IRQ/8.1A
	GPIO-BT-EN/8.1C
	UART1-RX/8.1C
	UART1-RTS/8.1C
	UARTI-RIS/8.1C
	UARTI-CIS/8.1D
	UART1-RTS/8.1C UART1-CTS/8.1D UART1-TX/8.1D
	MCBSP3-FCK/8.1D MCBSP3-CLK/8.1D
LOCK-GPIO/1.2A	MCBSP3-CLK/8.1D
POWERON/1.3A	MCBSP3-DR/8.1D
GPIO-VOL+/1.2B	MCBSP3-DX/8.1D
GPIO-VOL-/1.2B	SYSCLK/8.3C
CAM1-GPIO/1.3B	32KHZ/8.3D
CAM2-GPIO/1.3C	GPIO-FM-EN/8.3A
1000 CDA/0.40	GRIO-I WI-LIVO.SA
12C3-SDA9-4C 12C3-SCL/9-4C 12C3-SCL/9-4C 12C3-SCL/9-4C	GPIO-FMIRQ/8.3A
12C3-SCL/9.4C	MCBSP2-FCK/8.3A MCBSP2-CLK/8.3A
CHG_IND/3.1B_>	MCBSP2-CLK/8.3A
N\$131/3.1C>	MCBSP2-DR/8.3A>
N\$141/3.1C	MCBSP2-DX/8.3A
N\$143/3.1C	GPIN115/9.3B
BATTEMP/5.4A	GPIO56/9.3A
GPIO-EN-MODEM	GPIO\$51/9.3B
12C2-SDA/14.1A	<u>GPIO\$55/9.4A</u>
12C2-SCL/14.1A	GPIO\$56/9.4A
TNA23 <u>1-INT/4.4C</u>	ACCEL-INT/9.4C
HDQ/5.2A>	N\$129/10.2A
HDQ/5.2A GP/O\$70/7.3B	N\$129/10.2A N\$148/10.1A
GPI <u>O\$110/7.1D</u>	N\$149/10.1A
N\$19/7.2D	N\$150/10.1A
N\$229/7.3C	N\$153/10.1A
AD <u>C\$114/7.1C</u>	N\$154/10.1A
ADC1/7.4C	GPIO-ECI1/12.3B
ADC2/7.4C	GPIO-ECI0/12.3C
GPIO-COMPARATOR/7.4D	
	ECI-ADC/12.3C
MCBSP4-DR/6.2A	VMMC2/11.1A
IMCBSP4-DX/6.2A	GPIO-HP-EN/11.1B
MCBSP4-DX/6.2A MCBSP4-CLKX/6.2A MCBSP4-FSX/6.2A	GPIO\$60/11.2D
MCBSP4-FSX/6.2A	GPI0177/11.4C
UART?-RTS/6.2U	GPIO-HF-EN/13.1B ➤
_UART?-CTS/6.2C>	GPIO-FLASH-STR/14.1A
UART?-RX/6.2C>	GPIO-FLASH-EN/14.1A
UAR <u>T?-TX/6.2C</u> >	GPIO-FLASH-INT/14.1A
RING/6.2C	GPIO-BATT-LID/14.1B
GPIO-MODEM IGT/6.3A	SD-CMD/14.2A
GPIO-MODEM EMERG/6 3A	SD-CLK/14.2A
LEMERG OFF/6 3B	SD-CD/14.2B
I PWR IND/6 3R	SD-VDD/14.2A
EMERG OFF/6.3B PWR IND/6.3B	
CTATUC/C OD	SD-DAT0/14.2B
STATUS/6.3B	SD-DAT1/14.2B
3G-WOE/6.3B	SD-DAT2/14.2A
GPIO\$52/7.4A	SD-DAT3/14.2A
GPIO-WLAN-EN/8.1A	<u>VIB+/14.1D</u>
MMC3-CLK/8.1A	<u>  VIB-/14.1D</u> >
MMC3-CMD/8.1A	3V3/14.1A
MMC3-DATA0/8.1A	2V5/12.3B
	1V8/12.3B
	VBUS/3.1B>
	2V5/12.3B 1V8/12.3B VBUS/3.1B OTG-D-/3.1B
	OTG-D+/3.1B
	OTG-ID/2.2B
	VBUS-MODEM/6.3B
	USB-WWAN-D+/6.3B
	USB-WWAN-D-/6.3B
	2V7/7.4C
	GPIO-CAM-COVER/14.1B
	N\$38

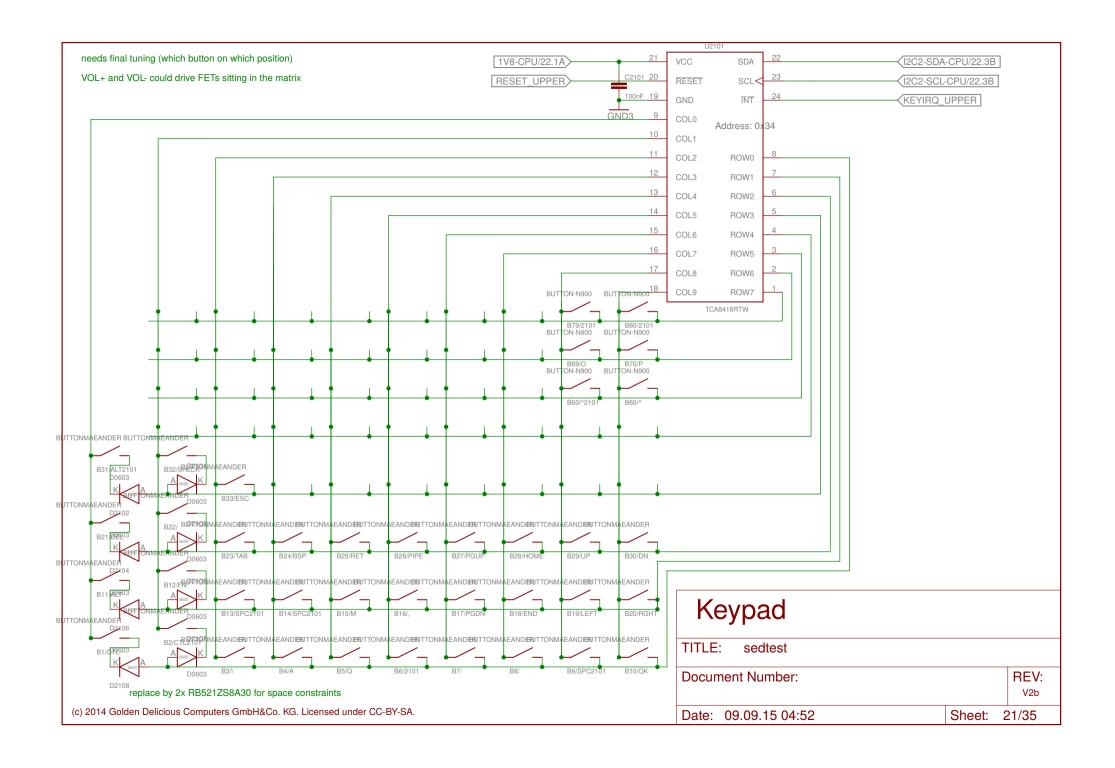
Pin assignment musr be optimized for final component placement we might have to switch to 80 or 100 pin connectors

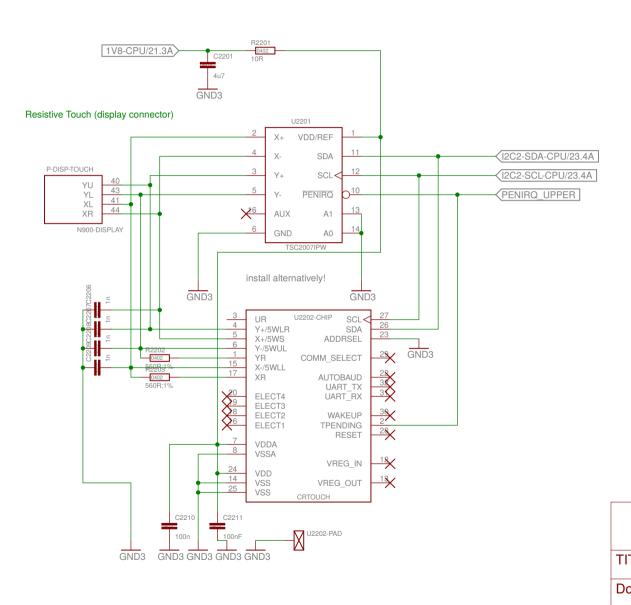


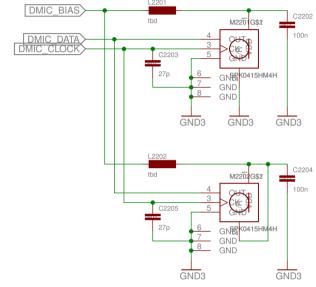
# B2B to upper board TITLE: sedtest Document Number: REV: V2b Date: 09.09.15 04:52 Sheet: 18/35











## Display-Peripherals

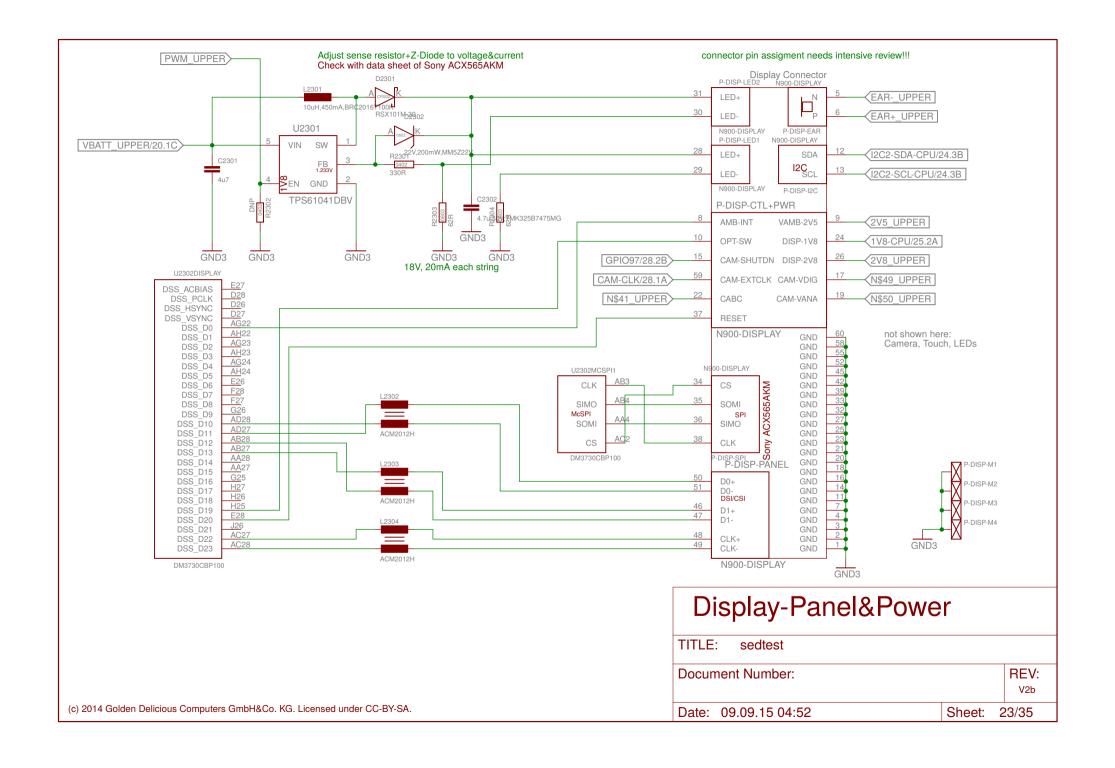
Date: 09.09.15 04:52

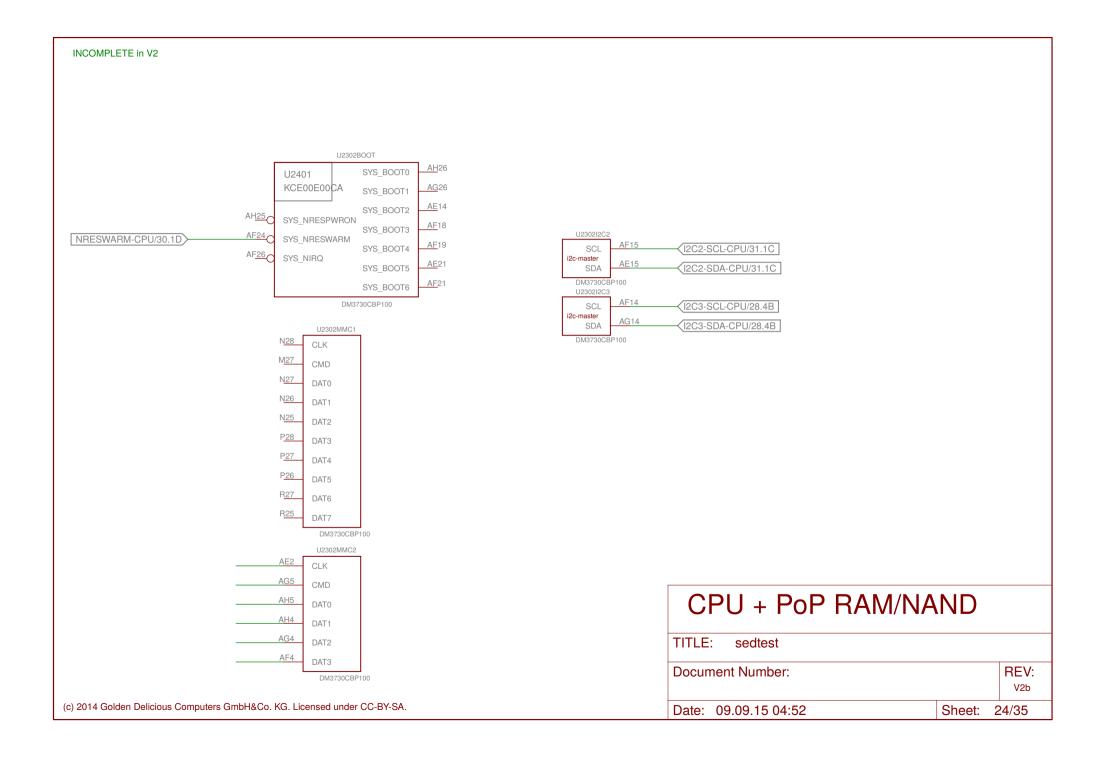
TITLE: sedtest

Document Number: REV: V2b

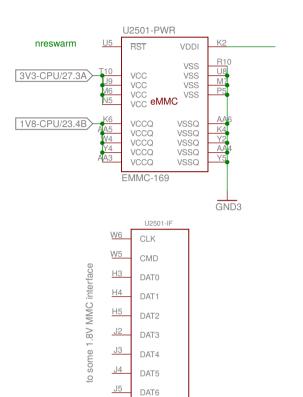
Sheet: 22/35

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#### INCOMPLETE in V2



DAT7 EMMC-169

## eMMC

TITLE: sedtest

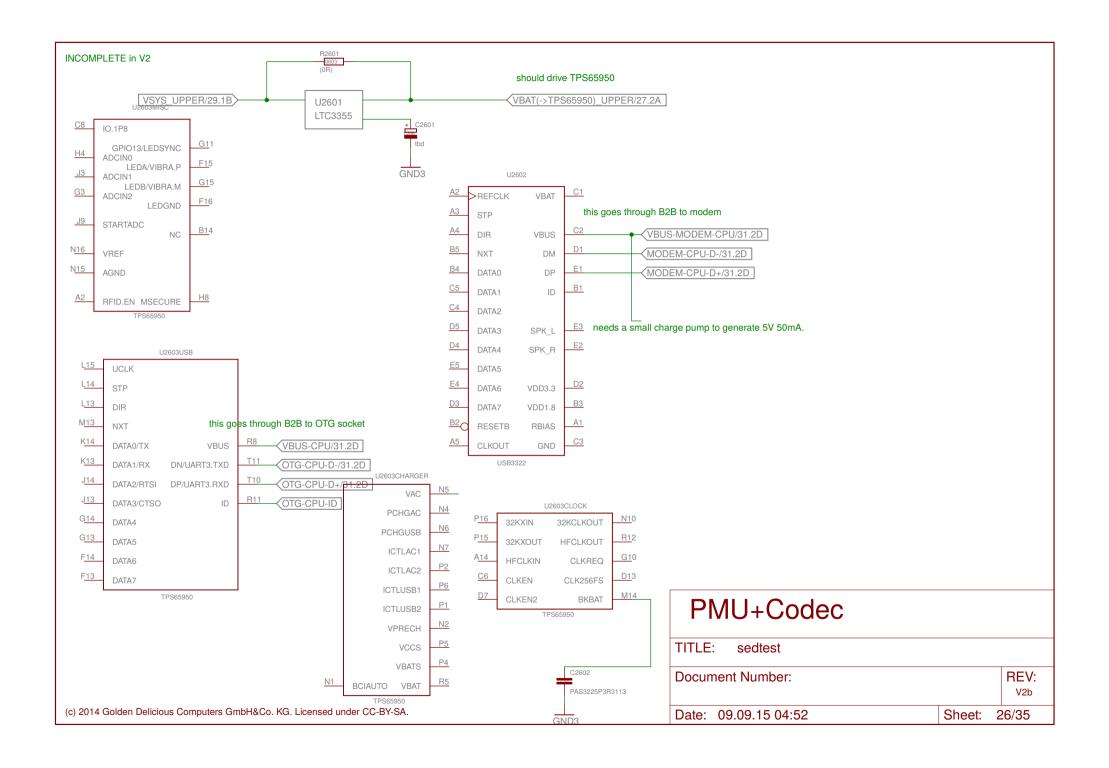
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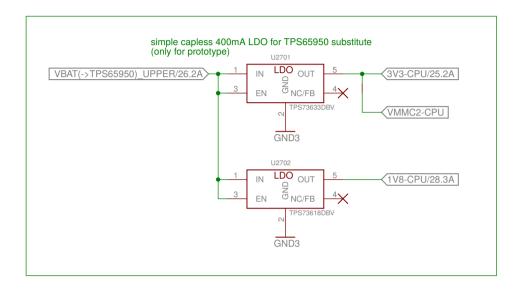
Date: 09.09.15 04:52

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REV:

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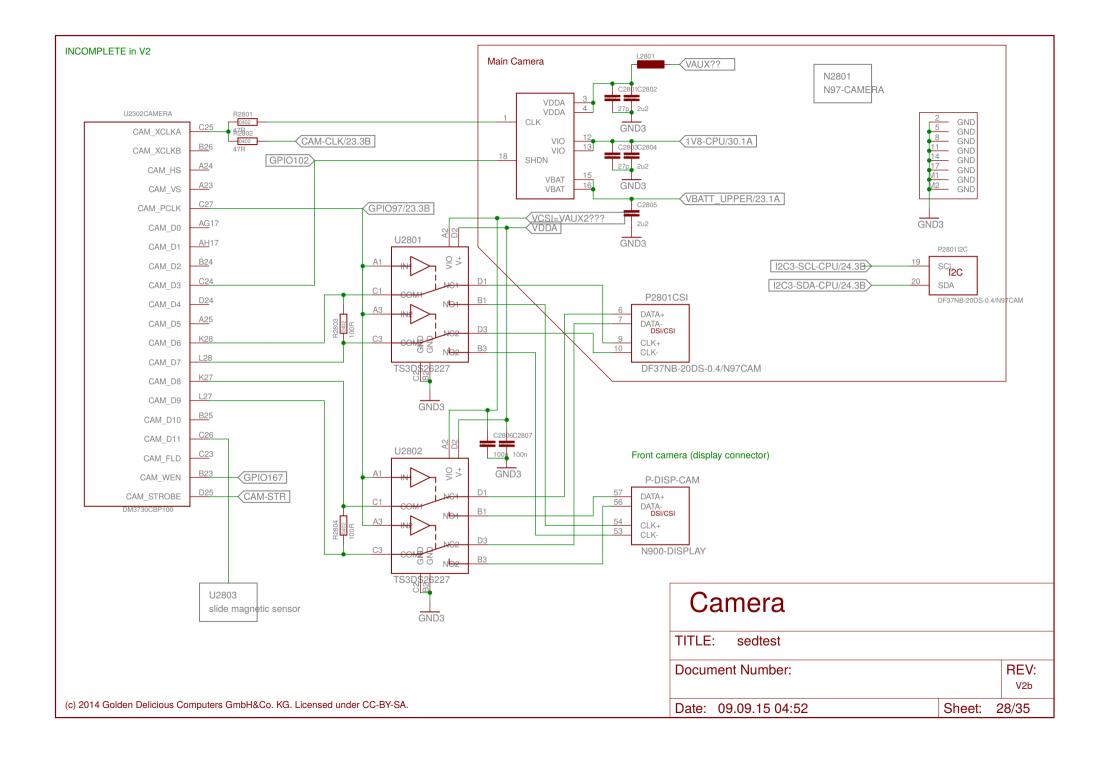


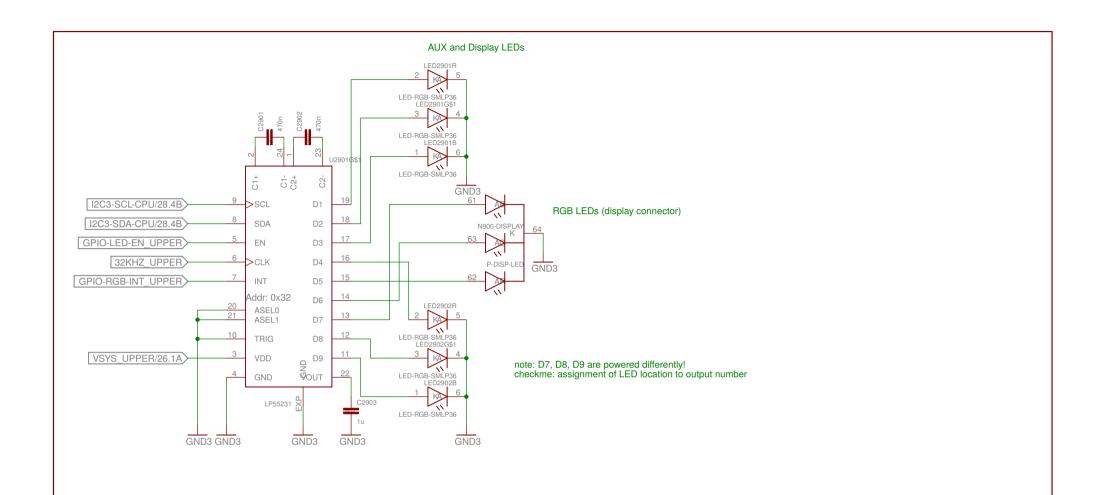
## BB-XM Dummy (TWL4030)

 TITLE:
 sedtest

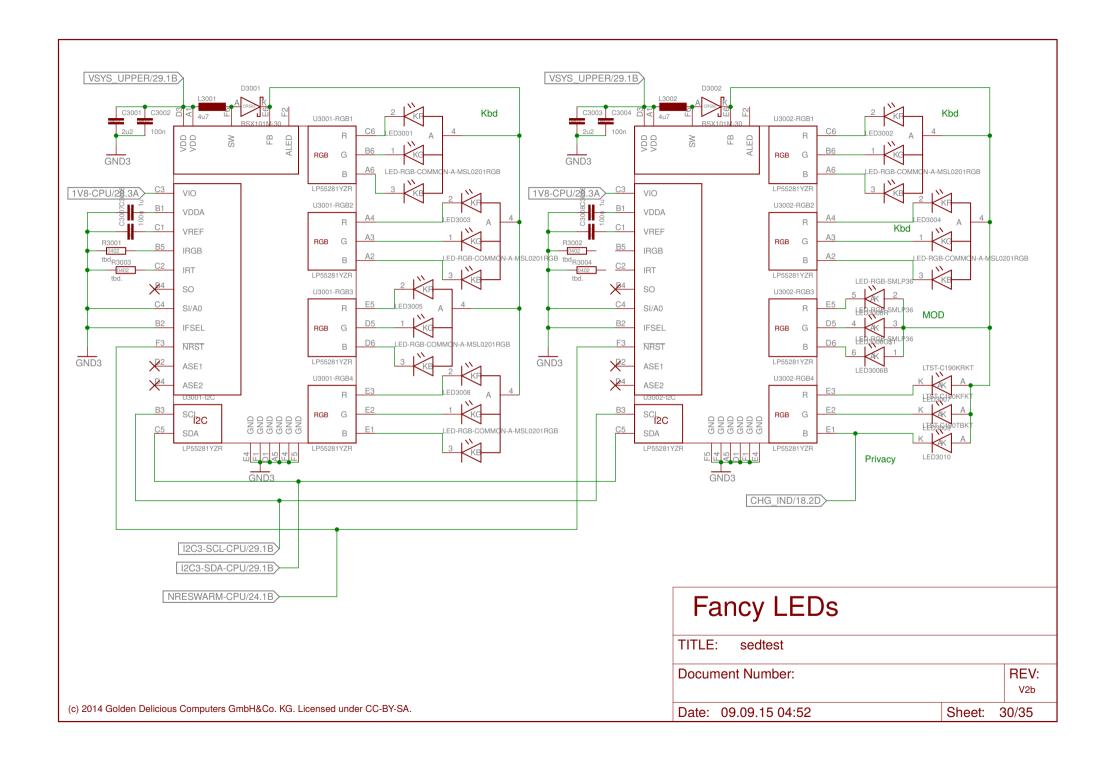
 Document Number:
 REV: V2b

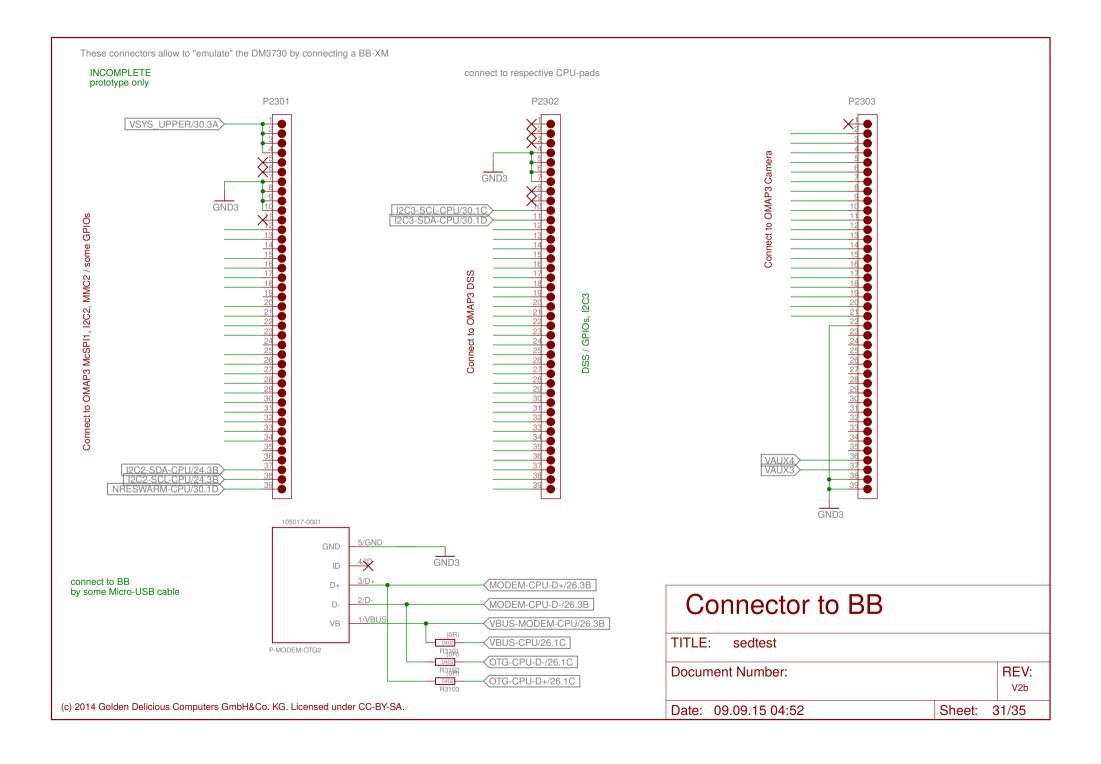
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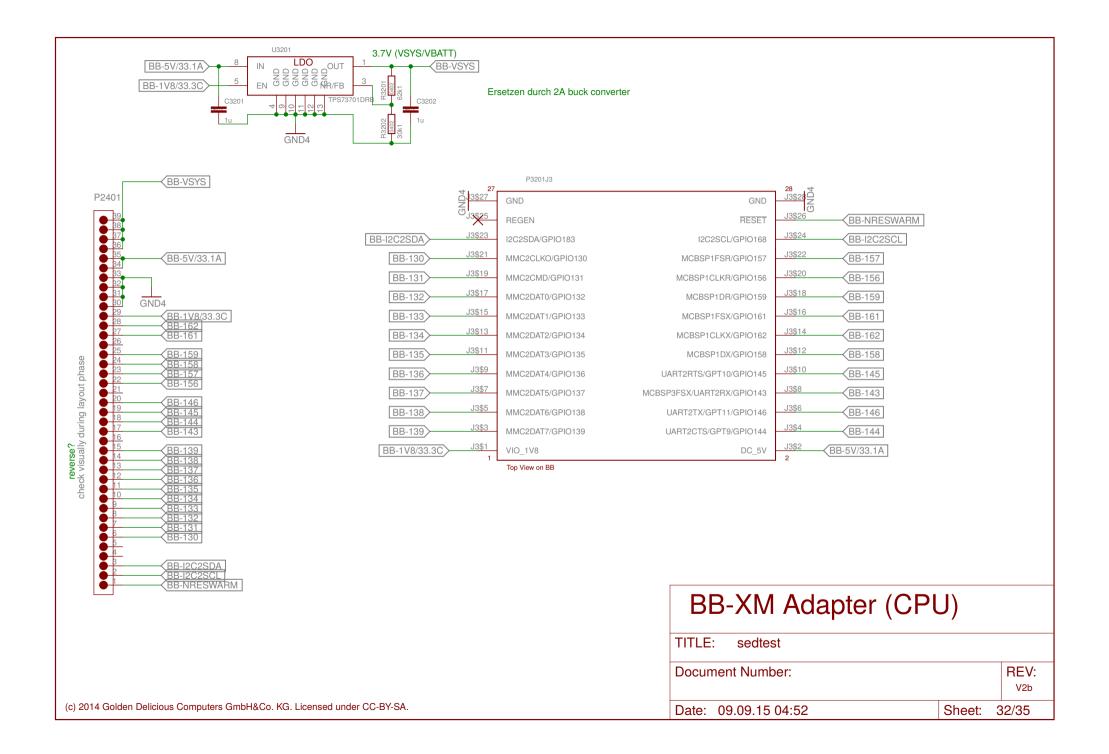


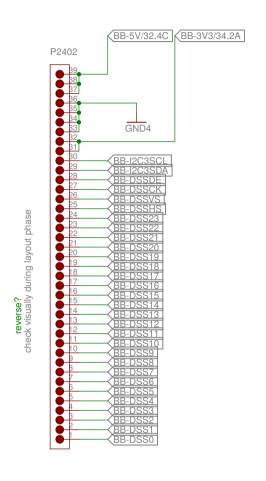


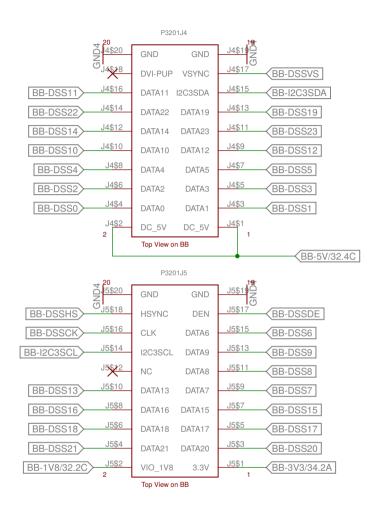
LE	Os		
TITLE:	sedtest		
Documen	t Number:		REV: V2b
Date: 09	.09.15 04:52	Sheet:	29/35











## BB-XM Adapter (DISP) TITLE: sedtest Document Number: REV: V2b Date: 09.09.15 04:52 Sheet: 33/35

