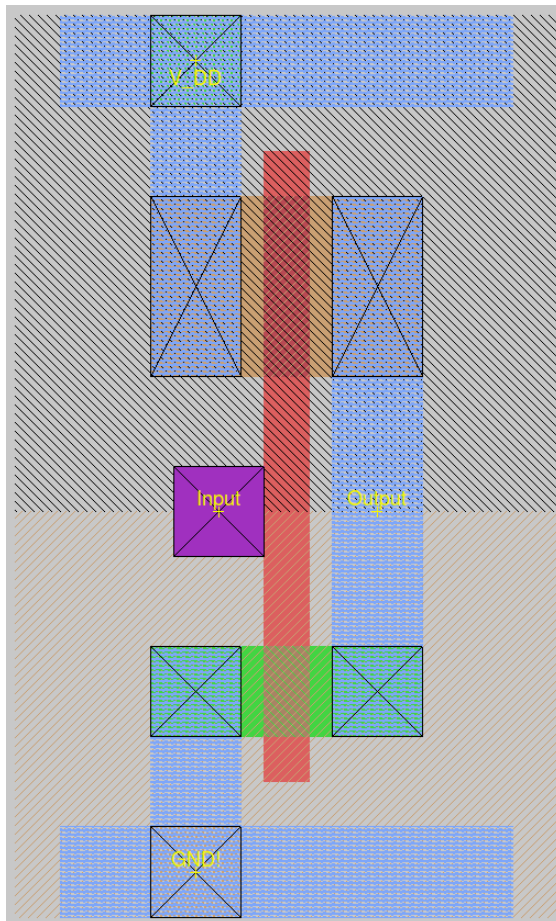


Digital Integrated Circuit

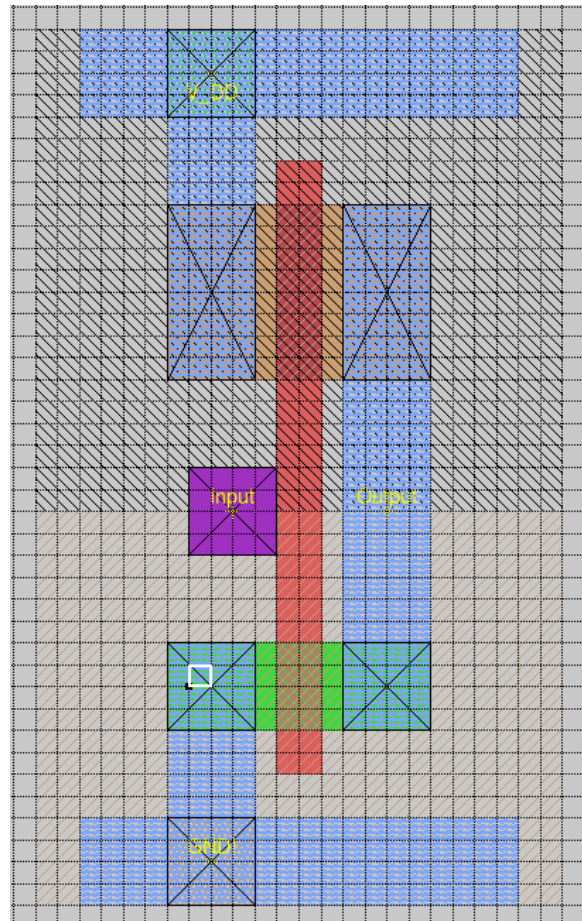
Fall semester, 2019

20165190 Han Seungcheol

1. Layout file (inverter.mag)



* Grid Off



* Grid On

2. Spice file (inverter.spice)

```
1 * SPICE3 file created from inverter.ext - technology: scmos
2
3 .option scale=1u
4
5 M1000 Output Input V_DD V_DD pfet w=8 l=2
6 + ad=0 pd=0 as=0 ps=0
7 M1001 Output Input GND GND nfet w=4 l=2
8 + ad=0 pd=0 as=0 ps=0
9 C0 Input GND 3.02fF
10 C1 V_DD Input 3.02fF
```