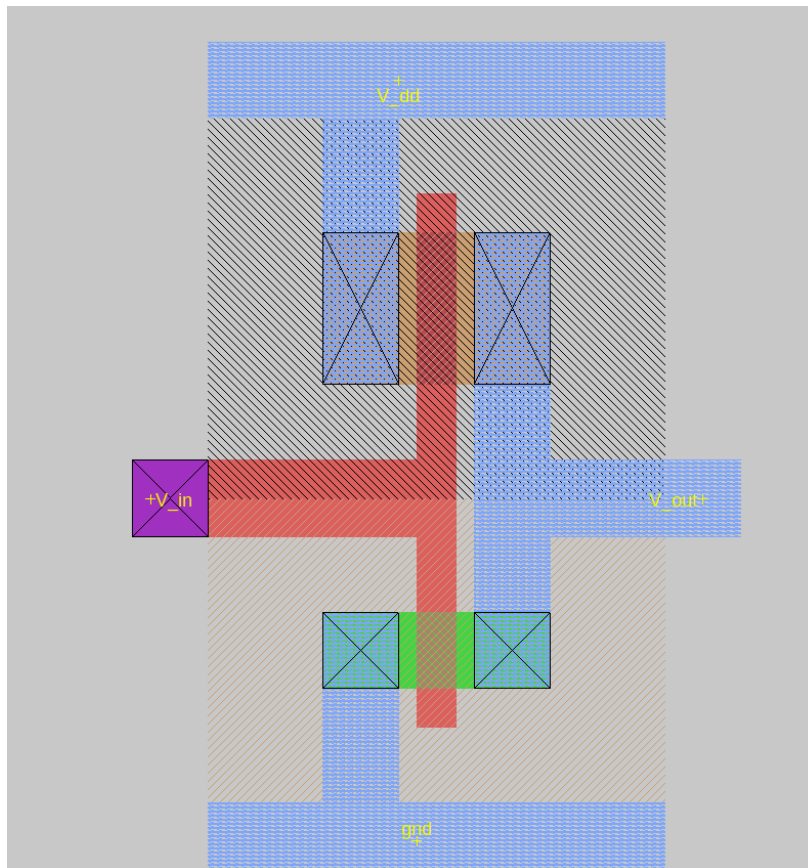


## Digital Integrated Circuit HW #1

2019.09.30

20175138 임경록

### 1. Layout of an inverter



### 2. Spice File

```
File Edit View Search Terminal Help
* SPICE3 file created from hw1.ext - technology: scmos

.option scale=1u

M1000 V_out V_in V_dd w_n10_8# pfet w=8 l=2
+ ad=0 pd=0 as=0 ps=0
M1001 V_out V_in gnd w_n10_8# nfet w=4 l=2
+ ad=0 pd=0 as=0 ps=0
C0 w_n10_8# V_in 3.96fF
C1 w_n10_8# V_in 3.96fF
C2 gnd 0 2.26fF
C3 V_dd 0 2.26fF

~
~
```