Version List

Version	Change List	Date	Author
1.0.0	First Release	2020-02-25	Mu.Chou Marlon.Zhang
4.0	BT_UART2_CTS/BT_UART2_RTS线序反	2020-02-25	Miller.zhang

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Title	01_Cover	
Size: A	Document Number: SG2000_DDR3	Rev: 1.0.0
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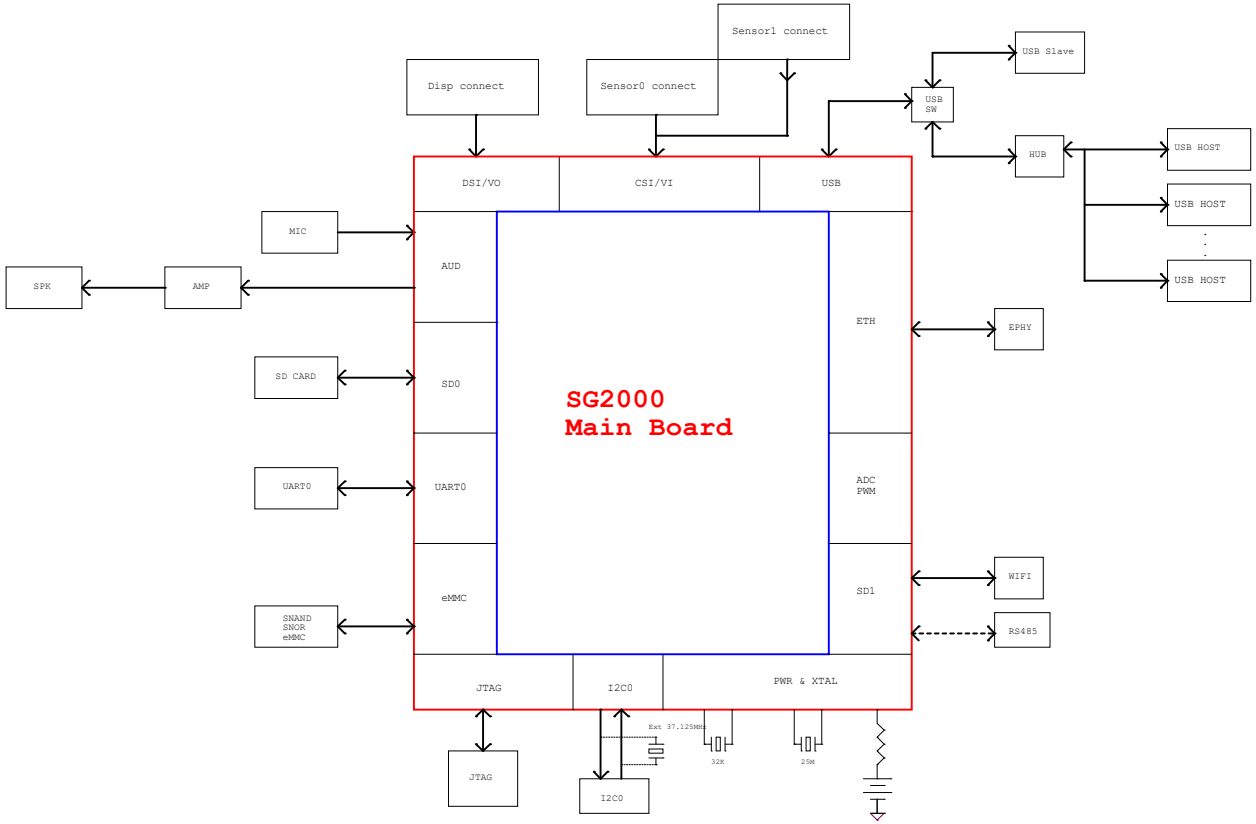
Block Diagram for SG2000



			Plug in Power			Battery		
			Ext RTC	SOC RTC QFN not support	32Kless	Ext RTC	SOC RTC QFN not support	32Kless
0.9V	RB1	SEQ1				V	V	V
	RB12	always on	V	V	V			
1.8V	RB2	SEQ1				V	V	V
	RB13	always on	V	V	V			
VDDQ	RB4	SEQ1				V	V	V
	RB15		V	V	V			
3.3V	RB3	SEQ2	V	V	V	V	V	V
	RB14	always on soc 3.3V switch that need next power sequence						
VDDIO_RTC	RB6	USE MAIN +1.8V	V	V				
VDDIO_RTC	RB7	USE V18AO				V	V	
VDDIO_RTC	RB9	USE V18RTC			V			V
VDDBKUP	RB8	USE V18AO						
VDDBKUP	RB10	USE V18RTC	V		V	V		V
VDDBKUP	RB11	USE VBACKUP		V			V	
VBACKUP	DB1	fource V33AO				V	V	V
VBACKUP	DB2	fource MAIN_+3.3V	V	V	V			
VAO18		WIFI or Other AO Can Share				V	V	V
VAO33		WIFI or Other AO Can Share				V	V	V
V18RTC_LDO					V			V
Ex1 RTC			V			V		
SOC_RTC				V			V	

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Title: 02_Diagram

Size: D | Document Number: SG2000_DDR3 | Rev: 0.0

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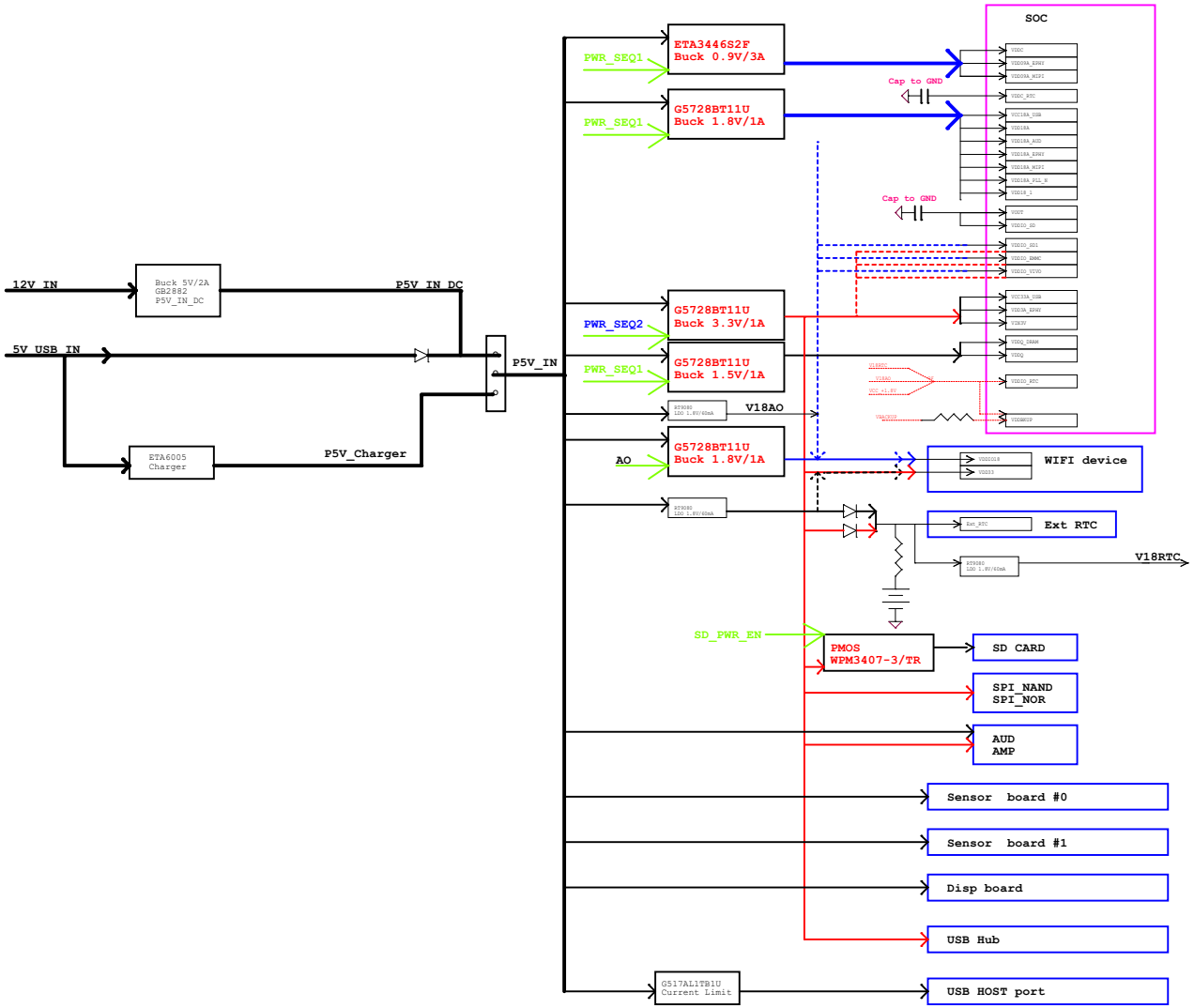

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Title	03_Power tree	
Size: D	Document Number: SG2000_DDR3	Rev: 0.0
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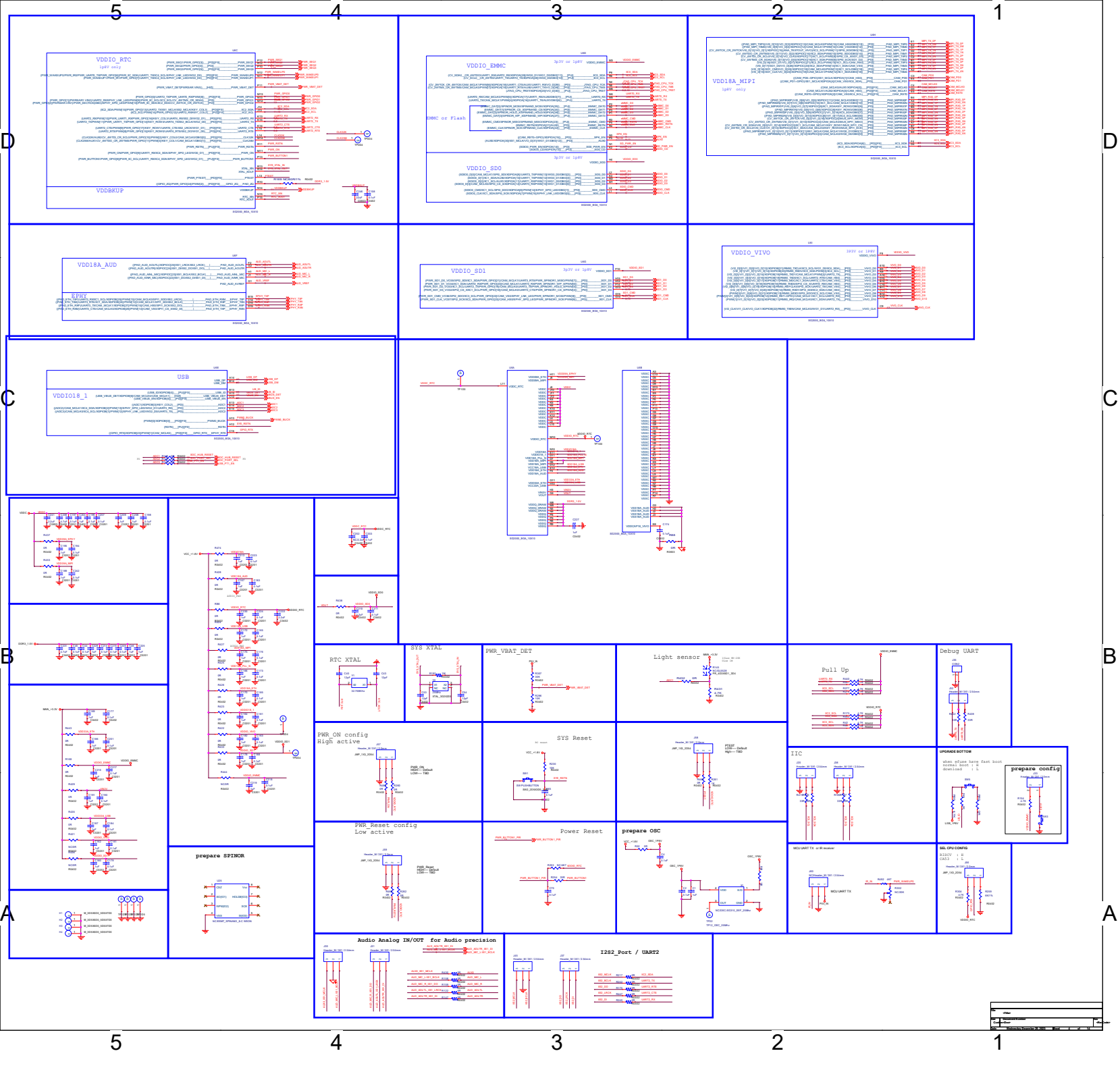
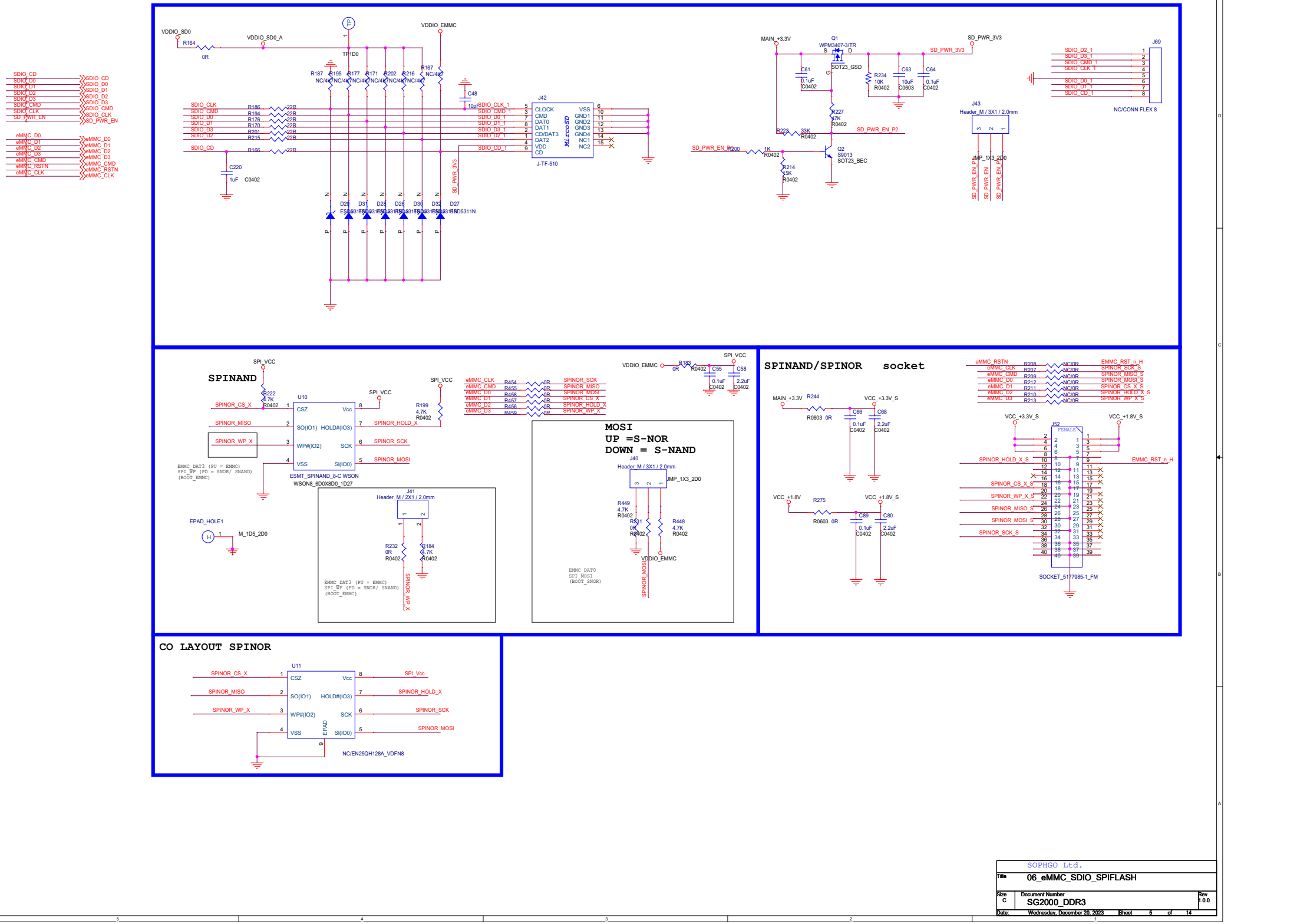

VDDIO_RTC
1p8V only
VDDBKUP
VDDIO_EMMC
3p3V or 1p8V
eMMC or Flash
VDDIO_SD0
VDD18A_MIPI
1p8V only
VDD18A_AUD
EPHY
VDDIO_SD1
VDDIO_VIVO
USB
VDDIO18_1
RTC XTAL
SYS XTAL
PWR_VBAT_DET
Light sensor
Pull Up
Debug UART
PWR_ON config
High active
SYS Reset
IIC
prepare config
PWR_Reset config
Low active
Power Reset
prepare OSC
prepare SPINOR
Audio Analog IN/OUT for Audio precision
I2S2_Port / UART2


SDIO_CD
SDIO_D0
SDIO_D1
SDIO_D2
SDIO_D3
SDIO_CMD
SDIO_CLK
SD_PWR_EN
eMMC_D0
eMMC_D1
eMMC_D2
eMMC_D3
eMMC_CMD
eMMC_RSTN
eMMC_CLK
VDDIO_SD0
R164
0R
VDDIO_SD0_A
TP1D0
VDDIO_EMMC
R167
NC/4K7
R187 R195 R177 R171 R202 R216
NC/4K7
C48
10pF
SDIO_CLK
SDIO_CMD
SDIO_D0
SDIO_D1
SDIO_D3
SDIO_D2
R186 R194 R176 R170 R201 R215
22R
SDIO_CD
R166
C220
1uF C0402
J42
CLOCK CMD DAT0 DAT1 CD/DAT3 DAT2 VDD CD
VSS GND1 GND2 GND3 GND4 NC1 NC2
MicroSD
J-TF-510
SDIO_CLK_1
SDIO_CMD_1
SDIO_D0_1
SDIO_D1_1
SDIO_D3_1
SDIO_D2_1
SDIO_CD_1
SD_PWR_3V3
D29 D31 D28 D26 D30 D32 D27
ESD5311N
MAIN_+3.3V
Q1
WPM3407-3/TR
SOT23_GSD
SD_PWR_3V3
C61
0.1uF C0402
R234
10K R0402
C63
10uF C0603
C64
0.1uF C0402
R227
47K R0402
R223
33K R0402
SD_PWR_EN_P2
SD_PWR_EN_P1
R200
1K R0402
Q2
S9013
SOT23_BEC
R214
15K R0402
J43
Header_M / 3X1 / 2.0mm
JMP_1X3_2D0
SD_PWR_EN_P1
SD_PWR_EN
SD_PWR_EN_P2
J69
SDIO_D2_1
SDIO_D3_1
SDIO_CMD_1
SDIO_CLK_1
SDIO_D0_1
SDIO_D1_1
SDIO_CD_1
NC/CONN FLEX 8
SPINAND
SPI_VCC
R222
4.7K R0402
U10
SPINOR_CS_X
SPINOR_MISO
SPINOR_WP_X
CSZ
SO(IO1)
WP#(IO2)
VSS
Vcc
HOLD#(IO3)
SCK
SI(IO0)
SPINOR_HOLD_X
SPINOR_SCK
SPINOR_MOSI
R199
4.7K R0402
EMMC_DAT3 (PU = EMMC)
SPI_WP (PD = SNOR/ SNAND)
(BOOT_EMMC)
ESMT_SPINAND_8-C WSON
WSON8_6D0X8D0_1D27
EPAD_HOLE1
M_1D5_2D0
J41
Header_M / 2X1 / 2.0mm
R232
0R R0402
R184
4.7K R0402
SPINOR_WP_X
eMMC_CLK
eMMC_CMD
eMMC_D0
eMMC_D1
eMMC_D2
eMMC_D3
R454 R455 R458 R457 R456 R459
0R
SPINOR_SCK
SPINOR_MISO
SPINOR_MOSI
SPINOR_CS_X
SPINOR_HOLD_X
SPINOR_WP_X
VDDIO_EMMC
R193
0R R0402
C55
0.1uF C0402
C58
2.2uF C0402
MOSI
UP =S-NOR
DOWN = S-NAND
J40
Header_M / 3X1 / 2.0mm
JMP_1X3_2D0
R449
4.7K R0402
R531
0R R0402
R448
4.7K R0402
EMMC_DAT0
SPI_MOSI
(BOOT_SNOR)
SPINOR_MOSI
SPINAND/SPINOR socket
MAIN_+3.3V
R244
R0603 0R
VCC_+3.3V_S
C66
0.1uF C0402
C68
2.2uF C0402
VCC_+1.8V
R275
R0603 0R
VCC_+1.8V_S
C89
0.1uF C0402
C80
2.2uF C0402
eMMC_RSTN
R208
eMMC_CLK
R207
eMMC_CMD
R209
eMMC_D0
R212
eMMC_D1
R211
eMMC_D2
R210
eMMC_D3
R213
NC/0R
EMMC_RST_n_H
SPINOR_SCK_S
SPINOR_MISO_S
SPINOR_MOSI_S
SPINOR_CS_X_S
SPINOR_HOLD_X_S
SPINOR_WP_X_S
VCC_+3.3V_S
VCC_+1.8V_S
J52
FEMALE
SPINOR_HOLD_X_S
EMMC_RST_n_H
SPINOR_CS_X_S
SPINOR_WP_X_S
SPINOR_MISO_S
SPINOR_MOSI_S
SPINOR_SCK_S
SOCKET_5177985-1_FM
CO LAYOUT SPINOR
U11
SPINOR_CS_X
SPINOR_MISO
SPINOR_WP_X
CSZ
SO(IO1)
WP#(IO2)
VSS
Vcc
HOLD#(IO3)
SCK
SI(IO0)
EPAD
SPI_Vcc
SPINOR_HOLD_X
SPINOR_SCK
SPINOR_MOSI
NC/EN25QH128A_VDFN8
SOPHGO Ltd.
Title
06_eMMC_SDIO_SPIFLASH
Size C
Document Number
SG2000_DDR3
Rev 1.0.0
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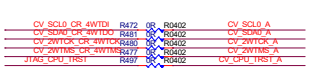
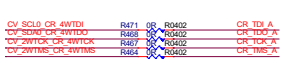
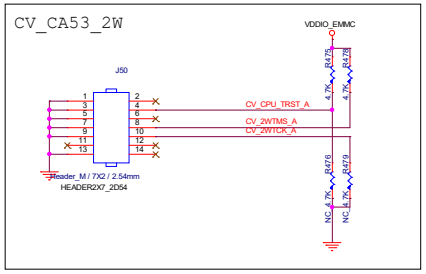
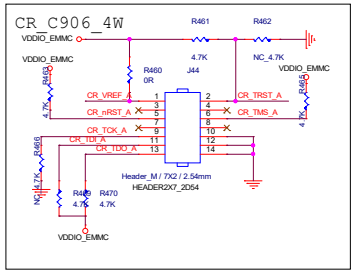
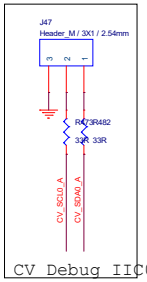
JTAG_0



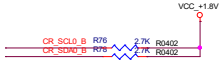


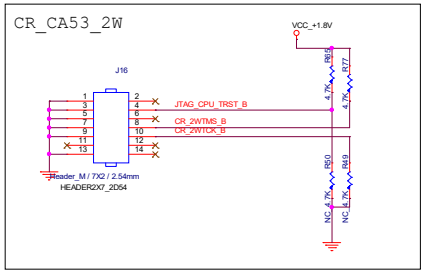


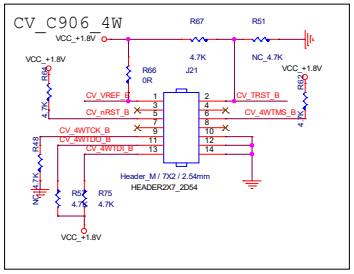


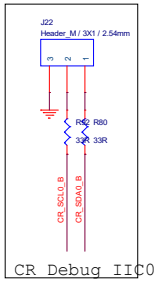


JTAG_1

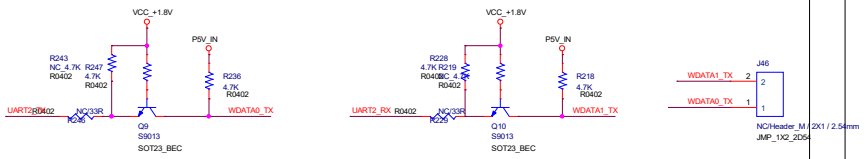




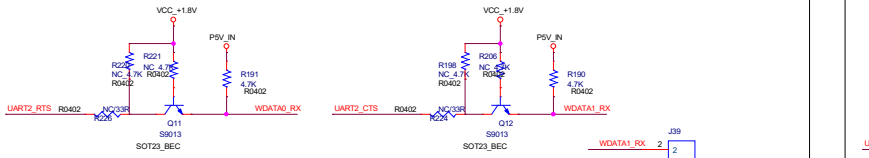


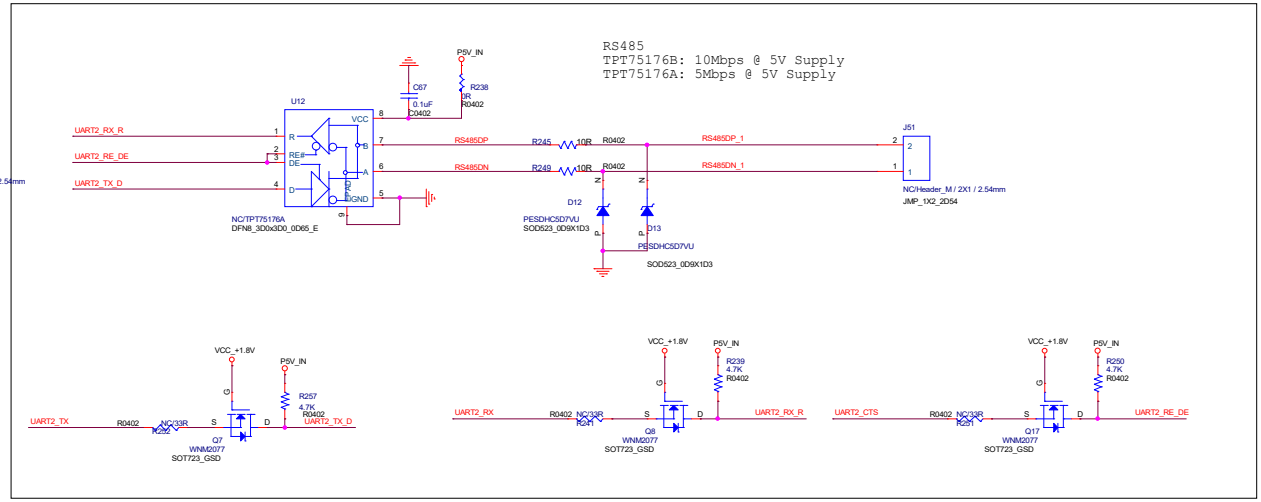


Wiegand TX



Wiegand RX





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Title	07_I2C_UART_PWM_JTAG	
Size: Custom	Document Number: SG2000_DDR3	Rev: 1.0.0
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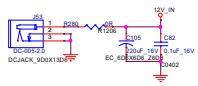
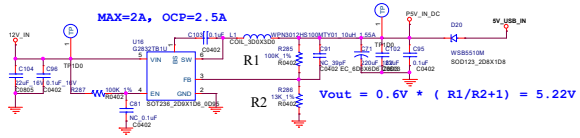
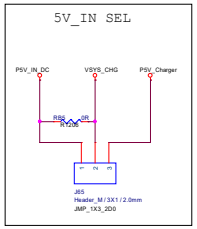
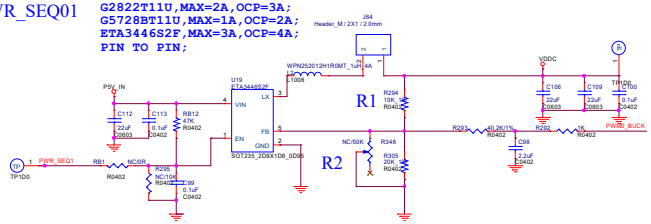






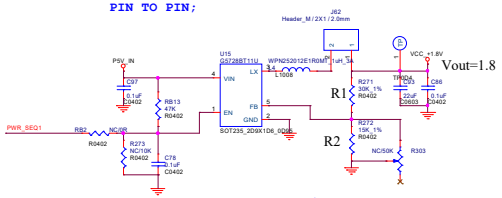
PWR_SEQ01

G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;



R2 >20k, for stability consideration.

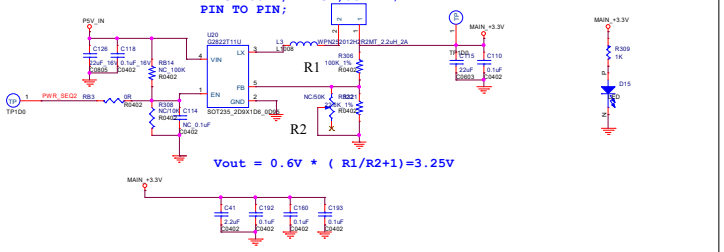
G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;



P5V_IN-->PWR_SEQ01-->PWR_SEQ02-->PWR_SEQ03

PWR_SEQ02

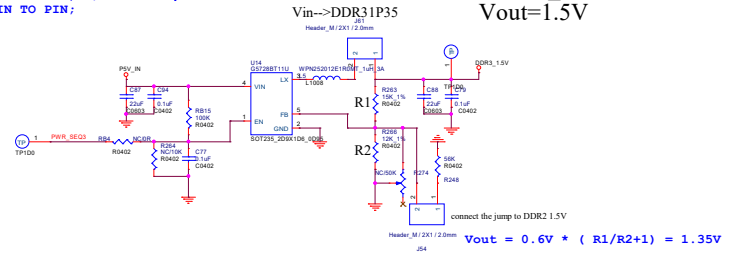
G2822T11U,MAX=2A,OCP=3A;
G5728BT11U,MAX=1A,OCP=2A;
ETA3446S2F,MAX=3A,OCP=4A;
PIN TO PIN;



PWR_SEQ03

G2822T11U,MAX=2A, OCP=3A;
G5728BT11U,MAX=1A, OCP=2A;
ETA3446S2F,3A, OCP=4.5A;
PIN TO PIN;

Note
VDDQ_POWER
Vout=1.5V



Title	04_SYS_Power	
Size D	Document Number SG2000_DDR3	Rev 0.0
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NET

PWR_WAKEUP0 PWR_WAKEUP1 JTAG_CPU_TCK JTAG_CPU_TMS JTAG_CPU_TRST UART2_RX UART2_TX UART2_CTS UART2_RTS ADC1 ADC2 ADC3 SD1_D0 SD1_D1 SD1_D2 SD1_D3 SD1_CMD SD1_CLK EPHY_TXP EPHY_TXN EPHY_RXP EPHY_RXN RTC_CLK

RTC_CLK R174 0R R0402 WIFI_RTC_CLK
JTAG_CPU_TCK R323 33R R0402 CHIP_EN
PWR_WAKEUP1 R326 33R R0402 WIFI_WAKE
SD1_D0 R295 33R R0402 WIFI_SD_D0
SD1_D1 R296 33R R0402 WIFI_SD_D1
SD1_D2 R316 33R R0402 WIFI_SD_D2
SD1_D3 R317 33R R0402 WIFI_SD_D3
SD1_CMD R298 33R R0402 WIFI_SD_CMD
SD1_CLK R299 33R R0402 WIFI_SD_CLK

SD1_CMD R157 0R R0402 EPHY_LNK_LED
SD1_CLK R162 0R R0402 EPHY_LNK_SPD

WIFI

Vout = 0.6V * (R1/R2+1)=1.8V

Vin-->WIFI_1P8V

J68 Header_M / 3X1 / 2.54mm — V33AO WIFI_3P3V — WIFI_VBAT
J49 Header_M / 3X1 / 2.54mm — WIFI_VDDIO — SDIO_VSEL
MAIN_+3.3VWIFI_3P3V — R338 0R
WIFI_1P8V — R336 0R — WIFI_VDDIO — C244 2.2uF — C245 0.1uF — TP42 NC_60MIL
WIFI_VBAT — C131 22uF C0603 — C141 0.1uF C0402 — TP54 NC_60MIL
P5V_IN — C146 22uF_16V C0805 — C144 0.1uF_16V C0402
U24 G2622T11U SOT235_2D9X1D6_0D95 — VIN LX FB EN GND
L7 L1008 WPN252012H2R2MT_2.2uH_2A
R342 47K R0402 — R336 100K_1% R0402 — R335 50K_1% R0402 — R1 — R2
C130 NC_0.1uF C0402
TP53 NC_60MIL — WIFI_1P8V — C137 22uF C0603 — C138 0.1uF C0402

VDDIO_SD1 — R281 0R — C85 2.2uF C0402
R318 4K7 — R319 4K7 — R300 4K7 — R301 4K7 — R29 4K7 — R291 4K7
Vout=1.8
WIFI_SD_D2 WIFI_SD_D3 WIFI_SD_CMD WIFI_SD_CLK WIFI_SD_D0 WIFI_SD_D1

U18A — WIFI_ANT — CHIP_EN — WIFI_WAKE — WL_BT_ANT — WIFI_REG_ON_A — WL_HOST_WAKE — GND
SDD2 SDD3 SDCMD SDCLK SDD0 SDD1 — WIFI_SD_D2 WIFI_SD_D3 WIFI_SD_CMD WIFI_SD_CLK WIFI_SD_D0 WIFI_SD_D1 — SD_D2 SD_D3 SD_CMD SD_CLK SD_D0 SD_D1
NC/WIFI_DXS-S1026

U18B — WL_XTAL_IN — WL_XTAL_OUT — XTAL_IN XTAL_OUT — WIFI_VBAT — VIN_LDO_OUT — VDDIO — VIN_LDO — WIFI_RTC_CLK — LPO_CLK_OUT — SDIO_VSEL — VDD_TCXO
TP_PCM_OUT TP_PCM_CLK TP_PCM_IN TP_PCM_SYNC — NC_FM_RX NC_N_WAKE NC_TCXO_IN NC_GPS_RF NC_I2C_SDA NC_I2C_SCL NC_REG_PU NC_TX2 NC_TX1
TP39 TP40 TP41 TP_PCM_SYNC TP_PCM_IN TP_PCM_CLK TP_PCM_OUT — NC_60MIL
NC/WIFI_DXS-S1026

UART2_RX R465 NC/0R BT_UART2_RX
UART2_TX R258 NC/0R BT_UART2_TX
UART2_CTS R233 NC/0R BT_UART2_CTS
UART2_RTS R225 NC/0R BT_UART2_RTS
JTAG_CPU_TRST R466 NC/0R BT_WAKE
PWR_WAKEUP0 R468 NC/0R BT_HOST_WAKE
JTAG_CPU_TMS R502 NC/0R BT_CLK_REF — R501 100K

U18C — BT_WAKE — BT_HOST_WAKE — BT_CLK_REF — BT_VIO/NC_HOST_WAKE — BT_RST_N — BT_UART2_CTS — BT_UART2_RX — BT_UART2_TX — BT_UART2_RTS — UART_RTS_N — UART_TXD — UART_RXD — UART_CTS_N
NC/WIFI_DXS-S1026

WL XTAL
WL_XTAL_OUT — R343 1M R0402 — WL_XTAL_IN
Y5 X1 X2 NC NC — 37.4MHz — XTAL_3D2X2D5
C139 12pF C0402 — C140 12pF C0402

U26 GND2 GND1 RF GND0 NC/UUS RF — R362 0R — WIFI_ANT
C145 NC/12pF — C142 NC/12pF

WPN252012H2R2MT_4.7uH_2A — L8 L1008 — VIN_LDO_OUT — VIN_LDO
C74 22uF C0603 — C73 0.1uF C0402

WIFI_VDDIO — R282 100K R0402 — BT_RST_N — C92 0.1uF

Internal EPHY

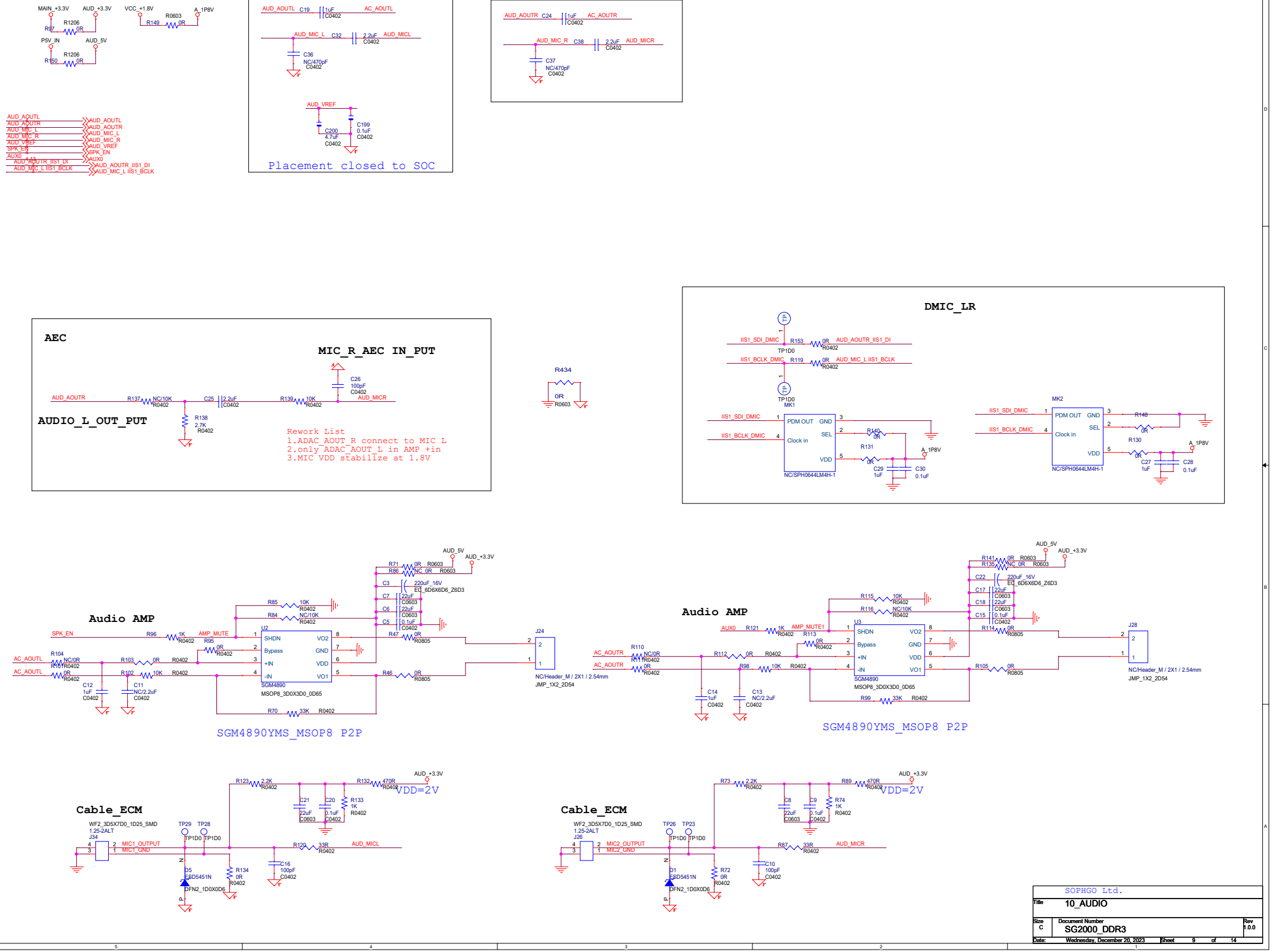
U1 M1601AG TF16_12D1X5D1_1D27
EPHY_TXP R34 0R R0402 — TD+ TCT TD- NC1 NC2 RD+ RCT RD- NC3 NC4 TCM — TX+ TX- RX+ RX-
EPHY_TXN R35 0R R0402
EPHY_RXP R45 0R R0402
EPHY_RXN R44 0R R0402
C158 NC_0.1uF C0402 — C159 NC_0.1uF C0402
TX_D+ TX_D- RX_D+ RX_D-
R410 75R_1% R0402 — R411 75R_1% R0402 — R417 75R_1% R0402 — R418 75R_1% R0402
C168 C1206 1000pF/2KV — C156 C1206 1000pF/2KV

J18 845-56311-188 RJ45_16D45X15D65_2D04
TXCT RXCT — TX_D+ TX_D- — RX_D+ RX_D- — NC CH_GND
YEL_C YEL_A+ — GRN_C GRN_A+ — NC1 NC2 — GND2 GND1
YEL_C — YEL_A — R14 1K R0402
GRN_C — GRN_A — R81 1K R0402
MAIN_+3.3V

EPHY_LNK_SPD — R1 1K R0402 — Q5 S9013 SOT23_BEC — YEL_C
EPHY_LNK_LED — R68 1K R0402 — Q6 S9013 SOT23_BEC — GRN_C

J17 CON1 — R36 NC/0R — EPHY_TXP
J19 CON1 — R37 NC/0R — EPHY_TXN
J23 CON1 — R41 NC/0R — EPHY_RXP
J25 CON1 — R45 NC/0R — EPHY_RXN

SOPHGO Ltd.
Title: 09_RJ45
Size: Custom | Document Number: SG2000_DDR3 | Rev: 1.0.0
Date: Wednesday, December 20, 2023 | Sheet 8 of 14



MAIN_+3.3V
AUD_+3.3V
VCC_+1.8V
A_1P8V
R1206
R97
0R
R0603
R149
0R
P5V_IN
AUD_5V
R1206
R150
0R
AUD_AOUTL
AUD_AOUTR
AUD_MIC_L
AUD_MIC_R
AUD_VREF
SPK_EN
AUX0
AUD_AOUTR_IIS1_DI
AUD_MIC_L IIS1_BCLK
AUD_AOUTL C19 1uF C0402 AC_AOUTL
AUD_MIC_L C32 2.2uF C0402 AUD_MICL
C36 NC/470pF C0402
AUD_VREF
C200 4.7uF C0402
C199 0.1uF C0402
Placement closed to SOC
AUD_AOUTR C24 1uF C0402 AC_AOUTR
AUD_MIC_R C38 2.2uF C0402 AUD_MICR
C37 NC/470pF C0402
AEC
MIC_R_AEC IN_PUT
AUD_AOUTR
R137 NC/10K R0402
C25 2.2uF C0402
R139 10K R0402
C26 100pF C0402
AUD_MICR
AUDIO_L_OUT_PUT
R138 2.7K R0402
Rework List
1.ADAC_AOUT_R connect to MIC L
2.only ADAC_AOUT_L in AMP +in
3.MIC VDD stabilize at 1.8V
R434
0R
R0603
DMIC_LR
IIS1_SDI_DMIC
R153 0R R0402 AUD_AOUTR_IIS1_DI
TP1D0
IIS1_BCLK_DMIC
R119 0R R0402 AUD_MIC_L IIS1_BCLK
TP1D0
MK1
PDM OUT
GND
SEL
Clock in
VDD
NC/SPH0644LM4H-1
R140 0R
R131 0R
C29 1uF
C30 0.1uF
A_1P8V
MK2
R148 0R
R130 0R
C27 1uF
C28 0.1uF
Audio AMP
SPK_EN
R96 1K R0402
AMP_MUTE
R95 0R R0402
AC_AOUTL
R104 NC/0R R0402
R103 0R R0402
R102 10K R0402
C12 1uF C0402
C11 NC/2.2uF C0402
U2
SHDN
Bypass
+IN
-IN
VO2
GND
VDD
VO1
SGM4890
MSOP8_3D0X3D0_0D65
R85 10K R0402
R84 NC/10K R0402
R71 0R R0603
R86 NC 0R R0603
C3 220uF_16V EC_6D6X6D6_Z6D3
C7 22uF C0603
C6 22uF C0603
C5 0.1uF C0402
R47 0R R0805
R46 0R R0805
R70 33K R0402
J24
NC/Header_M / 2X1 / 2.54mm
JMP_1X2_2D54
SGM4890YMS_MSOP8 P2P
AUD_5V
AUD_+3.3V
Audio AMP
AUX0
R121 1K R0402
AMP_MUTE1
R113 0R R0402
AC_AOUTR
R110 NC/0R R0402
R112 0R R0402
R98 10K R0402
C14 1uF C0402
C13 NC/2.2uF C0402
U3
SHDN
Bypass
+IN
-IN
VO2
GND
VDD
VO1
SGM4890
MSOP8_3D0X3D0_0D65
R115 10K R0402
R116 NC/10K R0402
R99 33K R0402
R141 0R R0603
R135 NC 0R R0603
C22 220uF_16V EC_6D6X6D6_Z6D3
C17 22uF C0603
C18 22uF C0603
C15 0.1uF C0402
R114 0R R0805
R105 0R R0805
J28
NC/Header_M / 2X1 / 2.54mm
JMP_1X2_2D54
SGM4890YMS_MSOP8 P2P
Cable_ECM
WF2_3D5X7D0_1D25_SMD
1.25-2ALT
J34
MIC1_OUTPUT
MIC1_GND
TP29
TP28
TP1D0
TP1D0
D5
ESD5451N
DFN2_1D0X0D6
R134 0R R0402
R123 2.2K R0402
R132 470R R0402
AUD_+3.3V
VDD=2V
C21 22uF C0603
C20 0.1uF C0402
R133 1K R0402
R120 33R R0402
AUD_MICL
C16 100pF C0402
Cable_ECM
WF2_3D5X7D0_1D25_SMD
1.25-2ALT
J26
MIC2_OUTPUT
MIC2_GND
TP26
TP23
TP1D0
TP1D0
D1
ESD5451N
DFN2_1D0X0D6
R72 0R R0402
R73 2.2K R0402
R89 470R R0402
AUD_+3.3V
VDD=2V
C8 22uF C0603
C9 0.1uF C0402
R74 1K R0402
R87 33R R0402
AUD_MICR
C10 100pF C0402
SOPHGO Ltd.
Title 10_AUDIO
Size C
Document Number SG2000_DDR3
Rev 1.0.0
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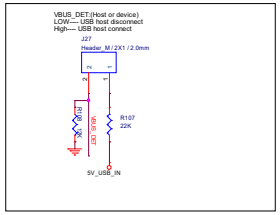
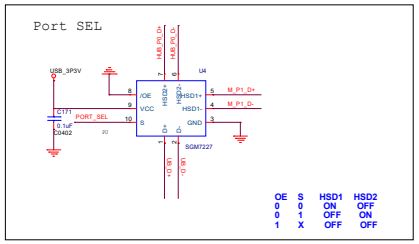
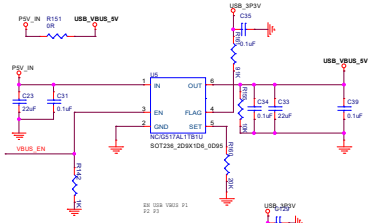
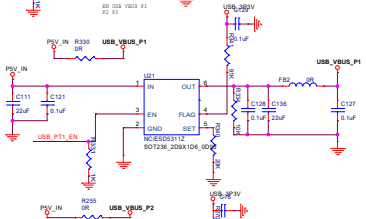
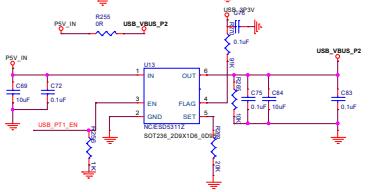


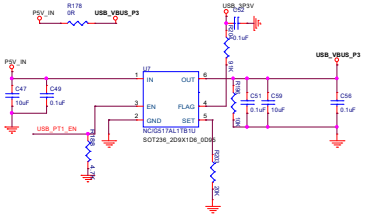


Level shift HUB Reset and Port SEL

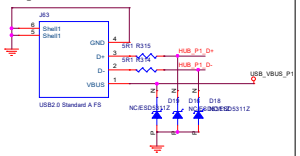


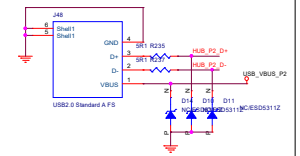


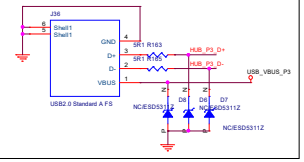


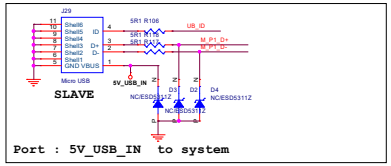


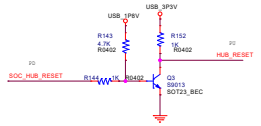
USB2.0 port 1/2/3



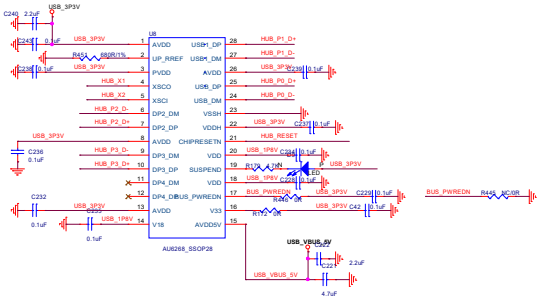


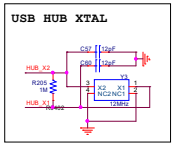






USB 2.0 HUB





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Title	08_USB	
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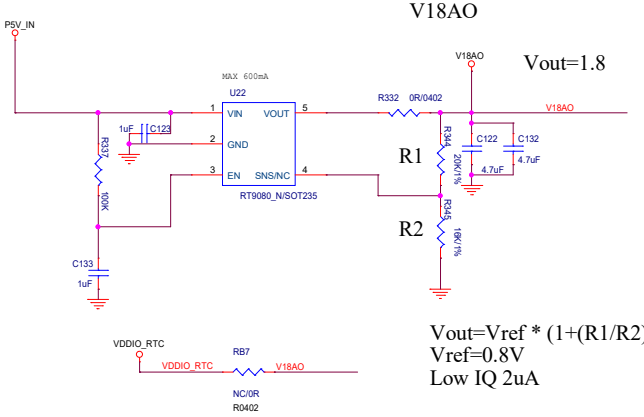
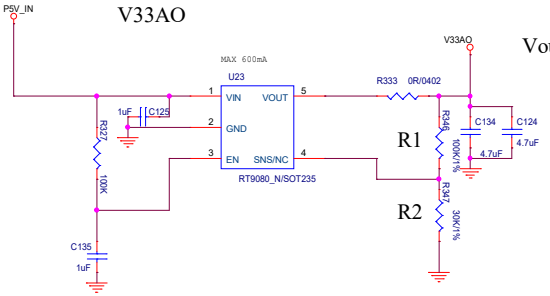
V18AO



Vout=1.8

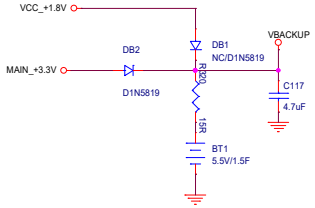
Vout=Vref * (1+(R1/R2))
Vref=0.8V
Low IQ 2uA

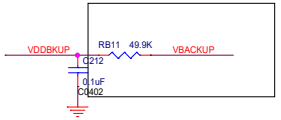
V33AO

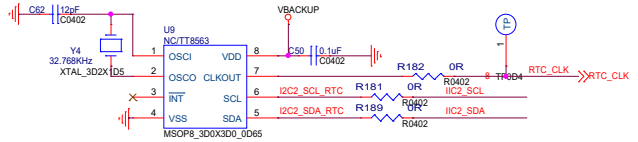


Vout=3.3

Vout=Vref * (1+(R1/R2))
Vref=0.8V
Low IQ 2uA







Title	<Title>		
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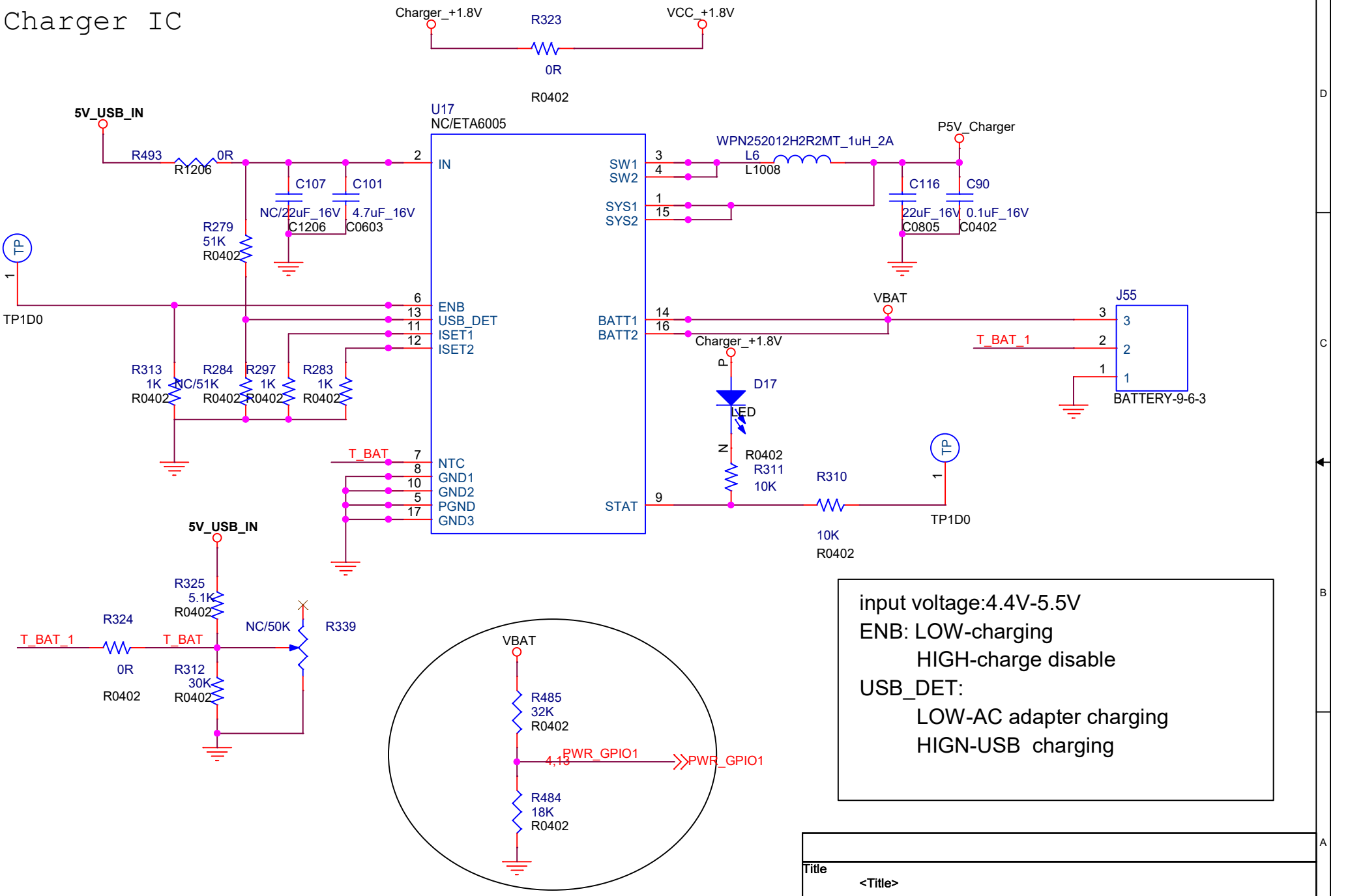
Charger IC



input voltage:4.4V-5.5V
ENB: LOW-charging
HIGH-charge disable
USB_DET:
LOW-AC adapter charging
HIGN-USB charging

Title	<Title>		
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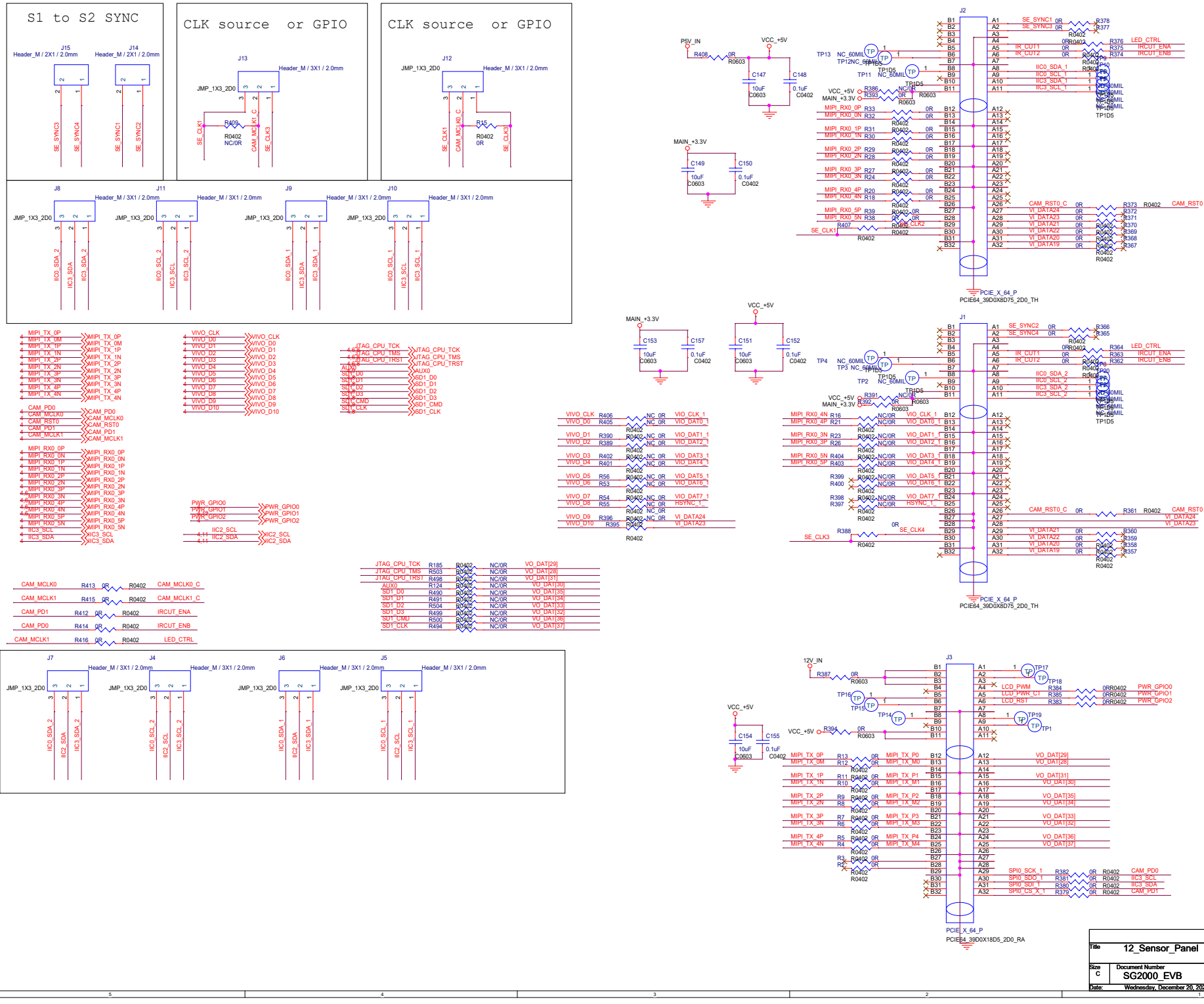
S1 to S2 SYNC
J15
Header_M / 2X1 / 2.0mm
J14
Header_M / 2X1 / 2.0mm
SE_SYNC3
SE_SYNC4
SE_SYNC1
SE_SYNC2
CLK source or GPIO
J13
Header_M / 3X1 / 2.0mm
JMP_1X3_2D0
SE_CLK1
R409
R0402
NC/0R
CAM_MCLK1_C
SE_CLK3
CLK source or GPIO
J12
Header_M / 3X1 / 2.0mm
JMP_1X3_2D0
SE_CLK1
CAM_MCLK0_C
R15
R0402
0R
SE_CLK3
J8
J11
J9
J10
Header_M / 3X1 / 2.0mm
JMP_1X3_2D0
IIC0_SDA_2
IIC3_SDA
IIC3_SCL
IIC0_SCL_2
IIC0_SDA_1
IIC0_SCL_1
P5V_IN
R408
0R
R0603
VCC_+5V
C147
10uF
C0603
C148
0.1uF
C0402
MAIN_+3.3V
C149
C150
J2
PCIE_X_64_P
PCIE64_39D0X8D75_2D0_TH
J1
PCIE_X_64_P
PCIE64_39D0X8D75_2D0_TH
J7
J4
J6
J5
12V_IN
R387
J3
PCIE_X_64_P
PCIE64_39D0X18D5_2D0_RA
MIPI_TX_0P
MIPI_TX_0M
MIPI_TX_1P
MIPI_TX_1N
MIPI_TX_2P
MIPI_TX_2N
MIPI_TX_3P
MIPI_TX_3N
MIPI_TX_4P
MIPI_TX_4N
CAM_PD0
CAM_MCLK0
CAM_RST0
CAM_PD1
CAM_MCLK1
MIPI_RX0_0P
MIPI_RX0_0N
MIPI_RX0_1P
MIPI_RX0_1N
MIPI_RX0_2P
MIPI_RX0_2N
MIPI_RX0_3P
MIPI_RX0_3N
MIPI_RX0_4P
MIPI_RX0_4N
MIPI_RX0_5P
MIPI_RX0_5N
IIC3_SCL
IIC3_SDA
VIVO_CLK
VIVO_D0
VIVO_D1
VIVO_D2
VIVO_D3
VIVO_D4
VIVO_D5
VIVO_D6
VIVO_D7
VIVO_D8
VIVO_D9
VIVO_D10
JTAG_CPU_TCK
JTAG_CPU_TMS
JTAG_CPU_TRST
AUX0
SD1_D0
SD1_D1
SD1_D2
SD1_D3
SD1_CMD
SD1_CLK
PWR_GPIO0
PWR_GPIO1
PWR_GPIO2
IIC2_SCL
IIC2_SDA
CAM_MCLK0
CAM_MCLK1
CAM_PD1
CAM_PD0
CAM_MCLK1
R413
R415
R412
R414
R416
IRCUT_ENA
IRCUT_ENB
LED_CTRL
SE_SYNC1
SE_SYNC2
SE_SYNC3
SE_SYNC4
IR_CUT1
IR_CUT2
IIC0_SDA_1
IIC0_SCL_1
IIC3_SDA_1
IIC3_SCL_1
IIC0_SDA_2
IIC0_SCL_2
IIC3_SDA_2
IIC3_SCL_2
CAM_RST0_C
VI_DATA24
VI_DATA23
VI_DATA21
VI_DATA22
VI_DATA20
VI_DATA19
SE_CLK2
SE_CLK4
VIO_CLK_1
VIO_DAT0_1
VIO_DAT1_1
VIO_DAT2_1
VIO_DAT3_1
VIO_DAT4_1
VIO_DAT5_1
VIO_DAT6_1
VIO_DAT7_1
HSYNC_1
VI_DATA24
VI_DATA23
VO_DAT[29]
VO_DAT[28]
VO_DAT[31]
VO_DAT[30]
VO_DAT[35]
VO_DAT[34]
VO_DAT[33]
VO_DAT[32]
VO_DAT[36]
VO_DAT[37]
LCD_PWM
LCD_PWR_CT
LCD_RST
SPI0_SCK_1
SPI0_SDO_1
SPI0_SDI_1
SPI0_CS_X_1
CAM_PD0
IIC3_SCL
IIC3_SDA
CAM_PD1
Title
12_Sensor_Panel
Size
C
Document Number
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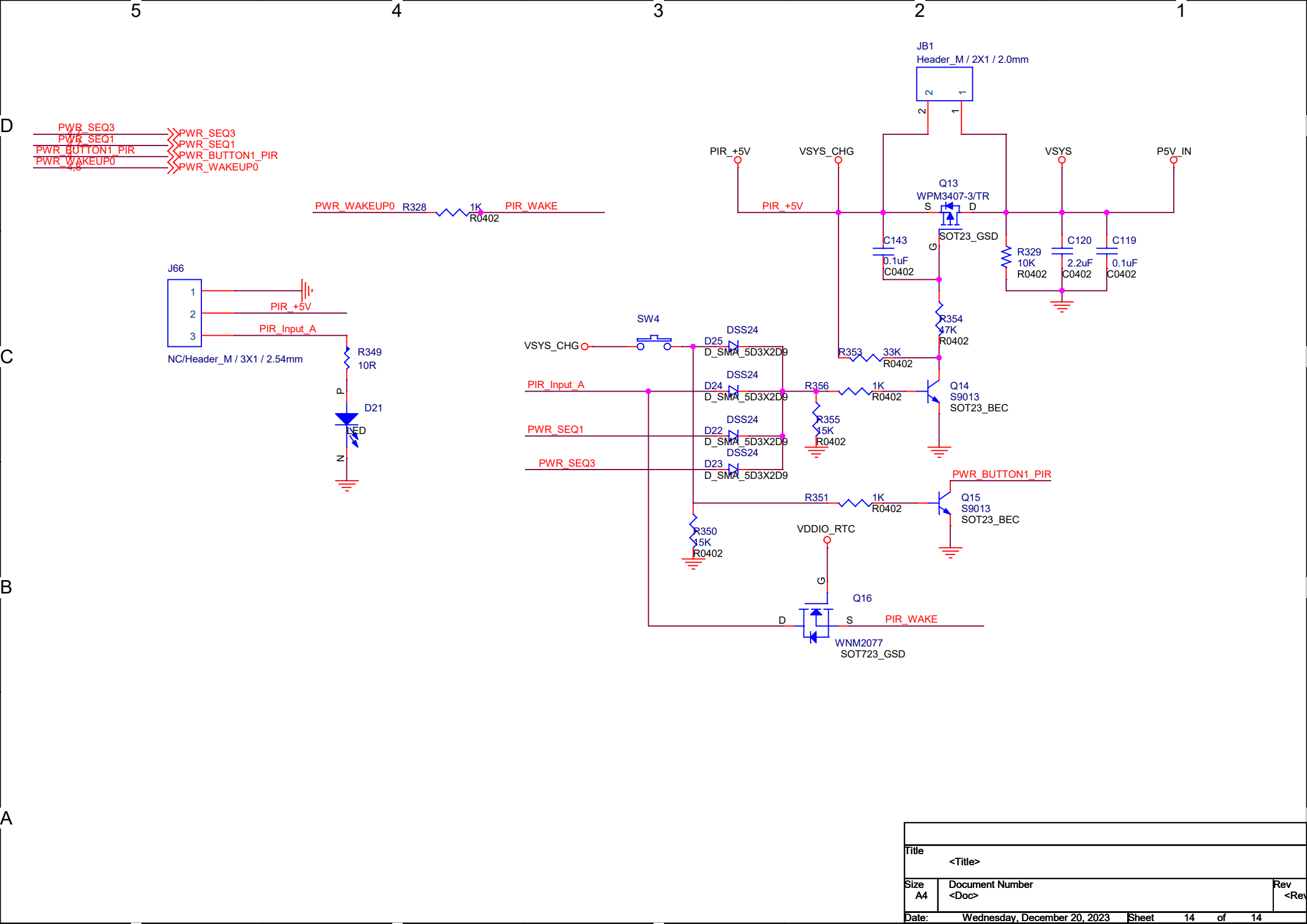
5
4
3
2
1
D
C
B
A
PWR_SEQ3
PWR_SEQ1
PWR_BUTTON1_PIR
PWR_WAKEUP0
4,8
PWR_SEQ3
PWR_SEQ1
PWR_BUTTON1_PIR
PWR_WAKEUP0
PWR_WAKEUP0
R328
1K
R0402
PIR_WAKE
J66
1
2
3
PIR_+5V
PIR_Input_A
NC/Header_M / 3X1 / 2.54mm
R349
10R
P
D21
LED
N
JB1
Header_M / 2X1 / 2.0mm
2
1
PIR_+5V
VSYS_CHG
VSYS
P5V_IN
Q13
WPM3407-3/TR
S
D
G
SOT23_GSD
C143
0.1uF
C0402
R329
10K
R0402
C120
2.2uF
C0402
C119
0.1uF
C0402
R354
47K
R0402
R353
33K
R0402
SW4
VSYS_CHG
DSS24
D25
D_SMA_5D3X2D9
PIR_Input_A
DSS24
D24
D_SMA_5D3X2D9
R356
1K
R0402
Q14
S9013
SOT23_BEC
R355
15K
R0402
PWR_SEQ1
DSS24
D22
D_SMA_5D3X2D9
PWR_SEQ3
DSS24
D23
D_SMA_5D3X2D9
PWR_BUTTON1_PIR
R351
1K
R0402
Q15
S9013
SOT23_BEC
R350
15K
R0402
VDDIO_RTC
G
Q16
D
S
PIR_WAKE
WNM2077
SOT723_GSD
Title
<Title>
Size
A4
Document Number
<Doc>
Rev
<Rev
Date:
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14
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14