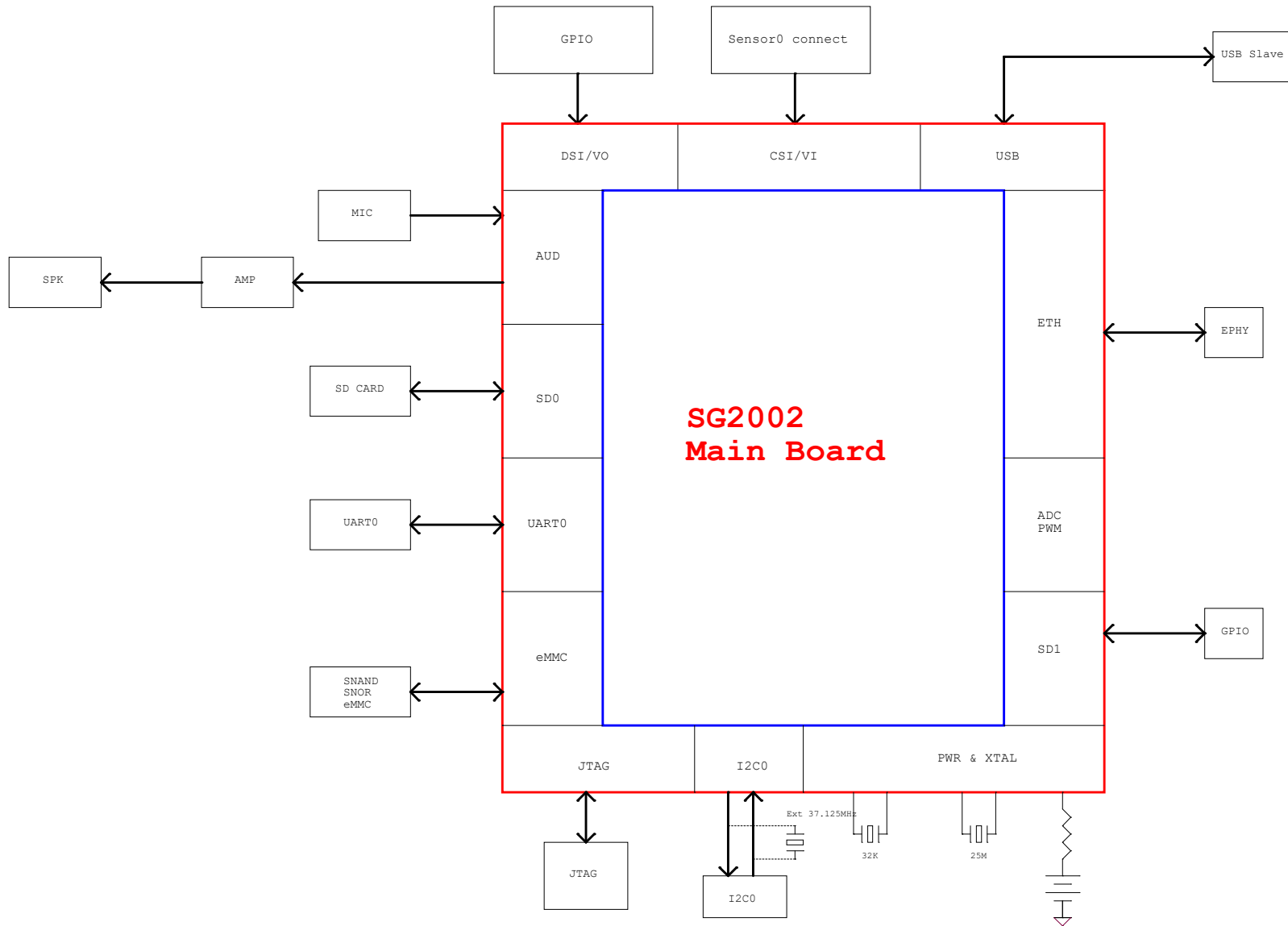


Block Diagram for SG2002



D

D

Version List			
Version	Change List	Date	Author
1.0.0	First Release	2020-03-07	Miller.Zhang
1.2.0	1、增加SG2002 VDDC电压0.92V说明	2022-12-28	Miller.Zhang

C

C

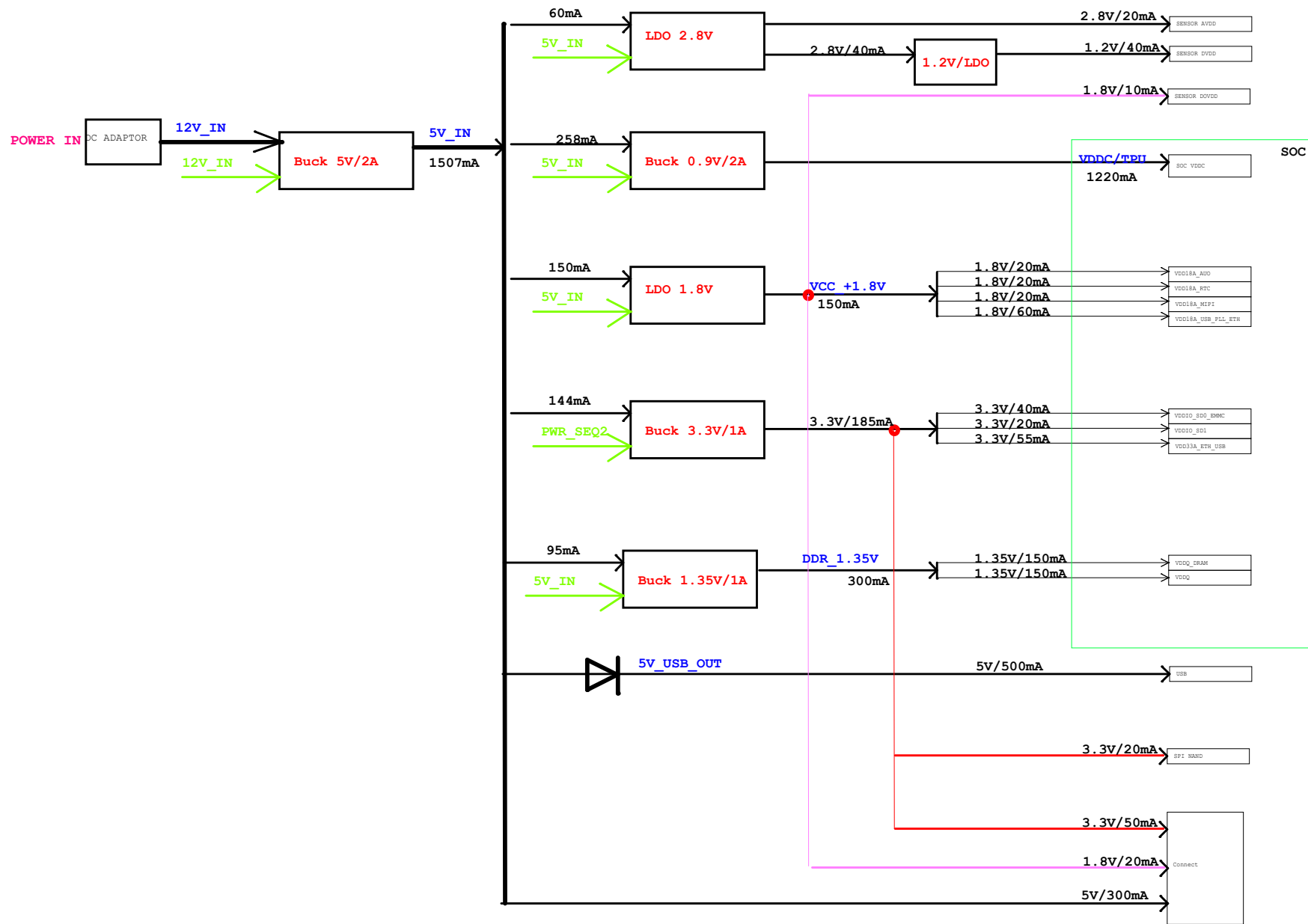
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B

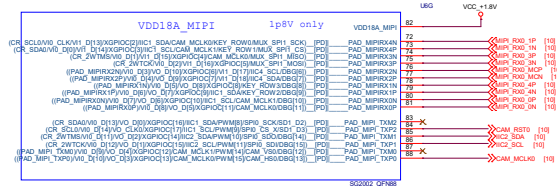
A

A

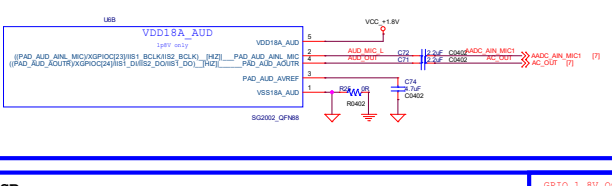
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Size A	Document Number SG2002_QFN_SIP_DDR		Rev 1.0.0
Date:	Wednesday, December 13, 2023	Sheet	2 of 10



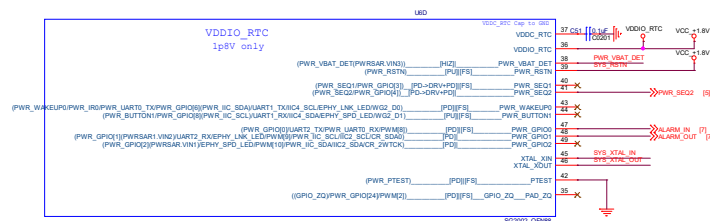
MIPI



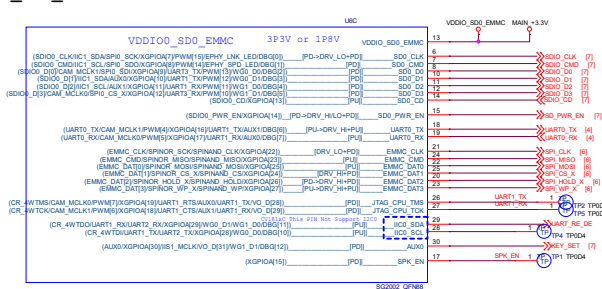
AUDIO



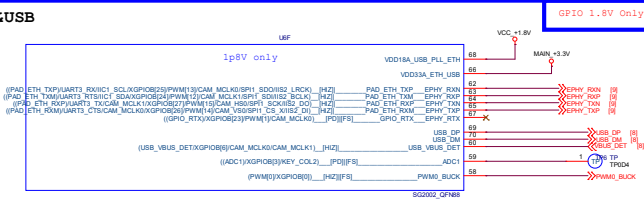
RTC



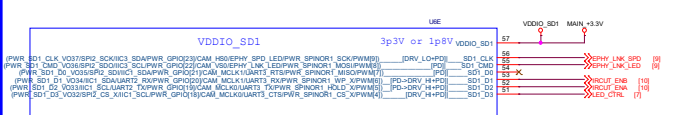
VDDIO SD0 EMMC



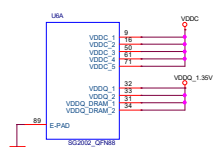
EPHY&USB



SD1



POWER&GND



EPAD Hole



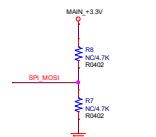
Pull Up



Debug

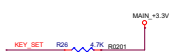


BOOT CONFIG

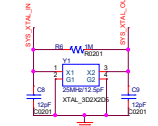


SPI_MCS1	SPI_WP_X	boot device selection
1	1	eMMC
1	0	Serial NOR
0	0	Serial NAND

Update&Reset Key



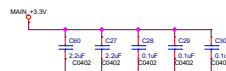
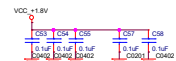
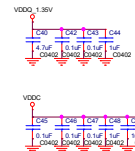
SYS XTAL

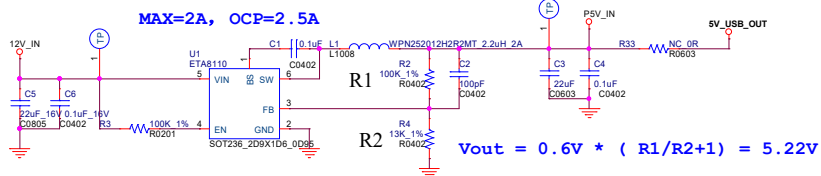


PWR DET



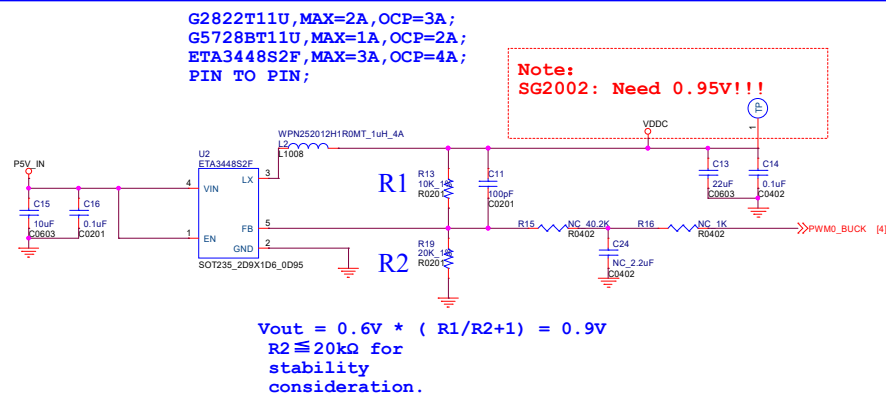
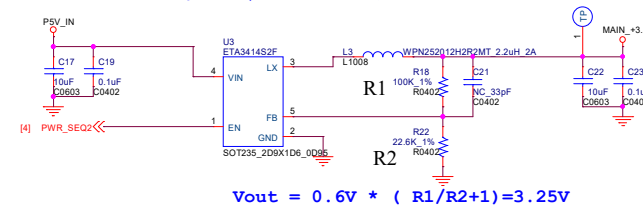
VBAT_DET Must be connect
Default 1.25V
When VBAT_DET< (1.0V)
shutdown





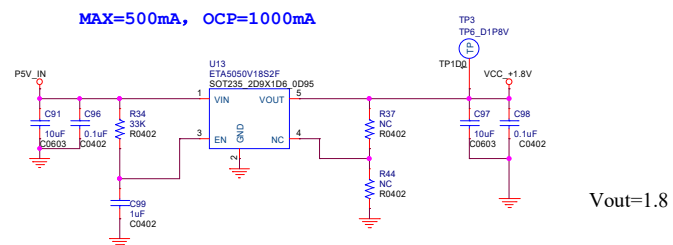
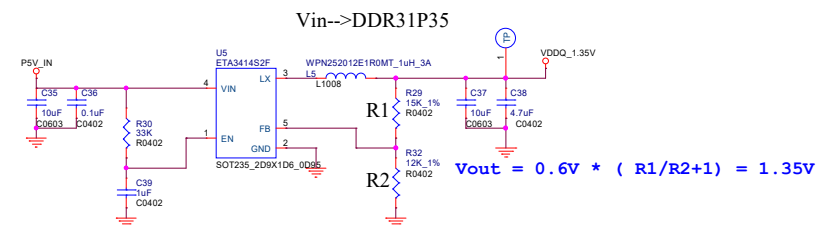
PWR_SEQ02

G2822T11U, MAX=2A, OCP=3A;
G5728BT11U, MAX=1A, OCP=2A;
ETA3414S2F, 1A, OCP=2A;
ETA3446S2F, 3A, OCP=4.5A;
以上4个PIN TO PIN;



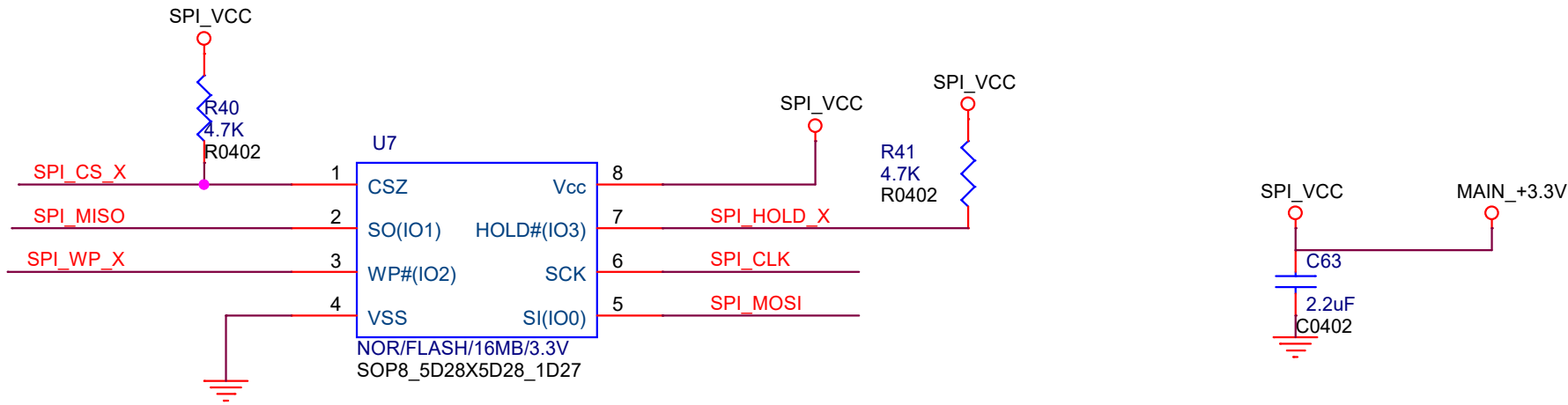
G2822T11U, MAX=2A, OCP=3A;
G5728BT11U, MAX=1A, OCP=2A;
ETA3414S2F, 1A, OCP=2A;
ETA3446S2F, 3A, OCP=4.5A;
以上4个PIN TO PIN;

Note:
SG2002: DDR3L VDDQ=1.35V



File	05_SYS_Power	Rev
Size	Document Number	Rev
Custom	SG2002_QFN_SIP_DDR	1.0.0
Date	Wednesday, December 13, 2023	Sheet 5 of 10

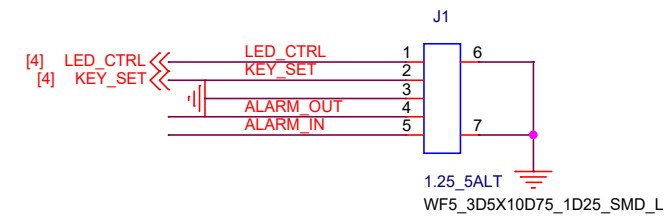
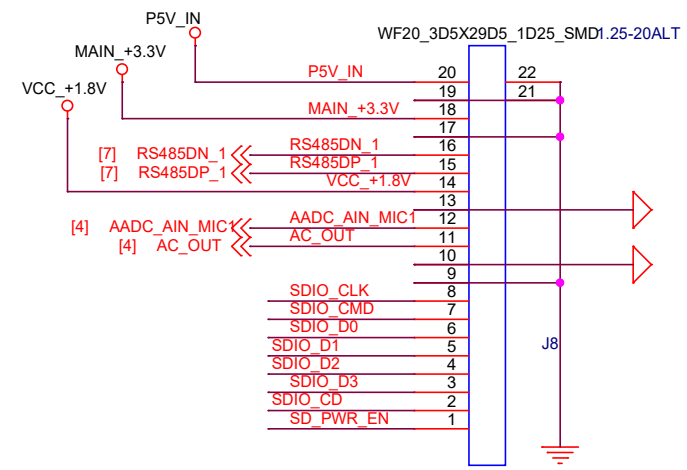
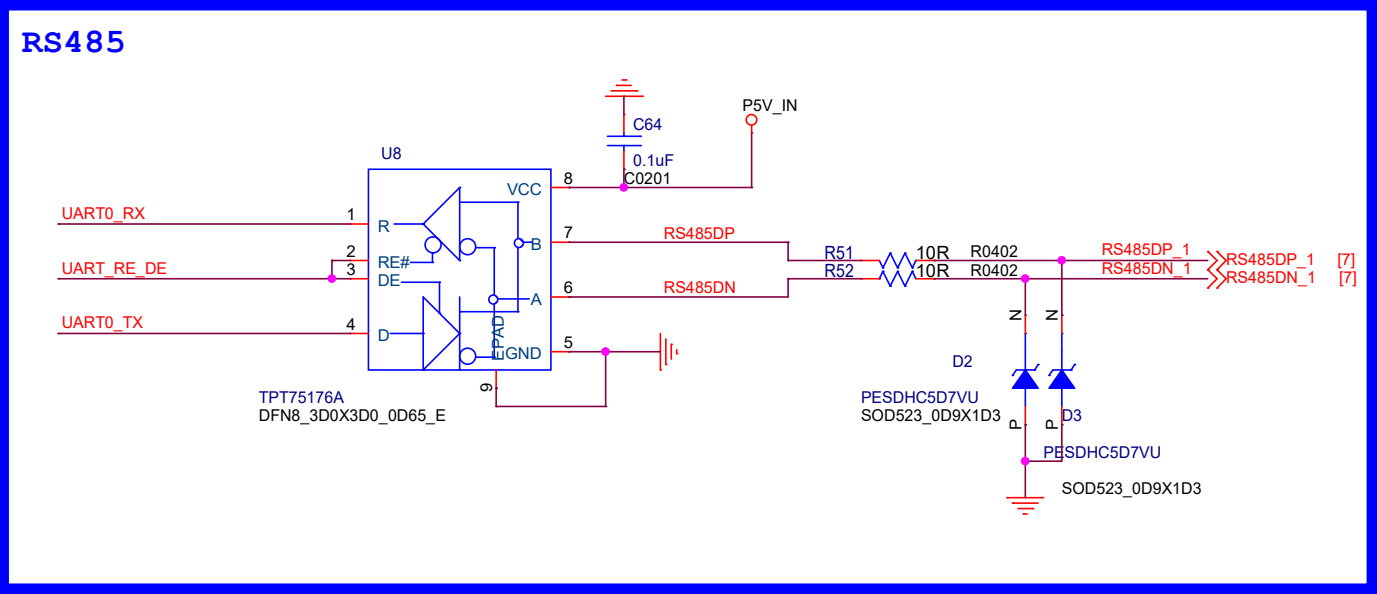
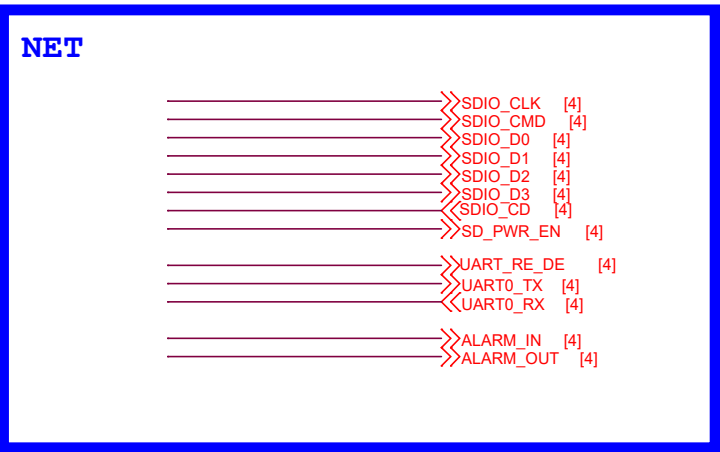
SPINOR



NET

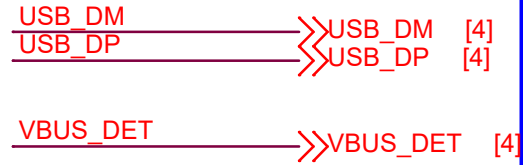


Title			06_eMMC_SDIO_SPIFLASH		
Size	Document Number				Rev
Custom	SG2002_QFN_SIP_DDR				1.0.0
Date:	Wednesday, December 13, 2023		Sheet	6	of 10

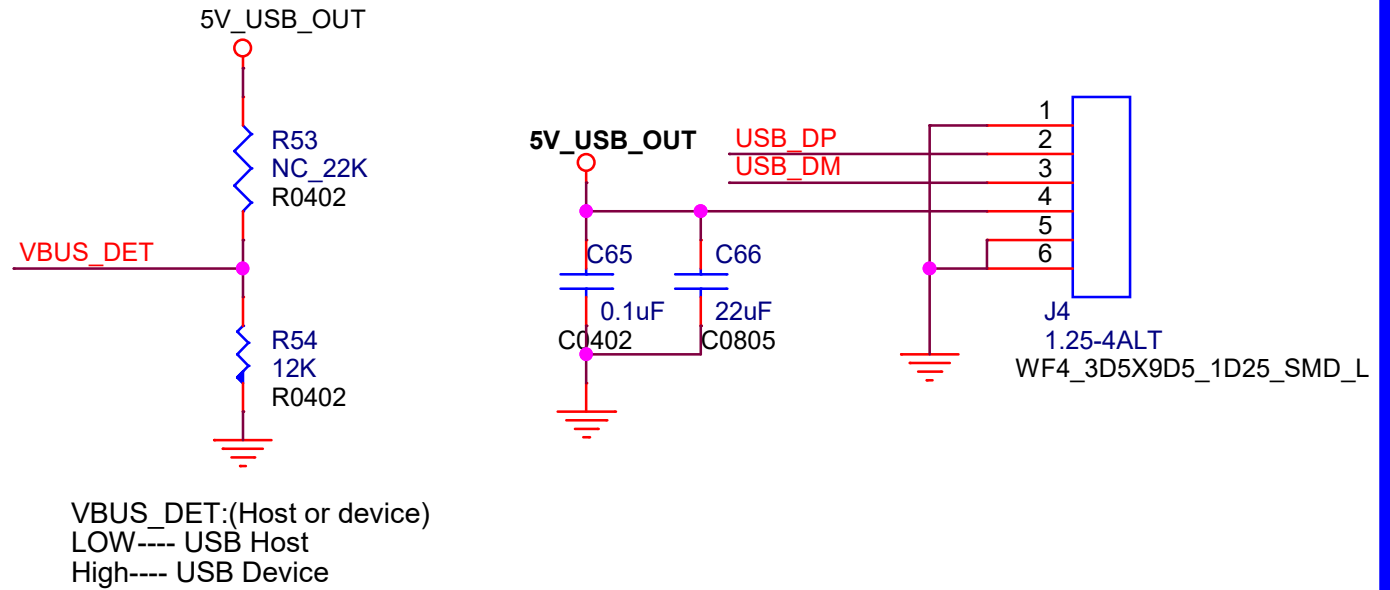


Title			07_I2C_UART
Size	Custom	Document Number	SG2002_QFN_SIP_DDR
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		Rev	1.0.0

NET



USB



Title

08_USB

Size

Custom

Document Number

SG2002_QFN_SIP_DDR

Rev

1.0.0

Date:

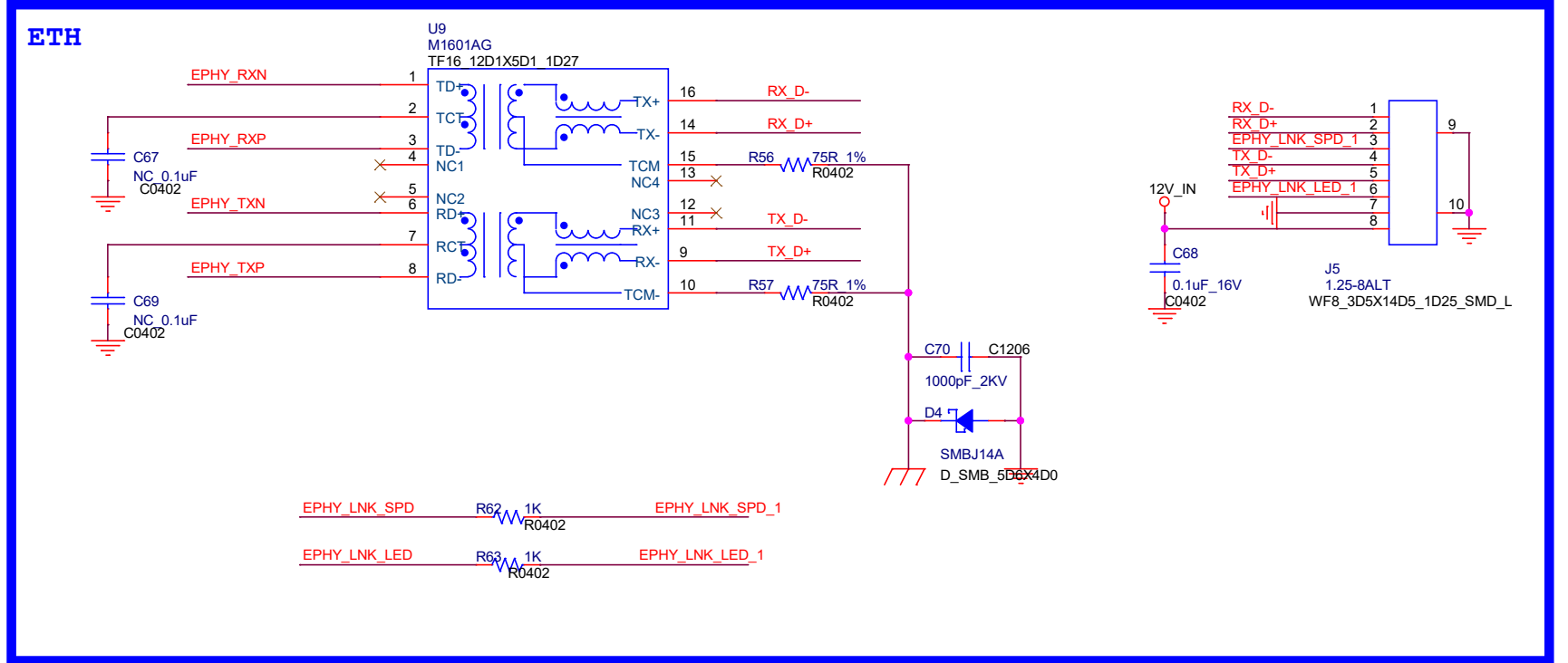
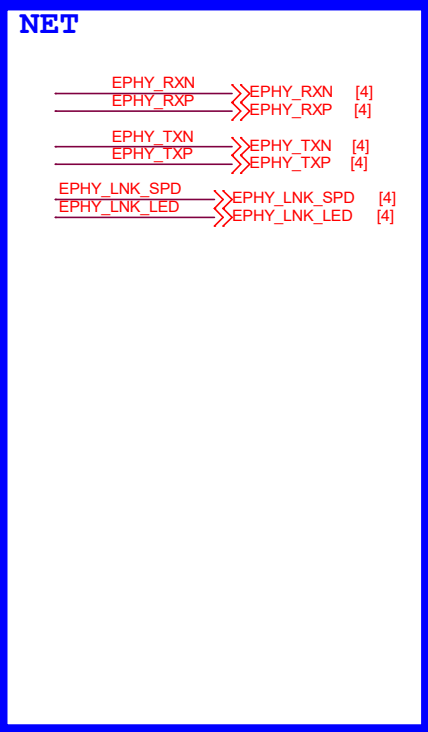
Wednesday, December 13, 2023

Sheet

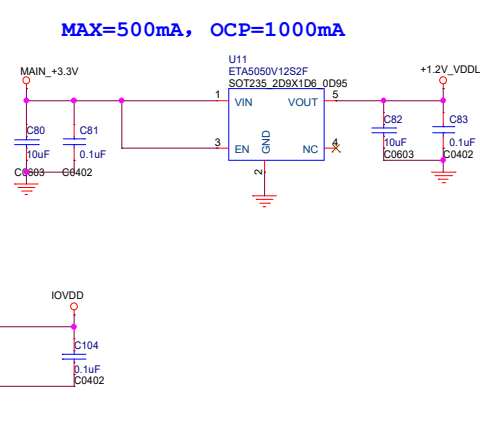
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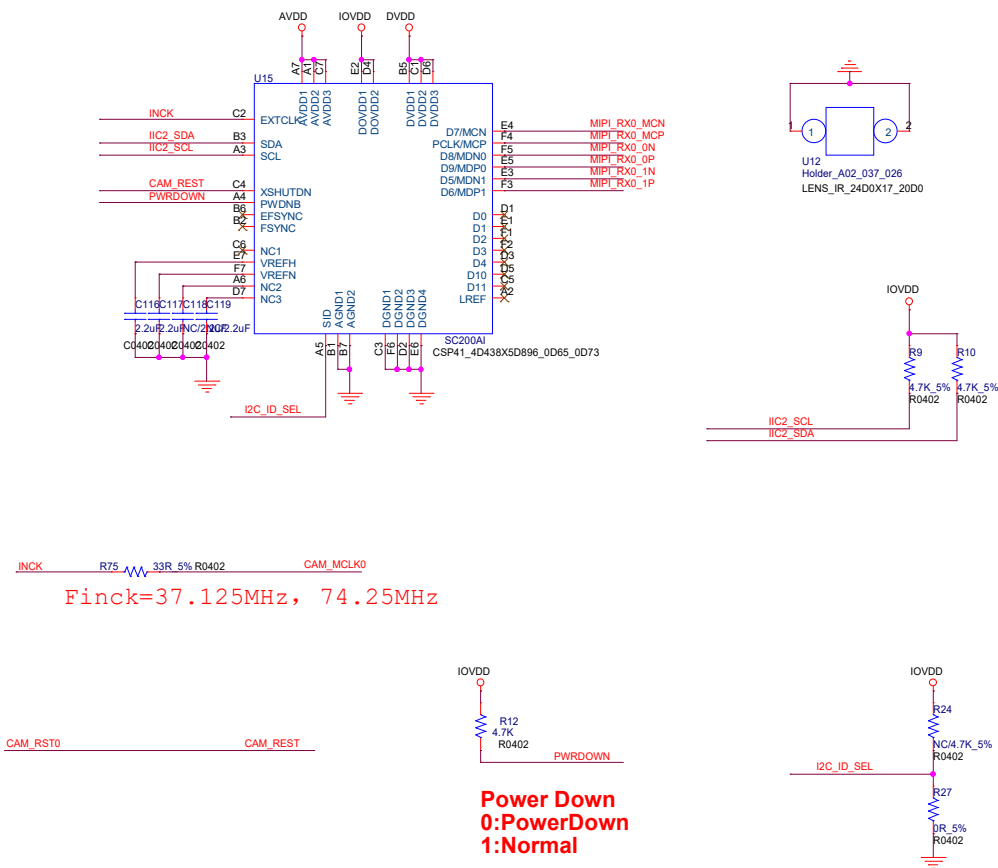
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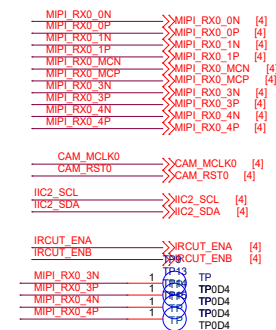
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Size	Custom	Document Number				Rev						
		SG2002_QFN_SIP_DDR				1.0.0						
Date:		Wednesday, December 13, 2023			Sheet		9		of		10	



SENSOR



NET



IR CUT

