

ELC 2137 Lab 2: Transistor

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Summary

The purpose of this lab was to demonstrate how transistors can be used to construct logic gates on an IDL-800 board. Several gates were constructed and tested such as the OR, NOT, and NOR gates as well as an unknown gate that combined a NOR gate with two inverting gates, showing how transistors can be used to hook gates together. It was found that the unknown gate was the AND gate. Additionally, the flow of current in each gate was analyzed as documented in the written results.

Q&A

1. What logic operation does it implement?

The logic operation in the "unknown" gate combination was found to be the AND style operation. This means the LED would only lit up when both switches were turned on(set as 1 values), as further discussed in the Results section.

Results

ELC 2137

Lab 2. Transistor Logic Gates

Circuit Demonstration Page

Student names: Ashlie Lackey CJ Jones

Instructor Initials

Pushbutton "Or Gate"

MB

Transistor Not gate

MB

Transistor Nor gate

MB

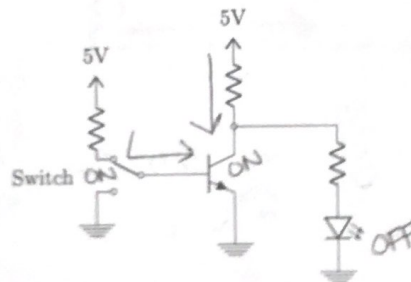
Transistor unknown gate

MB

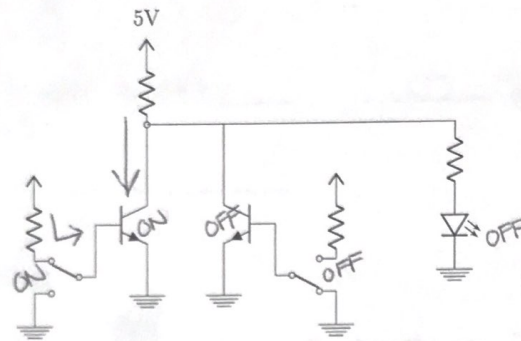
Diagrams

On each of the circuits below, draw the current paths and note whether each switch, transistor, and LED is ON or OFF.

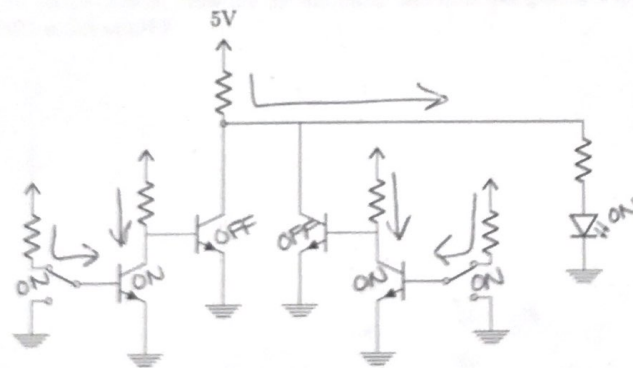
Inverter:



NOR:



Final gate:



| A | B | AND |
|---|---|-----|
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

Figure 1: This logic table demonstrates the results of the unknown gate, allowing it to be easily identified as an AND gate.

Conclusion

While the purpose of this report was not exactly to find values, but rather to test the construction of logic gates using transistors, the skills gained will help students in the future as they try to find how to accomplish goals using logic gates. Additionally, this lab allowed students to become comfortable with lab equipment that is sure to be utilized in the future.