

Introduction

- This Laboratory experiment, I was to display my name using a 7-segment display.
- Using knowledge logic gates and truth tables, I was to find the correct Boolean expressions that would fill up the segments for the display to properly display one letter at a time



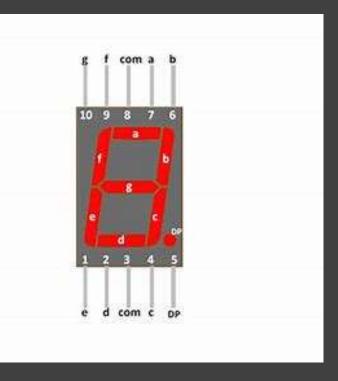
Logistics

- First step was to create the truth table.
- Once I had the truth table and I knew the output would be accurate, I would need to simplify each segment for the 7-segment display to get the Boolean expression ensuring I get the desired output.
- To get accurate Boolean Expressions, I would next have to create the kmaps

- Now that I have my Boolean Expressions, I would have to go into design my circuit and simulate it to make that everything is ok before I get my logic gates.
- If everything is ok, I could then get my components.
- Now and only now do I begin to build the physical circuit.
- Last step would be to trouble shoot any issues and make sure that everything is very similar to the simulation that I ran beforehand.

Truth Table

	Troth table		
Stat 0 1 2 3 4 5	000 B 1 1 001 E 1 0 010 R 1	11111	



Simplification

```
Fa(A,B,C)= (4,6)

Fa=ABC + ABC + ABC + ABC + ABC + ABC

- ABCC+C) + ABCC+C) + AC(B+B)

AB + AB + AC

ACB+B) + AC

A + AC = A + C

Fb=ABC + ABC + ABC + ABC + ABC

= ACCB+B) + BC (A+A) + ACCB+B)

= ACCB+B) + BC (A+A) + ACCB+B)

= ACCA+A) + BC = C+BC

Fb=C+B = FC
```

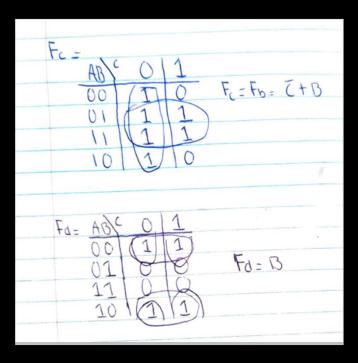
```
F_{a}(A,B,O) = (2,3,7) =
F_{d} = \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC}
= \overline{AB}(\overline{C} + C) + \overline{AB}(\overline{C} + C)
= \overline{AB} + \overline{AB}
= \overline{B}(\overline{A} + A)
= \overline{Fd} = \overline{B}
```

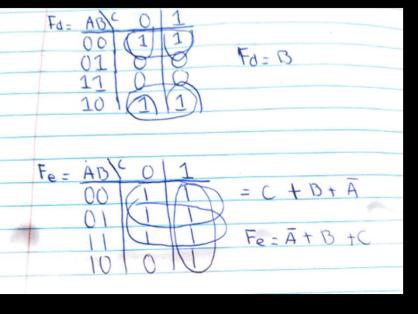
```
Fec A, B, O = (4)
Fe=ABC + ABC + ABC + ABC + ABC + ABC + ABC
= ABCZ+C) + ABCZ+C) + ABCZ+C) + ACCB+B)
 = AB + AB + AB + AC
   = ACB+B) + BCA+A) + AC
     = A+D+AC B+ A+C
        Fe= A+B+C
FFCA,B,O=
FR= ABC + ABC + ABC + ABC + ABC + ABC
+ ABC
= AB(C+c) + AB(C+c) + AB(C+c) +
ABCC +C)
     = AB + AB + AB + AB
    = A(B+B) + A(B+B)
          1 = A + A
```

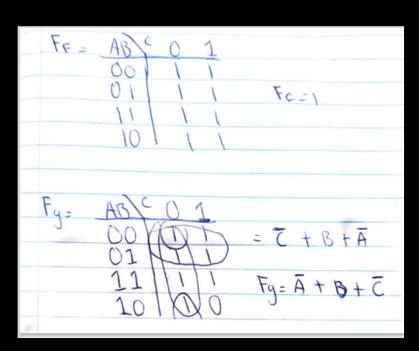
 $F_{g}(A,B,C)=(5)$ $F_{g}=\overline{ABC}+\overline{ABC}+\overline{ABC}+\overline{ABC}+\overline{ABC}$ $+AB\overline{C}+ABC$ $=\overline{ABCCC+C})+\overline{ABCCC+C})+\overline{ACCB}+\overline{B}$ $+ABC\overline{C}+C)$ $=\overline{AB}+\overline{AB}+\overline{AB}+\overline{AC}+\overline{AB}$ $+\overline{ACB}+\overline{B}$) $+\overline{ACB}+\overline{B}$) $+\overline{ACB}+\overline{AC}+\overline{BCA}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{BCA}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACB}+\overline{AC}+\overline{AC}+\overline{AC}+\overline{AC}$ $+\overline{ACCB}+\overline{AC}+\overline{ACCCB}+\overline{$

K-maps

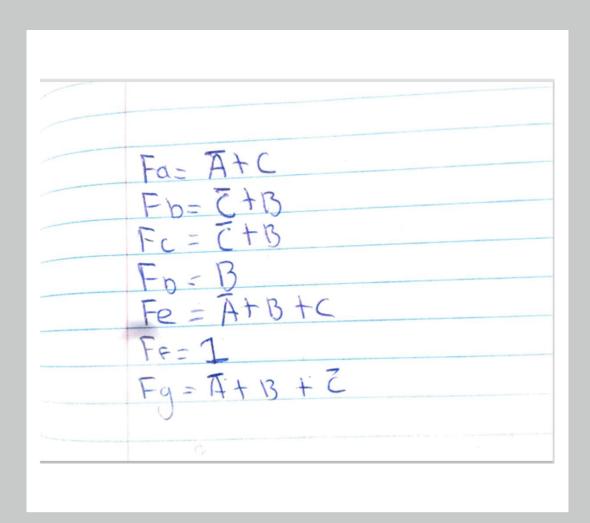
F _q = AB C 0 1 1 0 1 1 1 1 1 1	= A + C
$F_{b} = \frac{AB}{00} \begin{pmatrix} 0 & 1 \\ 00 & 1 & 0 \\ 01 & 1 & 1 \\ 11 & 1 & 1 \\ 10 & 1 & 0 \end{pmatrix}$	B + 5 =



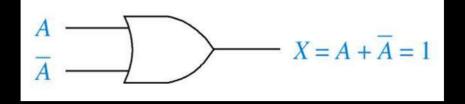


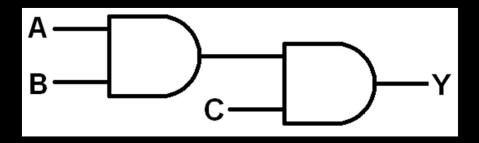


Boolean Expressions

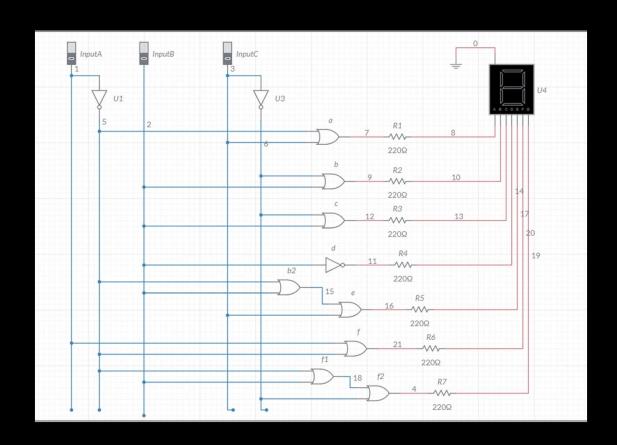


Logic Gates Operations Used



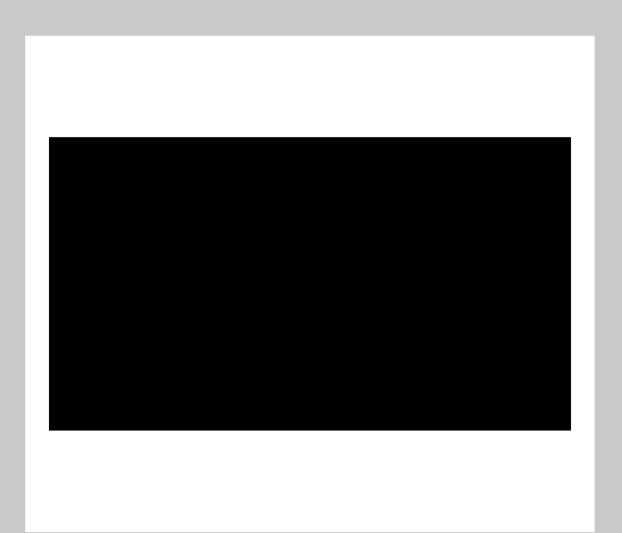


Multisim Simulation



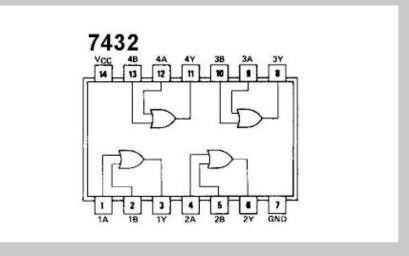
Video of Multisim Simulation

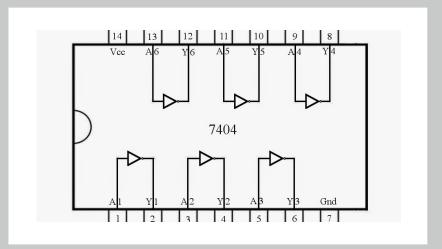
 Here is a quick simulation showing my circuit work with a simulation before I would eventually physically build it.



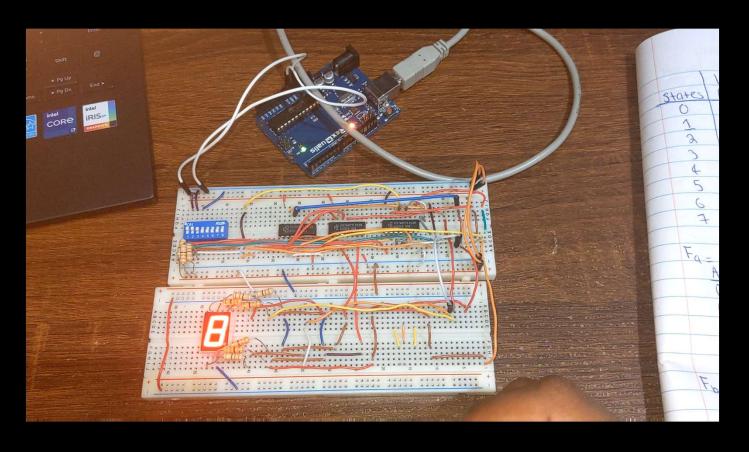
Components Needed

- 3- Inverters(1-7404)
- 8- OR gates(2-7432)





Video



Conclusion

- This experiment finally allowed me to implement the knowledge learned from previous classes involving logic gates and truth table.
- Finally, I was able to understand how to use a 7-segment display and how to display the proper characters throughout the different states.

References

- (1720) Designing a 7-segment hex decoder YouTube
- (1720) How To Drive A 7-segment Display The Learning Circuit YouTube
 - BCD to 7 Segment LED Display Decoder Circuit Diagram and Working (electronicshub.org)