**1.List some of the programmable logic (PL) interface types to the PS**

* EMIO
* Coresight
* IRQ
* DMA
* AXI\_GP de 32 bits (Maestro y esclavo)
* AXI\_ACP de 64 bits esclavo
* AXI\_HP (bus axi de alto rendimiento)
* CLK/PLL
* PCAP
* Reset
* DDR

**2.Explain the difference between interrupts from the PL to the PS and interrupts from the PS to the PL**

Interrupts from the PL to the PS are the ones the modules loaded in the bitstream generate to notify the PS.

Interrupts from the PS to the PL are the ones the PS generate to notify modules on the PL FPGA.