1. **Describe the boot sequence in the Zynq device**

Multi-stage boot process

— Stage 0: Runs from ROM; loads from non-volatile memory to OCM Provided by Xilinx; unmodifiable

— Stage 1: Runs from OCM; loads from non-volatile memory to DDRx memory User developed; Xilinx offers example code SDK project Initiates PS boot and PL configuration

— Stage 2: Optional; runs from DDR User developed; Xilinx offers example code

– Uboot Sourced from flash memory or through common peripherals, programmable logic I/O, etc.

1. **Describe the difference between secure and non-secure boot modes**

Non-secure boot path

— Flash memory to OCM or DDR or PL

— External peripheral to OCM or DDR or PL

Secure boot path

— Same as non-secure but passes through AES/SHA block in DEVCFG

1. **Explain how the device configuration interface is used in secure boot mode**

AES-256 and HMAC (SHA-256) is used to decrypt the bitstream and programs before been loaded to the memory.