add R[ra],R[rb],R[rc]

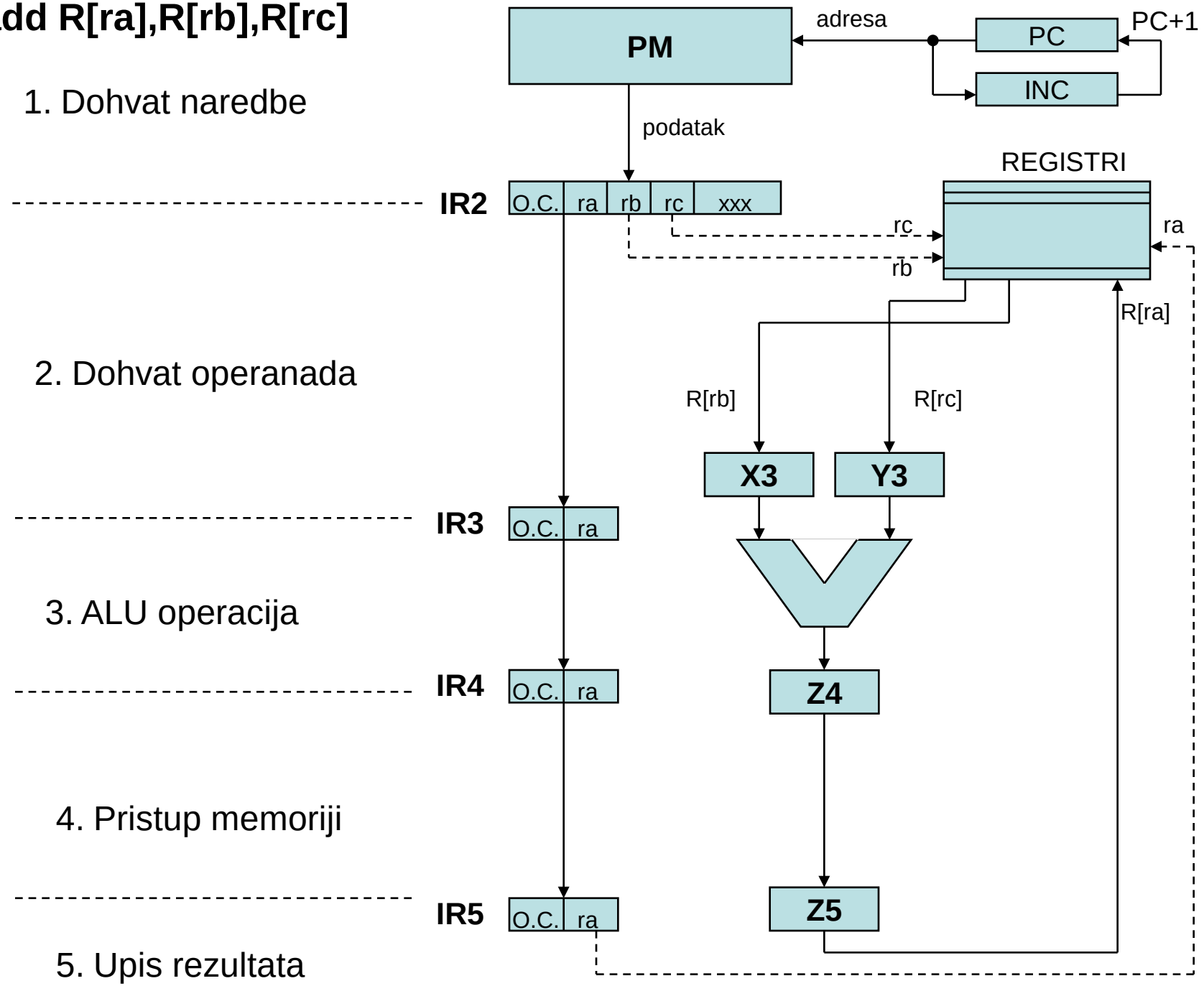
1. Dohvat naredbe

2. Dohvat operanada

3. ALU operacija

4. Pristup memoriji

5. Upis rezultata



addi R[ra],R[rb],c2

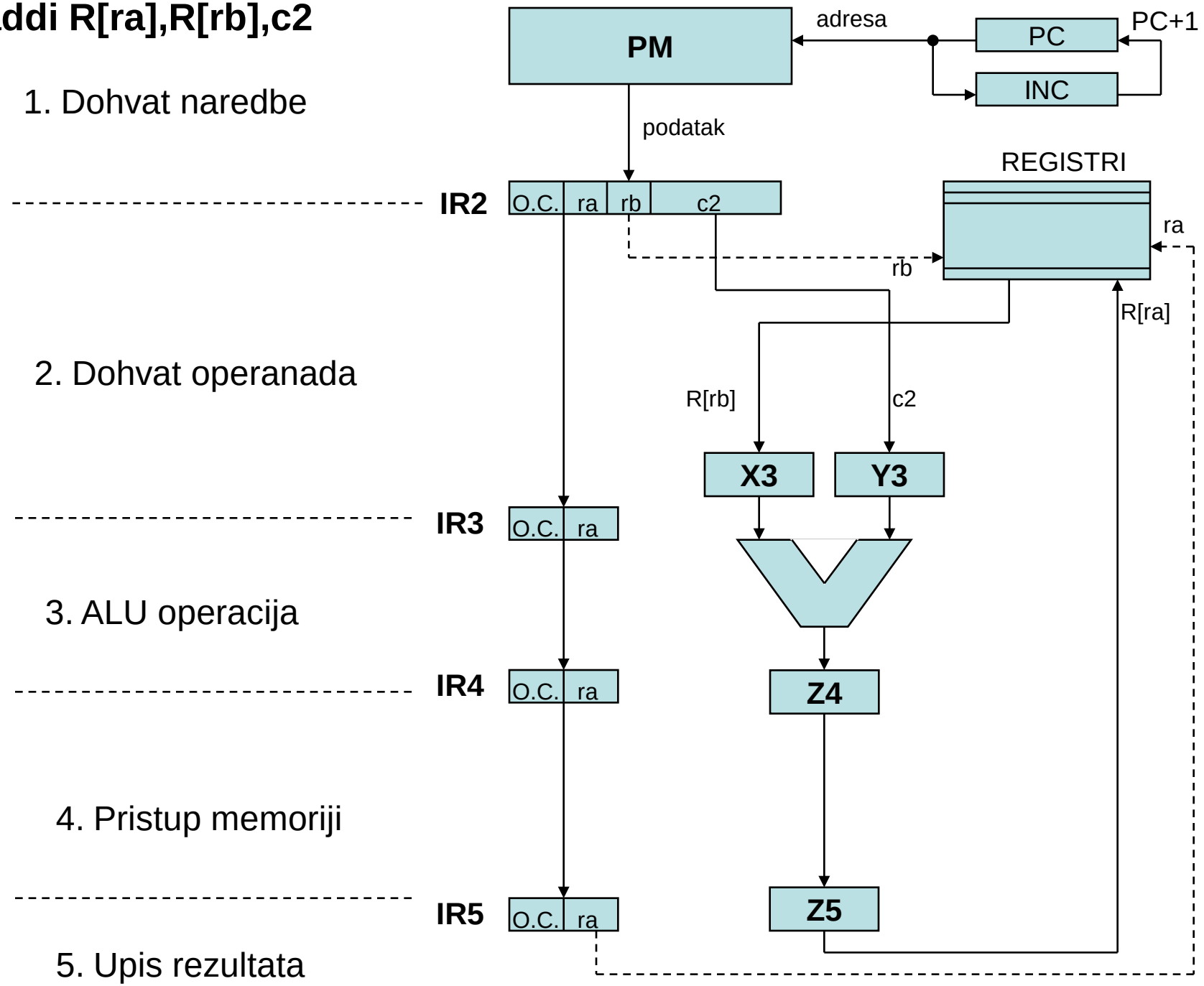
1. Dohvat naredbe

2. Dohvat operanada

3. ALU operacija

4. Pristup memoriji

5. Upis rezultata



add R[ra],R[rb],R[rc]
addi R[ra],R[rb],c2

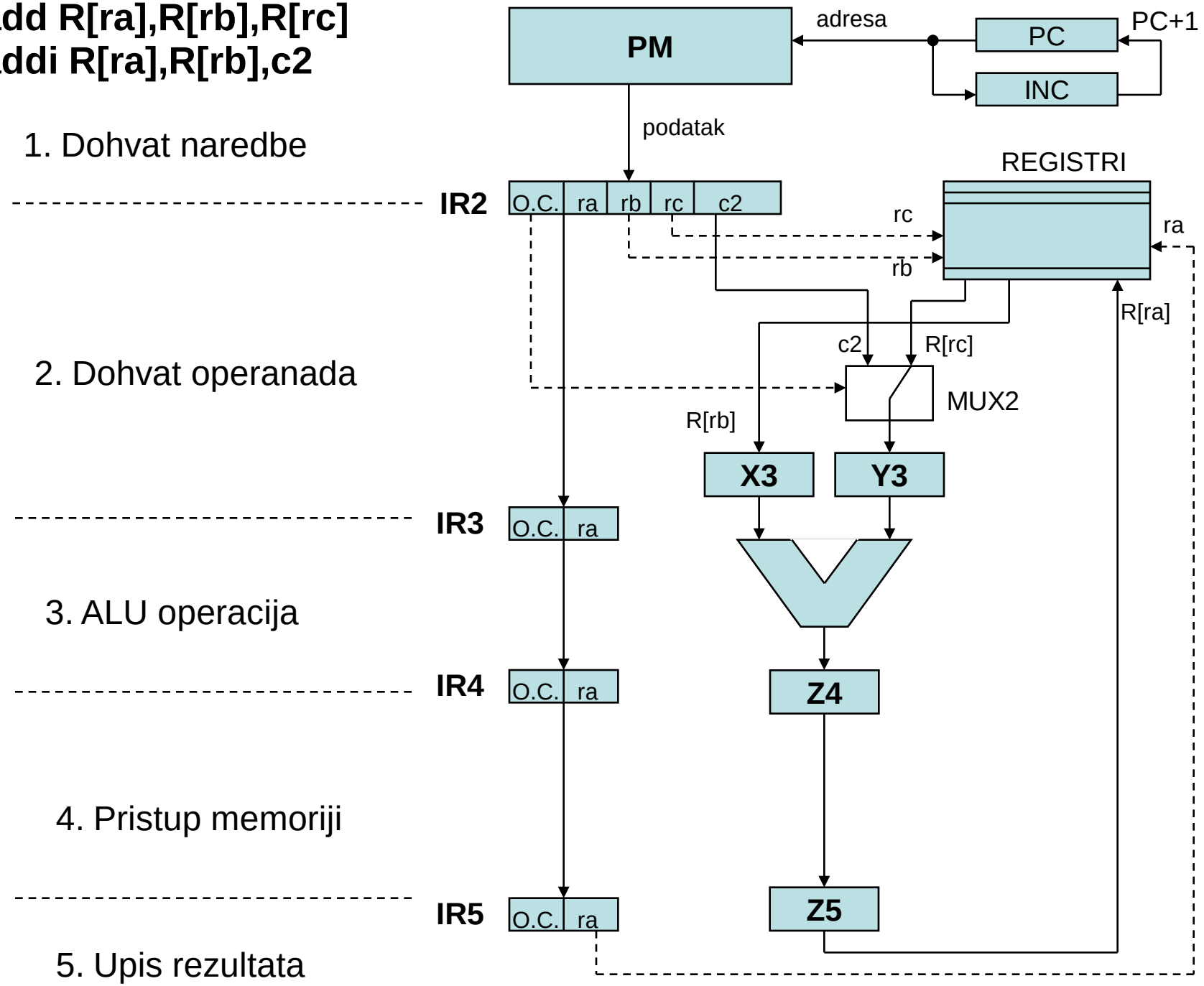
1. Dohvat naredbe

2. Dohvat operanada

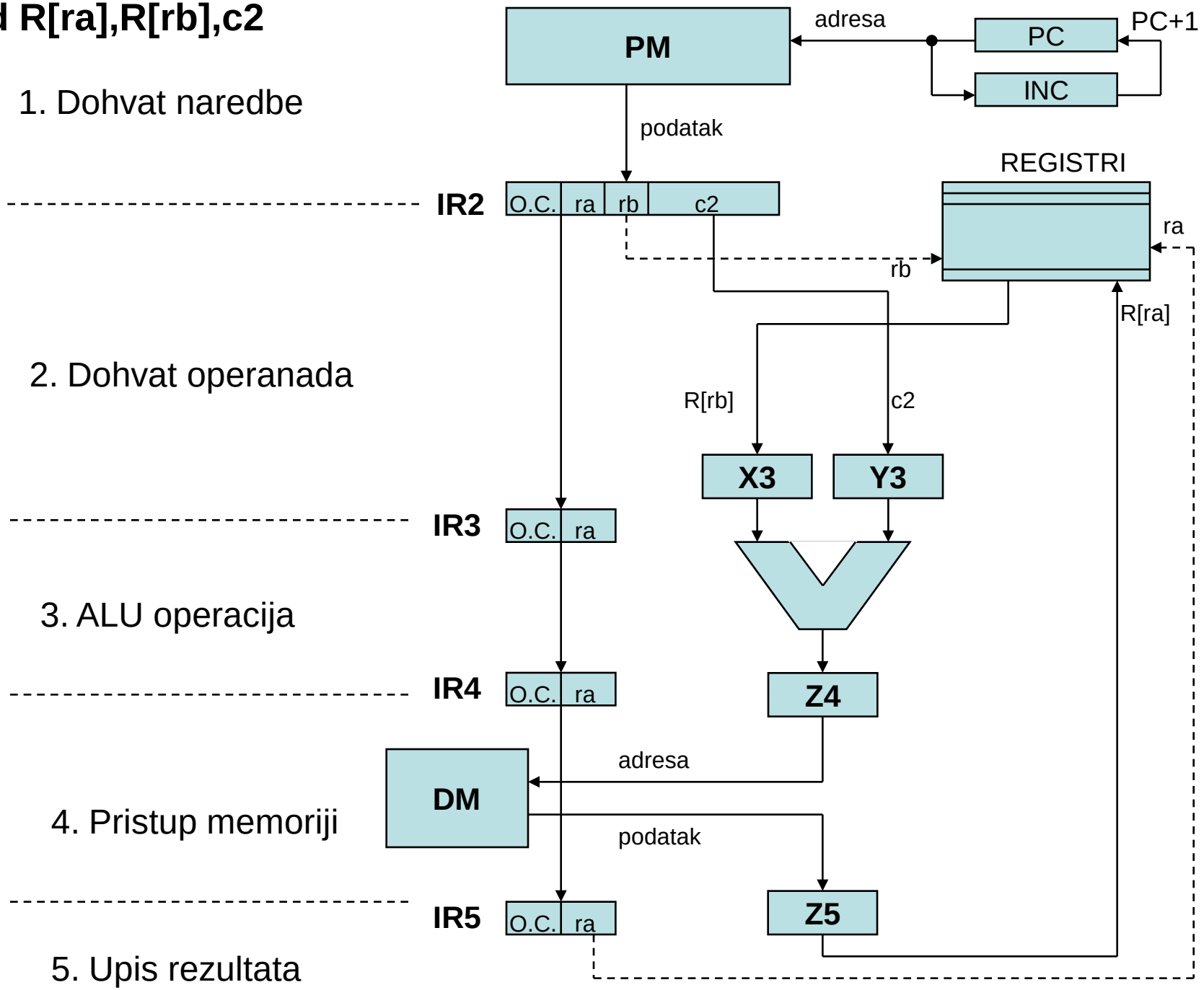
3. ALU operacija

4. Pristup memoriji

5. Upis rezultata



ld R[ra],R[rb],c2


1. Dohvat naredbe
PM
adresa
PC
PC+1
INC
podatak
REGISTRI
IR2
O.C.
ra
rb
c2
ra
rb
R[ra]
2. Dohvat operanada
R[rb]
c2
X3
Y3
IR3
O.C.
ra
3. ALU operacija
IR4
O.C.
ra
Z4
adresa
DM
4. Pristup memoriji
podatak
IR5
O.C.
ra
Z5
5. Upis rezultata

st R[ra],R[rb],c2

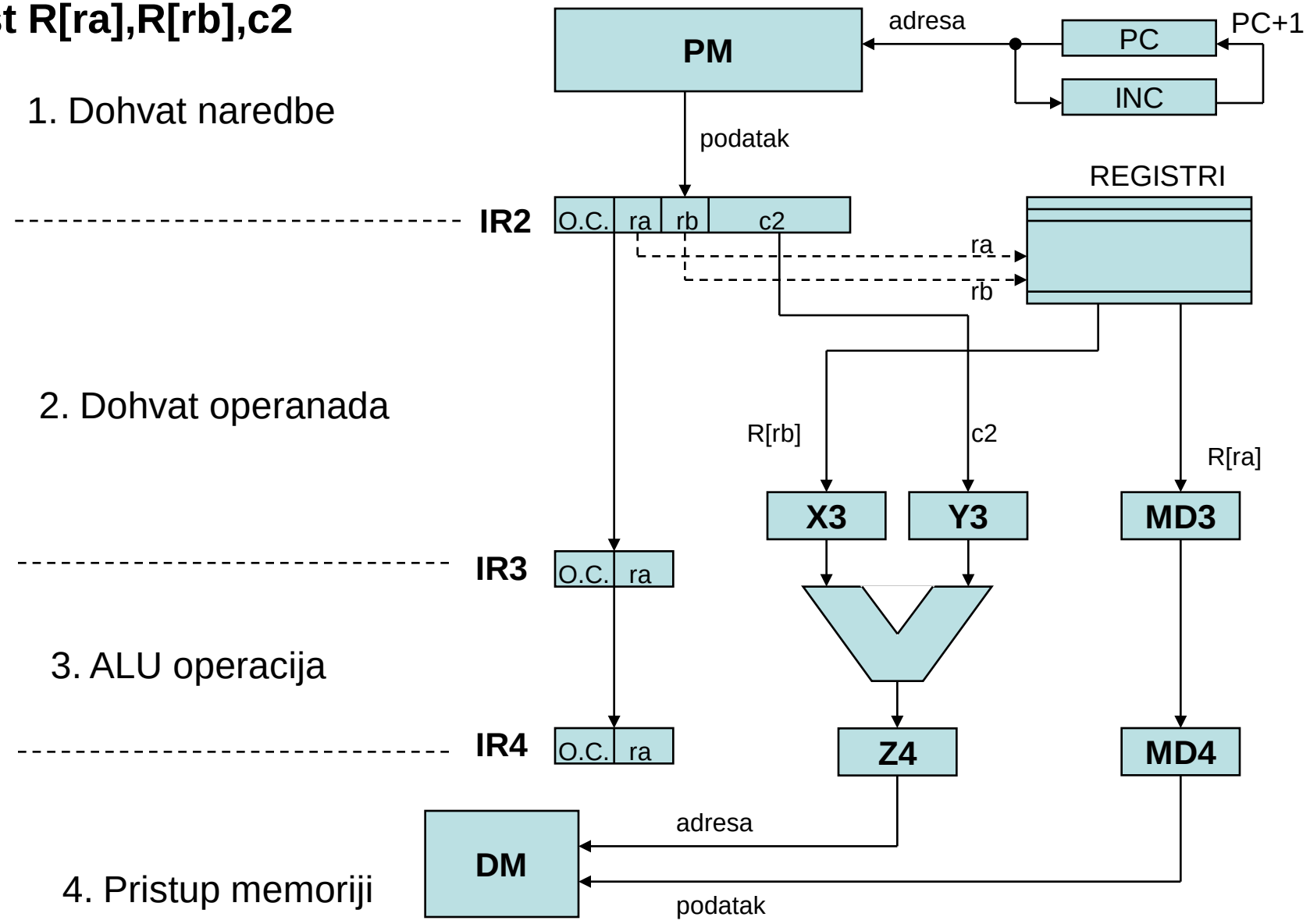
1. Dohvat naredbe

2. Dohvat operanada

3. ALU operacija

4. Pristup memoriji

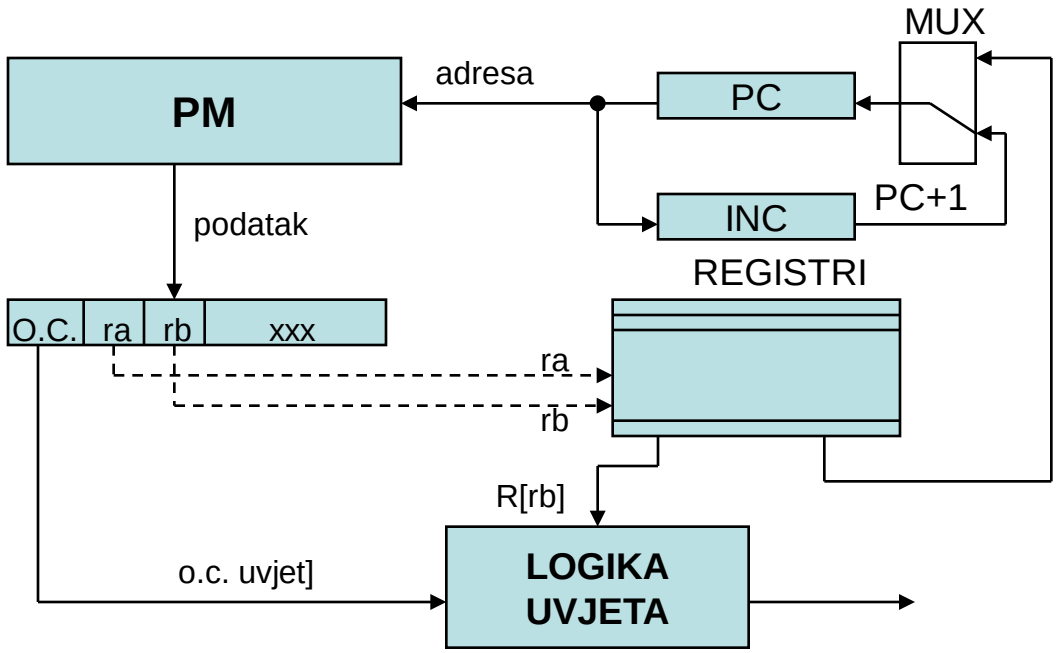
5. Upis rezultata



brxx R[ra],R[rb]

1. Dohvat naredbe

---------- **IR2**



2. Dohvat operanada

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3. ALU operacija

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4. Pristup memoriji

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5. Upis rezultata