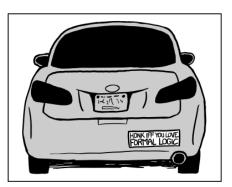
Practical 3

After completion of this tutorial you should be able to perform algebraic manipulations on logic functions, synthesize circuits from logic functions, extract logic behaviour truth tables, and implement small logic functions in C.

Praktika 3

Na afloop van die tutoriaal behoort u vertroud te wees met die algebraïese manupulasie van logiese funksies, sintese van logiese stroombane, beskrywing van logiese gedrag vanaf waarheidstabelle en die implementering van klein logiese funksies in C.



Please read and go through the following:

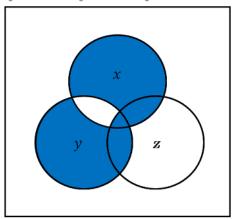
- This SunLearn quiz contains all the questions related to the practical session.
- Every individual need to complete the quiz and serves as an indication of attendance of the session.
- The quiz is open for the entire practical session and will automatically close and submit after the defined deadline.
- The are multiple types of questions, some will give feedback whether your answer is correct and others the feedback might be deferred.
- These questions serve as preparation for the main assessments and the type of questions and their interface are a good indication of what to expect.

Lees asseblief en gaan deur die volgende:

- Hierdie SunLearn vasvra bevat al die vra wat verband hou met die praktiese sessie.
- Elke individu moet die vasvra voltooi en herdie dien as indikasie van bywoning van die sessie.
- Die vasvra is oop vir die volledige praktika sessie en sal automaties sluit en die antwoorde indien na die gestelde slutingstyd.
- Daar is 'n paar tipes vrae, sommige van hulle sal terugvoer gee of die antwoord korrek is en ander is die terugvoer afgesit.
- Hierdie vrae dien as voorbereiding vir die hoof-assesserings en die tipe vrae en hul koppelvlak is 'n goeie indikasie wat om te verwag.

Question 1 Answer saved Marked out of 100

Identify the logical expression that corresponds to the following Venn diagram (blue = true): Identifiseer die logiese uitdrukking wat met die volgende Venn-diagram ooreenstem (blou = waar):



Select one:

a.
$$x\cdot ar{y}\cdot ar{z}+ar{x}\cdot y\cdot z+x\cdot ar{y}\cdot z+ar{x}\cdot y\cdot ar{z}+x\cdot y\cdot ar{z}$$

$$\bigcirc$$
 b. $x \cdot y \cdot ar{z}$

$$\bigcirc$$
 c. $x+y+ar{z}$

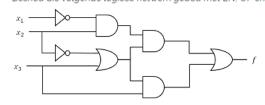
Od.
$$ar{x}\cdotar{y}\cdot z + x\cdot y\cdotar{z} + x\cdot y\cdotar{z}$$

$$\bigcirc$$
 e. $x \cdot y + \overline{x \cdot y} + \overline{z}$

Question 1

Answer saved Marked out of 100

Consider the following logical network built from AND, OR and NOT gates: Beskou die volgende logiese netwerk gebou met EN, OF en NIE hekke:



Which of the following is the simplest logical expression for the above network? Watter een van die volgende is die eenvoudigste logiese uitdrukking for die netwerk hierbo?

Select one

$$\bigcirc$$
 a. $f=(\overline{x}_1\cdot x_2)\cdot(\overline{x}_2+x_3)+(x_3\cdot(\overline{x}_2+x_3))$

This network cannot be expressed as a logic function Die netwerk kan nie as 'n logiese funksie uitgedruk word nie.

$$\bigcirc$$
 C. $f = (\overline{x}_1 \cdot x_2) \cdot (x_2 + \overline{x}_3) + (x_3 \cdot (x_2 + \overline{x}_3))$

$$lacksquare$$
 d. $f=x_{2}$

$$\bigcirc$$
 e. $f=x_1+x_2\cdot \overline{x_3}$

Clear my choice

Question 1

Answer saved Marked out of Find the dual of the logical function:

Bepaal die duaal van die logiese funksie:

$$f=x_1ar{x}_2\cdot(ar{x}_1+x_3)+\overline{x_3ar{x}_2}$$

Select one:

f is a dual of itself $m{f}$ is 'n duaal van homself

$$lackbox{igle b}. \ (x_1+ar{x}_2+ar{x}_1x_3)\cdot \overline{x_3+ar{x}_2}$$

$$\bigcirc$$
 C. $ar{x}_1+x_2+x_1ar{x}_3\cdot\overline{x_3+ar{x}_2}$

$$\bigcirc$$
 d. $\overline{x_1ar{x}_2}\cdot(\overline{x}_1+x_3)+x_3ar{x}_2$

$$\bigcirc$$
 e. $x_1ar{x}_2\cdot(x_1+ar{x}_3)+ar{x}_3x_2$

Question 1 Answer saved Marked out of

For the function f represented by the following truth table:

Vir die funksie $m{f}$ what deur die volgende waarheidstabel vertoon word:

х	у	Z	f
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

Find the minimum-cost product-of-sums expression.

(Hint: Begin with the product-of-maxterms and use algebraic manipulation.)

Bepaal die minimum-koste produk-van-somme uidtrukking.

(Wenk: begin met die produk-van-maksterme en gebruik algebraïese manipulering.)

Select one:

$$(x+y+z)(ar x+y+z)(ar x+y+ar z)$$

$$\cap$$
 b. $f=(x+y+z)(ar{x}+y)$

$$(\bar{x}+\bar{y}+\bar{z})(x+\bar{y}+\bar{z})(x+\bar{y}+z)$$

O d.
$$f=(x+ar{z})(x+ar{y}+z)(ar{x}+ar{y})$$

$$ullet$$
 e. $f=ar{y}(x+ar{z})$

Question 1

Answer saved Marked out of 100 Select the combination of minterms that equal the sum-of-minterms for the function: Kies die kombinasie van minterme wat gelyk is aan die som-van-minterme vir die funksie:

$$f = \overline{xar{y}} + y \cdot z \cdot x$$

(Hint: Use a truth table.)

(Wenk: gebruik 'n waarheidstabel.)

Select one or more:

$$m_0=ar xar yar z$$

$$m_1=ar xar yz$$

$$m_2=\bar xy\bar z$$

$$m_3=ar xyz$$

$$m_4=xar{y}ar{z}$$

$$m_5 = x \bar{y} z$$

$$m_6=xyar{z}$$

$$m_7 = xyz$$

Question 1

Answer saved Marked out of 100

Determine the SOP function that implements the truth table below and simplify it algebraically. Use the graphical method (using Bubble pushing and De Morgan's theorem) to change the function so that only NAND

Bepaal die SOP funksie wat die onderstaande waarheidstabel implementeer en vereenvoudig dit algebraies. Gebruik die grafiese metode (met borrelstoting en De Morgan se stelling) om die funksie te verander sodat slegs NENhekke benodig word.

x1	х2	хЗ	f
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

$$f=\overline{(x_2)(\overline{ar{x}_1}ar{x}_3)}$$

$$f=(x_2)(\overline{\bar{x}_1}\bar{x}_3)$$

$$f=(x_1)(\overline{\bar{x}_2}\bar{x}_3)$$

$$f=\overline{(x_2)(\overline{\bar{x}_1}x_3)}$$

$$f=\overline{(x_1)(\overline{ar{x}_2}ar{x}_3)}$$

■ Q7_i Version 1 (latest)

Information

Assume that a large room has three doors and one light in the center of the room. Three latching switches (one at each door) are provided to switch the light on and off. Changing the state of any one of the switches, should change the state of the light. Design a system that takes the three latching switches as inputs and provide an output signal for the light switching circuit.

Aanvaar dat a groot kamer drie deure het en een lig in die middel van die kamer. Drie skakelaars (een by elke deur) is beskikbaar om dit lig aan en af te skakel (skakelaars bly in toestand waarin hulle gelos word). As enige van die skakelaars se toestand verander, dan behoort die lig se toestand ook te verander. Ontwerp 'n stelsel wat drie skakelaars neem as intrees en 'n uittreesein vir die ligskakelstroombaan verskaf.

Q7_1 Version 1 (latest)

Question 1

Answer saved Marked out of Populate the truth table of the required system. Take x1, x2, and x3 as the input and y as the output of the

Voltooi die waarheidstabel van die verlangde stelsel. Neem x1, x2, en x3 as die intree en y as die uitree van die stelsel

X ₁	Х2	Хз	У
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Question 7 (c) Version 1 (latest)

Question 1

Not complete Marked out of Capture the behavior of the function in a C-function with prototype (HINT: Sum-of-product or Product-of-sum): Implementeer die gedrag van die funksie in 'n C-funksie met die prototipe (HINT: Som-van-produkte of Produk-

bool LightStatus(bool x1, bool x2, bool x3)

Answer: (penalty regime: 0 %)

Reset answer

```
bool LightStatus (bool x1, bool x2, bool x3)
2 .
3
        bool term1 = (!x1) \&\& (!x2) \&\& (x3);
4
        bool term2 = (!x1) \&\& (x2) \&\& (!x3);
5
        bool term3 = (x1) \&\& (!x2) \&\& (!x3);
        bool term4 = (x1) && (x2) && (x3);
6
        return term1 || term2 || term3 || term4;
8
Check
```

■ Q7 4 Version 1 (latest)

Information

Download the template Digital design file (question.dig) from SunLearn. Use logic gates (Components>Logic) to implement the function using the given inputs and outputs. You can change the number of inputs a logic gate has by right-clicking on it. When you are done, save your file and exit Digital. Open the Practical 3 Digital Quiz and follow the instructions.

Kry die Digital templaat ontwerp leer (question.dig) vanaf SunLearn. Gebruik die logiese hekke (Components>Logic) om die funksie te implementeer met die gegewe intrees en uitrees. Jy kan die aantal intrees van 'n logiese hek verander deur regs te kliek daarop. Waaneer jy klaar is, stoor jou leer en maak Digital toe. Maak die "Practical 3 Digital Quiz" oop en volg die instruksies.

■ Digital Submission Version 1 (latest)

Question 1

Not yet answered Marked out of 100

Download the template Digital design file (quesion.dig) from SUNLearn. Use logic gates (Components>Logic) to implement the function using the given inputs and outputs. You can change the number of inputs a logic gate has by right-clicking on it. When you are done, save your file and exit Digital. Right click on the file containing your answer, click on "Open With" and then select Notepad. Press CTRL+A to highlight the entire file. Press CTRL+C to copy the file text and then CTRL+V to paste it into the text box.

This text describes your circuit. We will use this text to run your circuit and test it according to the expected behaviour. Ensure that the entire description of your circuit is pasted successfully otherwise your circuit will not be able to run during the tests.

Kry die Digital templaat ontwerp leer (question.dig) vanaf SunLearn. Gebruik die logiese hekke (Components>Logic) om die funksie te implementeer met die gegewe intrees en uitrees. Jy kan die aantal intrees van 'n logiese hek verander deur regs te kliek daarop. Waaneer jy klaar is, stoor jou leer en maak Digital toe. Regs-kliek op die leer wat ju antwoord bevat, en selekteer "Open With" en selekteer dan Notepad. Druk CTRL+A om die hele inhoud van die leer te selekteer. Druk dan CTRL+C om die seleksie te kopieer en dan CTRL+V om dit in die teks-invoer te plak.

Hierdie teks beskryf jou stroombaan. Ons gaan hierdie beskrywing van jou baan gebruik en dit toets teenoor die verlangde gedrag. Maak seker dat die volledige beskrywing van jou stroombaan suksesvol in die teksveld is. anders gaan ons nie jou stroombaan kan simuleer nie.