edaboard.com
An EE World Online Resource

| Forum | Activity Stream | Search | Help | Rules | Groups | B___ ? |

EDA Resources   ...vered Posts   FAQ   Forum Actions   Community   Quick Links   Advanced Search

🏠 Forum   Analog Design   Analog Circuit Design

How to set the properties of the ideal adc modle in Cadence?

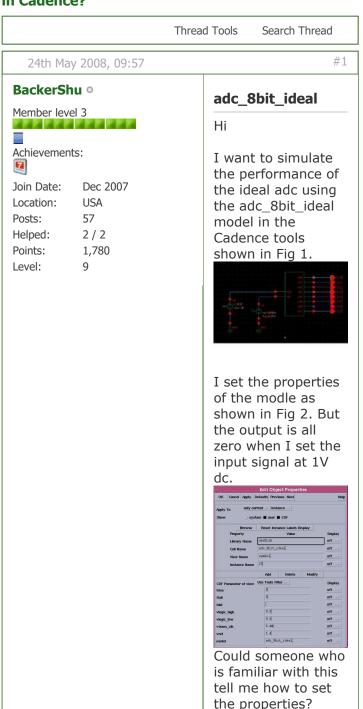### Part and Inventory Search ▲

Top Helped / Month ▲

KlausST (7), Easy peasy (3), FvM (3), betwixt (3), TrickyDicky (2)

Welcome to EDABoard.com ▲

Design Resources ▲

Sponsor ▲

💬 New Posts ▲

Def-Stan 59-411 proble with MOVs don't dissipate energy (1)
Where to get schematics about SCRs full-wave triggering circuits ? (69)
Op amp integrator formula (1)
water flow detection in a pipe (6)
How to trace the original of netlist gates back to the original RTL description (2)
Cw decoder by ik3oil (6)
Cascoding the two stage folded class AB ampliifer (8)
Low price but good quality PCB manufacturer in China (2)
PIC16F877 interface with ds1307 ...RTC running 10 sec slow (11)
Off-the shelf transformer for push pull converter (9)
Tool for transforming python code to FPGA for Machine Learning (14)
How good is this omnidirectional radiation pattern? (0)
My test pattern is not like should be! Somebody can help me please? (9)
MATLAB funstion "dsolve" gives complex terms (1)

+ Post New Thread

How to set the properties of the ideal adc modle in Cadence?

Results 1 to 4 of 4

| Thread Tools | Search Thread |

24th May 2008, 09:57   #1

BackerShu ○

Member level 3

Achievements:

Join Date:	Dec 2007
Location:	USA
Posts:	57
Helped:	2 / 2
Points:	1,780
Level:	9

adc_8bit_ideal

Hi

I want to simulate the performance of the ideal adc using the adc_8bit_ideal model in the Cadence tools shown in Fig 1.

I set the properties of the modle as shown in Fig 2. But the output is all zero when I set the input signal at 1V dc.

Could someone who is familiar with this tell me how to set the properties?

In Circuit Tester-Standards,Guidelines,Literature or Recommendation (0)

UC3845 ETD29 transformer PWM signal (23)

INTEST, EXTEST and BYPASS modes without wrapper in DFT (4)

Need help for ADS equation reprentation... (2)

Enable circuit for backup battery boost converter (3)

How to estimate power consumption at the IP design level? (4)

Antenna design with 'Air Substrate' (2)

how to select capacitor for ruhmkorff coil? (10)

Temprature sensors. LM35 vs MCP9700 (12)

LDO & BUck Converter (3)

Solving issues in my PCB grounding (2)

Top Posters ⌃

FvM (46361), KlausST (16635), betwixt (13817), BradtheRad (13194), alexan_e (11895)

Recently Updated Groups ⌃

DFM Coaching, FPGA designers, PCB Reversing Engineering, FREE COURSES, PCB design, Electrical and electronics, Power Electronics, VLSI & DESIGN TECHNOLOGY, community of Engineers, Verilog HDL

Sponsor ⌃

Top Experience Points ⌃

FvM (265341), betwixt (83946), KlausST (81918), IanP (74061), bigdogguru (69512)

EE World Online ⌃

Electrical Engineering News and Products
Electronics Engineering Resources, Articles, Forums, Tear Down Videos and Technical Electronics How-To's
Apr 02 2020, 10:25 am : Oscillator modules incorporate safeguards against GNSS signal loss
Apr 02 2020, 9:58 am : Reliability report covers gallium-nitride power semiconductors
Apr 02 2020, 9:42 am : Carlo Gavazzi laser time of flight photoelectric sensors with IO-Link
Apr 02 2020, 7:21 am : Test bench checks out automotive radomes
Apr 01 2020, 5:36 pm : Three-phase built-in filter with neutral line suited for tight spaces

---

Thank you!

Regards!

7th December 2010, 10:25　　　　　　　　#2

whitemystic ○

Newbie level 3

Join Date:	Dec 2010
Posts:	3
Helped:	0 / 0
Points:	374
Level:	4

Re: How to set the properties of the ideal adc modle in Cadence?

hi
do u know the answer now? i am also simulatind an ideal ADC in cadence and m not able to do it..

7th December 2010, 11:11　　　　　　　　#3

akhilrathore ○

Member level 1

Join Date:	Jan 2008
Location:	bangalore
Posts:	33
Helped:	4 / 4
Points:	1,275
Level:	8

Re: How to set the properties of the ideal adc modle in Cadence?

1 members found this post helpful.

Apr 01 2020, 4:31 pm : How can the world solve the global ventilator shortage problem? Pt 3
Apr 01 2020, 3:12 pm : Hack the Crisis: Harting increases cable assembly supplies in response to COVID-19 pandemic
Apr 01 2020, 10:07 am : Controllers manage 8-W high-voltage converters
Apr 01 2020, 9:51 am : Chip design software promotes high-throughput camera and display SoC development
Apr 01 2020, 12:39 am : How can the world solve the global ventilator shortage problem? Pt 2

Design Fast ⌃

7th December 2010, 13:11   #4

whitemystic ○

Newbie level 3

Join Date: Dec 2010
Posts: 3
Helped: 0 / 0
Points: 374
Level: 4

Re: How to set the properties of the ideal adc modle in Cadence?

i get a error "Error found durin netlisting..the netlist may be corrupt or may not be generated at all" Could u provide a screenshot of the schematic too? As I am confused wat to connect at the output ..

---------- Post added at 13:11 ---------- Previous post was at 13:08 ----------

this happens during simulation.. i am not able to simulate it. :(

+ Post New Thread

Quick Navigation | **Analog Circuit Design** | **Top** |

« Previous Thread | Next Thread »

Similar Threads

ideal switch in Cadence (16)
Ideal Op amp differential in cadence (15)
Simulation of the ideal DAC in Cadence (1)
Function properties window problem in Cadence 5.033 (4)
how to use ideal balun in cadence (2)

| -- Edaboard Classic   ▼ | **Site Map   Contact Us   Forum for Electronics   Privacy Statement   Terms of Service   Top**

vBulletin Optimisation by vB Optimise.

--[[ ]]--