

Verification of truth table for NOR gate



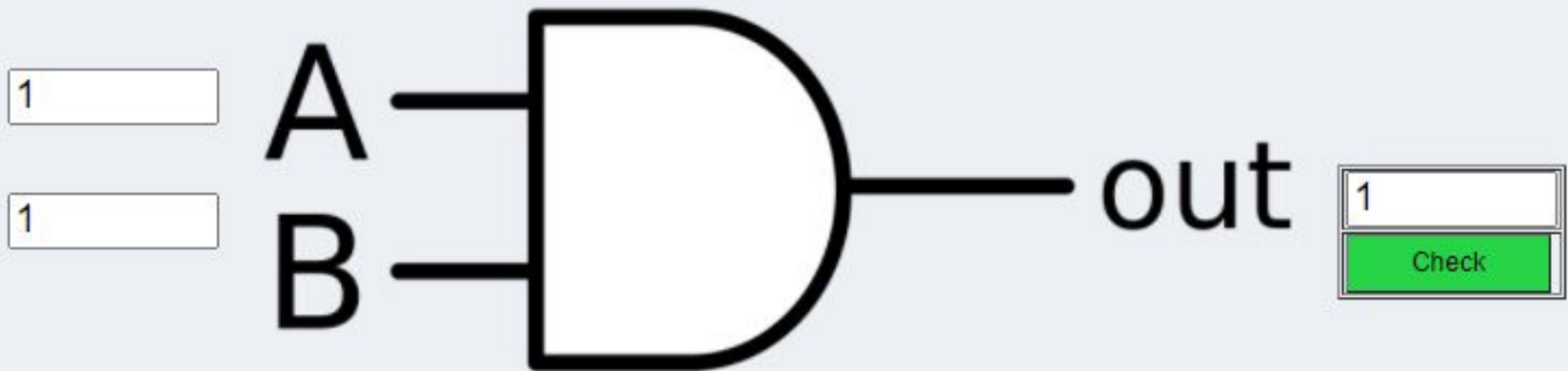
TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	0	Correct

Reset

Verification of truth table for AND gate



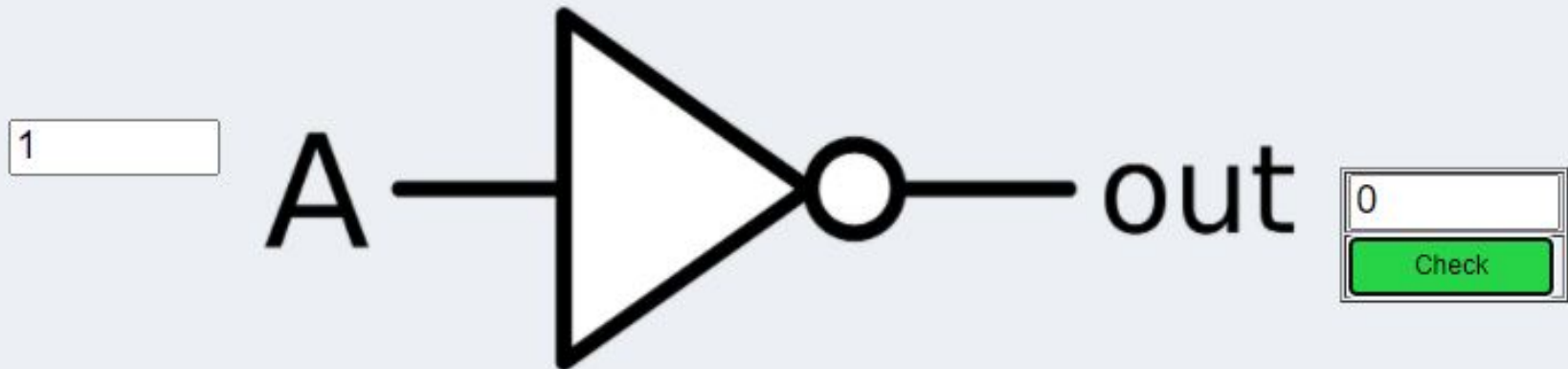
TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	1	Correct

Reset

Verification of truth table for NOT gate



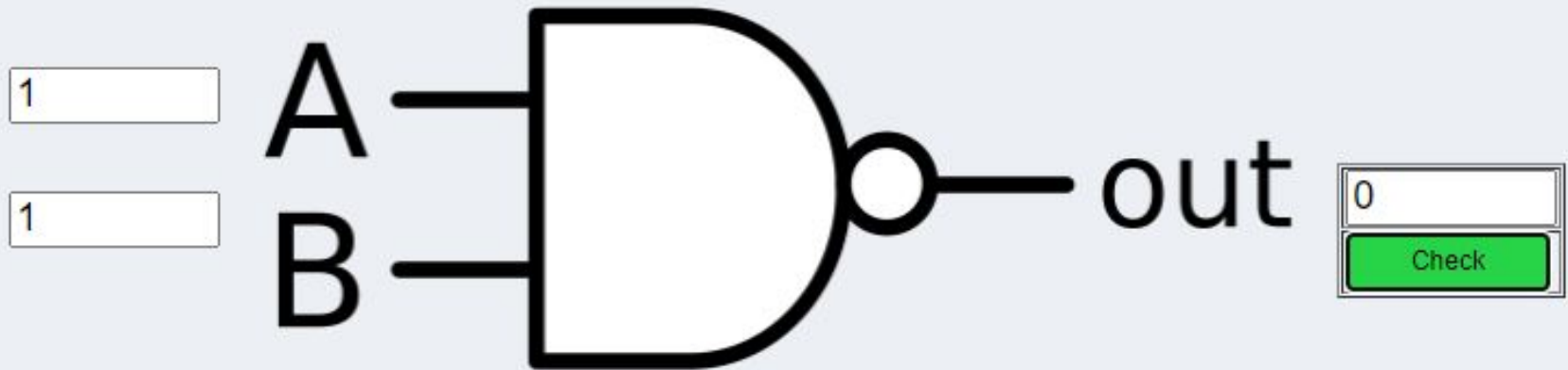
TRUTH TABLE

Print

Serial No.	A	Output	Remarks
1	0	1	Correct
2	1	0	Correct

Reset

Verification of truth table for NAND gate



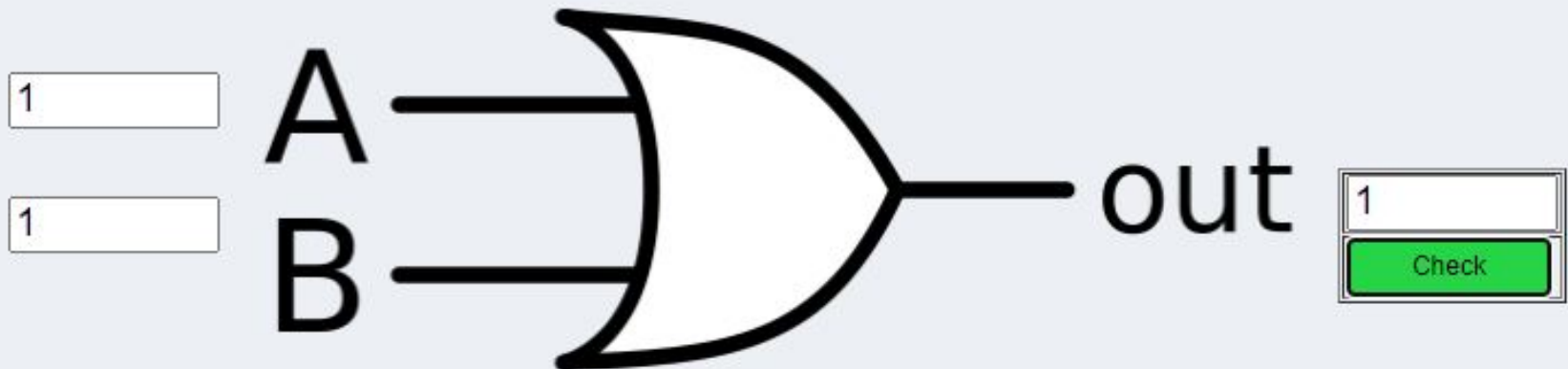
TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	0	Correct

Reset

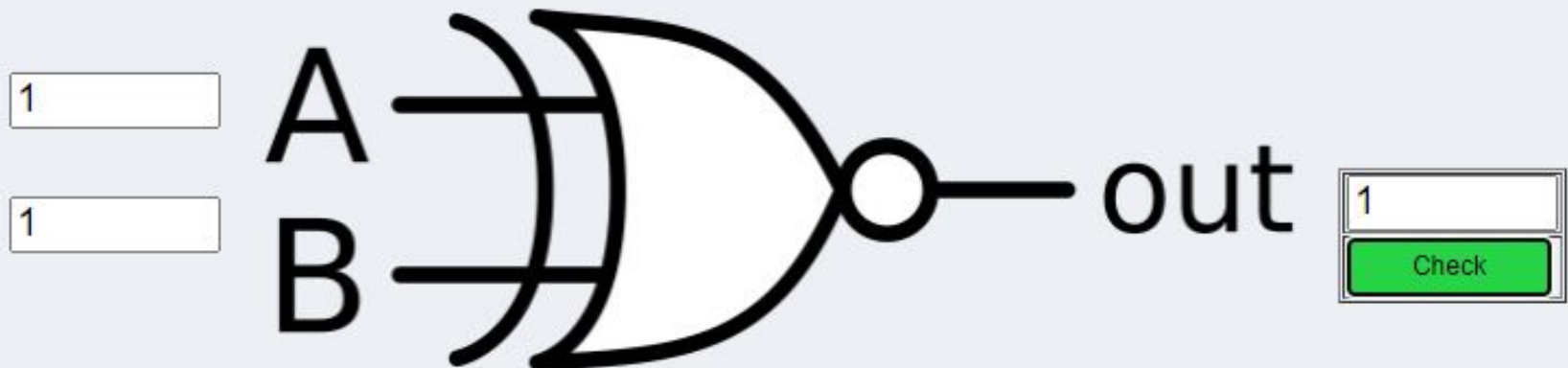
Verification of truth table for OR gate



TRUTH TABLE

Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	1	Correct

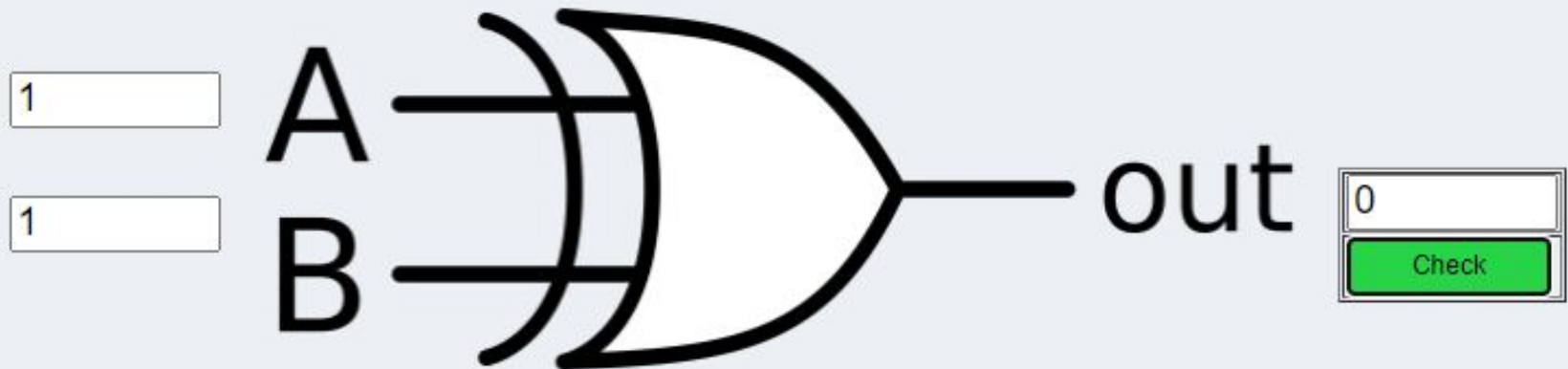
Verification of truth table for XNOR gate



TRUTH TABLE

Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	1	Correct

Verification of truth table for XOR gate



TRUTH TABLE

Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	0	Correct