**QOSF Screening Task 3** We will be using qiskit.QuantumCircuit as our circuit object throughout the notebook. 0. Task Description Please write a simple compiler – program, which translates one quantum circuit into another, using a restricted set of gates. You need to consider just the basic gates for the input circuit, such as (I, H, X, Y, Z, RX, RY, RZ, CNOT, CZ). The output circuit should consist only from the following gates: RX, RZ, CZ. In other words, each gate in the original circuit must be replaced by an equivalent combination of gates coming from the restricted set (RX, RZ, CZ) only. For example, a Hadamard gate after compilation looks like this: RZ(pi/2) RX(pi/2) RZ(pi/2) Analyze what's the overhead of the compiled program compared to the original one and propose how to improve it. What we mean by overhead is the following: by replacing all the initial gates with the restricted set of gates given in the problem, you will see that the resulting circuit is much more involved than the original one. This is what we called the overhead, and you may think about how to treat this problem, i.e. you could try to simplify as much as possible the resulting circuit. 1. Gate Identities Rotational operators are defined as:  $egin{aligned} R_x( heta) &= egin{pmatrix} cos(rac{ heta}{2}) & -isin(rac{ heta}{2}) \ -isin(rac{ heta}{2}) & cos(rac{ heta}{2}) \end{pmatrix} \ R_y( heta) &= egin{pmatrix} cos(rac{ heta}{2}) & -sin(rac{ heta}{2}) \ sin(rac{ heta}{2}) & cos(rac{ heta}{2}) \end{pmatrix} \end{aligned}$  $R_z( heta) = \left(egin{array}{cc} e^{-irac{ heta}{2}} & 0 \ 0 & e^{irac{ heta}{2}} \end{array}
ight)$ It's easy to observe the following identities:  $X = egin{pmatrix} 0 & 1 \ 1 & 0 \end{pmatrix} \propto R_x(\pi) \qquad Y = egin{pmatrix} 0 & -i \ i & 0 \end{pmatrix} \propto R_y(\pi) \qquad Z = egin{pmatrix} 1 & 0 \ 0 & -1 \end{pmatrix} \propto R_z(\pi)$ where  $\propto$  denotes "proportional to", or "equivalent up to global phase". With the famous commutation relations of X, Y and Z, we can also observe that:  $\sigma_i \sigma_j \propto \sigma_j \sigma_i$ where  $\sigma_i, \sigma_j \in \{I, X, Y, Z\}$ We can also easily express S in terms of  $R_z(\theta)$ :  $S = \left(egin{array}{cc} 1 & 0 \ 0 & i \end{array}
ight) \propto R_z(rac{\pi}{2})$ Therefore, we have a way to express  $R_y(\theta)$  in terms of  $R_x(\theta)$  and  $R_z(\theta)$  by using the Clifford property of S gate:  $R_{u}(\theta) = SR_{x}(\theta)S^{\dagger}$  $\propto R_z(\frac{\pi}{2})R_x(\theta)R_z(-\frac{\pi}{2})$ and also a way to express Y:  $Y \propto R_u(\pi)$  $\propto R_z(\frac{\pi}{2})R_x(\pi)R_z(-\frac{\pi}{2})$ We can also express H in terms of  $R_x(\theta)$  and  $R_z(\theta)$ :  $H = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}$  $\propto R_y(\frac{\pi}{2})R_z(\pi)$  $\propto R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})$ Therefore, transforming CX to CZ,  $R_x(\theta)$  and  $R_z(\theta)$  is also realized using the Clifford property of H gate:  $CX_{0.1} = H_1CZ_{0.1}H_1$  $\propto [R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})]_1 CZ_{0,1}[R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})]_1$  $\propto [R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})]_1 CZ_{0,1}[R_z(\pi)R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})]_1$ We can further observe X gate is equivalent to a 180° rotation around X-axis (or simple X gate) sandwiched by two 90° around Z-axis: X = HZH $\propto R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})ZR_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})$  $\propto R_z(\frac{\pi}{2})Rx(\pi)R_z(\frac{\pi}{2})$  $\propto R_z(\frac{\pi}{2})XR_z(\frac{\pi}{2})$ By symmetry, we have the other 5 equations:  $X \propto R_y(\frac{\pi}{2})XR_y(\frac{\pi}{2})$  $Y \propto R_x(\frac{\pi}{2})YR_x(\frac{\pi}{2})$  $Y \propto R_z(\frac{\pi}{2})YR_z(\frac{\pi}{2})$  $Z \propto R_x(\frac{\pi}{2})ZR_x(\frac{\pi}{2})$  $Z \propto R_y(\frac{\pi}{2})ZR_y(\frac{\pi}{2})$ Therefore, a new way with one less gate to express Y in terms of  $R_x(\theta)$  and  $R_z(\theta)$  is:  $Y \propto R_z(\frac{\pi}{2})R_x(\pi)R_z(-\frac{\pi}{2})$  $\propto R_z(\frac{\pi}{2})R_z(\frac{\pi}{2})R_x(\pi)R_z(-\frac{\pi}{2})R_z(\frac{\pi}{2})$  $\propto R_z(\pi)R_x(\pi)$ Therefore, we have all the identities used for circuit rewriting. Next we will mention several identities that will be useful for circuit optimization. The two very obvious ones are gate merging:  $R_x( heta_1)R_x( heta_2) = R_x( heta_1 + heta_2) \qquad R_z( heta_1)R_z( heta_2) = R_z( heta_1 + heta_2)$ Two of the equations mentioned before can also be used during circuit optimization. One is:  $R_x(\pi)R_z(\pi) \propto R_z(\pi)R_x(\pi)$ which can be used to change order and seek for gate merging. The other is:  $R_z(\frac{\pi}{2})R_x(\pi)R_z(\frac{\pi}{2}) \propto R_x(\pi)$  $R_x(\frac{\pi}{2})R_z(\pi)R_x(\frac{\pi}{2}) \propto R_z(\pi)$ which can be used to directly reduce the number of gates. Another identity can be found by sandwiching  $R_z(\theta)$  with H gate and expand it into a series of  $R_z(\theta)$  and  $R_x(\theta)$ :  $R_x(\theta) = HR_x(\theta)H$  $\propto R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})R_z(\theta)R_z(\frac{\pi}{2})R_x(\frac{\pi}{2})R_z(\frac{\pi}{2})$  $=R_z(rac{\pi}{2})R_x(rac{\pi}{2})R_z( heta+\pi)R_x(rac{\pi}{2})R_z(rac{\pi}{2})$ for wich we can do the same for  $R_z(\theta)$  and reverse the order to get two very useful identities for circuit optimization:  $R_z(\alpha)R_x(\frac{\pi}{2})R_z(\theta)R_x(\frac{\pi}{2})R_z(\beta) \propto R_z(\alpha-\frac{\pi}{2})R_x(\theta-\pi)R_z(\beta-\frac{\pi}{2})$  $R_x(\alpha)R_z(\frac{\pi}{2})R_x(\theta)R_z(\frac{\pi}{2})R_x(\beta) \propto R_x(\alpha-\frac{\pi}{2})R_z(\theta-\pi)R_x(\beta-\frac{\pi}{2})$ It's also worth mentioning that  $R_z(\theta)$  can freely move across CZ on both control and target qubit:  $CZ_{0.1}R_z(\theta)_0 \propto R_z(\theta)_0 CZ_{0.1}$  $CZ_{0,1}R_z(\theta)_1 \propto R_z(\theta)_1 CZ_{0,1}$ and that concludes all the gate identities we'll be using throught the notebook. 2. Initialization This part of the notebook intializes packages and defines global variables. In [1]: #initialization import math import numpy as np import qiskit from qiskit import execute, Aer from qiskit.circuit.library import IGate, XGate, YGate, ZGate, HGate, SGate, RXGate, RYGate, RZGate, CX from qiskit.quantum\_info import Statevector import random **from tabulate import** tabulate pi = np.pi GATE\_SET = {'i', 'h', 'x', 'y', 'z', 'rx', 'ry', 'rz', 'cx', 'cz'} BASIS GATE SET = {'rx', 'rz', 'cz'} I = IGate().to matrix() X = XGate().to\_matrix() Y = YGate().to\_matrix() Z = ZGate().to matrix()H = ZGate().to\_matrix() S = ZGate().to\_matrix() RX = lambda t:RXGate(t).to\_matrix() RY = lambda t:RYGate(t).to matrix() RZ = lambda t:RZGate(t).to\_matrix() CX = CXGate().to\_matrix() CZ = CZGate().to\_matrix() c:\python37\lib\site-packages\requests\\_\_init\_\_.py:91: RequestsDependencyWarning: urllib3 (1.25.10) o r chardet (3.0.4) doesn't match a supported version! RequestsDependencyWarning) 3. Helper Functions This part of the notebook defines several helper functions. In [2]: # Helper functions def presice\_matrix(gates): # return the matrix of a list of gates in the precision of machine epsilon if len(gates) == 0: return 1 return gates[-1].to matrix().dot(preside matrix(gates[:-1])) def remove\_global\_phase(mat): # return the matrix with its global phase removed # global phase = the phase of the first non-zero term in first row for i in range(len(mat[0])): if np.round(mat[0][i], 3) != 0: global\_phase\_angle = np.angle(mat[0][i]) break global\_phase = np.e\*\*(global\_phase\_angle\*1j) return mat/global\_phase def matrix(gates, precision=3): # return the matrix of a list of gates in certain precision if precision == None: return precise matrix(gates) else: return np.round(presice matrix(gates), precision) def matrix\_ng(gates, precision=3): # return the matrix of a list of gates without global phase removed in certain precision if len(gates) == 0: mat = presice matrix(gates) mat = remove\_global\_phase(mat) if precision == None: return mat else: return np.round(mat, precision) def equal(gates1, gates2): # return if two lists of gates are equal return (matrix(gates1) == matrix(gates2)).all() def equal ng(gates1, gates2): # return if two lists of gates are equal up to global phase return (matrix\_ng(gates1) == matrix\_ng(gates2)).all() def equal mat(mat1, mat2): # return if two matrices are equal return (mat1 == mat2).all() def equal mat ng(mat1, mat2): # return if two lists of gates are equal up to global phase return (remove global phase(mat1) == remove global phase(mat2)).all() def commute(gates1, gates2): # return if two lists of gates commute return equal(gates1+gates2, gates2+gates1) def commute\_ng(gates1, gates2): # return if two matrices commute up to global phase, or  $AB = \exp(i * theta) BA$ return equal\_ng(gates1+gates2, gates2+gates1) def commute mat(mat1, mat2): # return if two matrices commute return equal\_mat(mat1.dot(mat2), mat2.dot(mat1)) def commute mat ng(mat1, mat2): # return if two matrices commute up to global phase, or  $AB = \exp(i * theta) \; BA$ return equal mat ng(mat1.dot(mat2), mat2.dot(mat1)) def gate to str(gate): # return info about a gate in str format return gate.name if len(gate.params) == 0 else f'{gate.name}({np.round(gate.params[0]/pi, 3)}pi)' def equal circuit(qc1, qc2): # return if two circutis are equal backend sim = Aer.get backend('unitary simulator') job sim = execute([qc1, qc2], backend sim) result sim = job sim.result() unitary1 = result sim.get unitary(qc1) unitary2 = result sim.get unitary(qc2) return np.allclose(unitary1, unitary2) def equal\_circuit\_ng(qc1, qc2): # return if two circuits are equal up to global phase s1 = Statevector.from instruction(qc1) s2 = Statevector.from instruction(qc2)return s1.equiv(s2) def equal test(gates1, gates2): # print if two set of gates are equal or euqal up to global phase print(f'{[gate to str(g) for g in gates1]} vs {[gate to str(g) for g in gates2]}') print(f'equal: {equal(gates1, gates2)}') print(f'equal up to global phase: {equal ng(gates1, gates2)}') print() def commute\_test(gates1, gates2): # print if two set of gates commute or commute up to global phase print(f'{[gate to str(g) for g in gates1]} vs {[gate to str(g) for g in gates2]}') print(f'commute: {commute(gates1, gates2)}') print(f'commute up to global phase: {commute\_ng(gates1, gates2)}') print() 4. Identity Verification We then use the helper functions defined above to verify some of the gate identies derived in the first section. In [3]: gates1 = [XGate()] gates2 = [ZGate()]commute\_test(gates1, gates2) gates1 = [YGate()] gates2 = [XGate(), ZGate()] equal test(gates1, gates2) gates1 = [RZGate(-pi/2), RXGate(pi), RZGate(pi/2)]gates2 = [XGate(), ZGate()] equal\_test(gates1, gates2) gates1 = [XGate()] gates2 = [RXGate(pi)] equal\_test(gates1, gates2) gates1 = [HGate()] gates2 = [RZGate(pi), RYGate(pi/2)]equal\_test(gates1, gates2) gates1 = [HGate()] gates2 = [RZGate(pi/2), RXGate(pi/2), RZGate(pi/2)]equal\_test(gates1, gates2) gates1 = [RYGate(1.2345)]gates2 = [RZGate(-pi/2), RXGate(1.2345), RZGate(pi/2)]equal\_test(gates1, gates2) gates1 = [RXGate(5.4338)]gates2 = [RXGate(2.2922), RXGate(pi)] equal\_test(gates1, gates2) gates1 = [RXGate(1.2345)]gates2 = [RZGate(pi/2), RXGate(pi/2), RZGate(1.2345+pi), RXGate(pi/2), RZGate(pi/2)]equal\_test(gates1, gates2) gates1 = [RZGate(1.2345)]gates2 = [RXGate(pi/2), RZGate(pi/2), RXGate(1.2345+pi), RZGate(pi/2), RXGate(pi/2)]equal\_test(gates1, gates2) gates1 = [RYGate(pi/2), XGate(), RYGate(pi/2)]gates2 = [XGate()] equal\_test(gates1, gates2) gates1 = [RZGate(pi/2), XGate(), RZGate(pi/2)]gates2 = [XGate()] equal\_test(gates1, gates2) gates1 = [RXGate(pi/2), YGate(), RXGate(pi/2)]gates2 = [YGate()] equal\_test(gates1, gates2) gates1 = [RZGate(pi/2), YGate(), RZGate(pi/2)]gates2 = [YGate()] equal\_test(gates1, gates2) gates1 = [RXGate(pi/2), ZGate(), RXGate(pi/2)]gates2 = [ZGate()] equal\_test(gates1, gates2) gates1 = [RYGate(pi/2), RZGate(pi), RYGate(pi/2)]gates2 = [RZGate(pi)]equal\_test(gates1, gates2) print(commute\_mat\_ng(np.kron(I, RZ(5.4321)), CZ)) print(commute\_mat\_ng(np.kron(RZ(5.4321), I), CZ)) ['x'] vs ['z'] commute: False commute up to global phase: True ['y'] vs ['x', 'z'] equal: False equal up to global phase: True ['rz(-0.5pi)', 'rx(1.0pi)', 'rz(0.5pi)'] vs ['x', 'z']equal: False equal up to global phase: True ['x'] vs ['rx(1.0pi)'] equal: False equal up to global phase: True ['h'] vs ['rz(1.0pi)', 'ry(0.5pi)'] equal: False equal up to global phase: True ['h'] vs ['rz(0.5pi)', 'rx(0.5pi)', 'rz(0.5pi)'] equal: False equal up to global phase: True ['ry(0.393pi)'] vs ['rz(-0.5pi)', 'rx(0.393pi)', 'rz(0.5pi)'] equal: True equal up to global phase: True ['rx(1.73pi)'] vs ['rx(0.73pi)', 'rx(1.0pi)'] equal: True equal up to global phase: True ['rx(0.393pi)'] vs ['rz(0.5pi)', 'rx(0.5pi)', 'rz(1.393pi)', 'rx(0.5pi)', 'rz(0.5pi)'] equal: False equal up to global phase: True ['rx(0.393pi)'] vs ['rx(0.5pi)', 'rz(0.5pi)', 'rx(1.393pi)', 'rz(0.5pi)', 'rx(0.5pi)'] equal: False equal up to global phase: True ['ry(0.5pi)', 'x', 'ry(0.5pi)'] vs ['x'] equal: True equal up to global phase: True ['rz(0.5pi)', 'x', 'rz(0.5pi)'] vs ['x'] equal: True equal up to global phase: True ['rx(0.5pi)', 'y', 'rx(0.5pi)'] vs ['y'] equal: True equal up to global phase: True ['rz(0.5pi)', 'y', 'rz(0.5pi)'] vs ['y'] equal: True equal up to global phase: True ['rx(0.5pi)', 'z', 'rx(0.5pi)'] vs ['z'] equal: True equal up to global phase: True ['ry(0.5pi)', 'rz(1.0pi)', 'ry(0.5pi)'] vs ['rz(1.0pi)'] equal: True equal up to global phase: True True True 5. Random Circuit Generator This function can generate a random circuit given number of qubits, number of gates and a restricted set of gates. In [4]: def random qc(num q, num g, gate set): # create a random circuit with num q qubits, num g gates, and a restricted set of gates gate set qc = qiskit.QuantumCircuit(num q) for \_ in range(num\_g): g\_name = random.choice(list(gate\_set)) **if** g name[0] == 'c': c\_index, t\_index = random.sample(range(num\_q), 2) getattr(qc, g\_name)(c\_index, t\_index) elif g\_name[0] == 'r': index = random.randint(0, num q-1)param = random.uniform(-2\*pi, 2\*pi)getattr(qc, g\_name) (param, index) index = random.randint(0, num q-1)getattr(qc, g\_name) (index) return qc 6. Circuit Rewriting We rewrite the circuit by replacing each of the gate with their expressions in  $\{R_x, R_z, CZ\}$ . A complete set of rewriting rules: I o Wire $H o R_z(rac{\pi}{2})R_x(rac{\pi}{2})R_z(rac{\pi}{2})$  $X o R_x(\pi)$  $Y o R_u(\pi)$  $Z o R_z(\pi)$  $R_x(\theta) o R_x(\theta)$  $R_y( heta) 
ightarrow R_z(rac{\pi}{2}) R_x( heta) R_z(-rac{\pi}{2})$  $R_z( heta) o R_z( heta)$  $CX_{0,1} 
ightarrow [R_z(rac{\pi}{2})R_x(rac{\pi}{2})]_1 CZ_{0,1}[R_z(\pi)R_x(rac{\pi}{2})R_z(rac{\pi}{2})]_1$  $CZ_{0.1} 
ightarrow CZ_{0.1}$ In [5]: def rewrite(qc): # rewrite the circuit with {Rx, Rz, CZ} only output\_qc = qiskit.QuantumCircuit(qc.num\_qubits) for gate info in qc.data: gate = gate\_info[0] g name = gate.name index\_list = gate\_info[1] if len(index list) == 1: index = index list[0] if g name == 'i': pass elif g name == 'h': output qc.rz(pi/2, index) output\_qc.rx(pi/2, index) output qc.rz(pi/2, index) elif g name == 'x': output qc.rx(pi, index) elif g name == 'y': output qc.rx(pi, index) output qc.rz(pi, index) elif g name == 'z': output qc.rz(pi, index) elif g\_name == 'rx': output\_qc.rx(gate.params[0], index) elif g name == 'ry': output qc.rz(-pi/2, index) output\_qc.rx(gate.params[0], index) output qc.rz(pi/2, index) elif g name == 'rz': output qc.rz(gate.params[0], index) else: c\_index, t\_index = index\_list if g name == 'cx': output qc.rz(pi/2, t index) output\_qc.rx(pi/2, t index) output\_qc.rz(pi/2, t index) output qc.rz(pi/2, t index) output qc.cz(c index, t index) output qc.rx(pi/2, t index) output\_qc.rz(pi/2, t\_index) elif g name == 'cz': output\_qc.cz(c\_index, t\_index) return output\_qc 7. Circuit Optimization (Solving the Overhead Problem) In this section, we will focus on circuit optimization. Our main goal is to reduce both the number of gates and circuit depth, and we will focus on reducing the number of one-qubit gates. I. Gate Cleaning The first way of optimization is to remove gates with parameter values as multiples of  $2\pi$ ; also ensures angles are in the range  $[-\pi,\pi]$ .  $R_x(4\pi) o I \qquad R_z(3\pi) o R_z(\pi)$ The cleaning process will not take place individually, but during and after other processes of optimization. In [6]: def clean gates(qc): # remove all Rx and Rz gates with parameter as mutiples of 2pi # ensure parameters in the range (-pi, pi) output\_qc = qiskit.QuantumCircuit(qc.num\_qubits) num gates = len(gc.data) for i in range(num gates): gate\_info\_i = qc.data[i] index\_list\_i = gate\_info\_i[1] gate\_i = gate\_info\_i[0] g name i = gate i.name if g\_name\_i in {'rz', 'rx'}: param = gate i.params[0] if math.remainder(param, 2\*pi) == 0: pass elif param > pi or param < -pi:</pre> getattr(output\_qc, g\_name\_i) (math.remainder(param, 2\*pi), index\_list\_i[0]) getattr(output\_qc, g\_name\_i)(param, index\_list\_i[0]) elif g name i == 'cz': getattr(output\_qc, g\_name\_i)(\*index\_list\_i) return output\_qc II. Gate Merging The merging process will be the main way to reduce the number of gates  $R_x( heta_1)R_x( heta_2) = R_x( heta_1 + heta_2) \qquad R_z( heta_1)R_z( heta_2) = R_z( heta_1 + heta_2)$ The code also takes into account the commutation property of  $R_z(\theta)$  and CZ:  $CZ_{0,1}R_z(\theta)_0 \propto R_z(\theta)_0 CZ_{0,1}$  $CZ_{0,1}R_z(\theta)_1 \propto R_z(\theta)_1 CZ_{0,1}$ which is very useful to combine two  $R_z(\theta)$  gates across CZ. We will also remove consecutive pairs of CZ gates. Under computational basis CZ gate has no distinction between control and target qubits, and its conjugate transpose is itself, so adjacent pairs can be directly removed:  $CZ_{0,1} = CZ_{1,0}$  $CZ_{0,1}CZ_{1,0} = I$ The code breaks down into two parts: merge\_once and merge\_all. merge\_once searches through the circuit and merge every pair it finds, while merge\_all continues merging gates untill reaching the optimal circuit. In [7]: def merge\_once(qc): # Merge adjacent gates with same type once. output qc = qiskit.QuantumCircuit(qc.num qubits) num\_gates = len(qc.data) check list = [] for i in range(num gates): if i in check\_list: continue gate info i = qc.data[i] index list i = gate info i[1] gate i = gate info i[0] g name i = gate i.name if g\_name\_i in {'rz', 'rx'}: index i = index list i[0] for j in range(i+1, num gates): gate info j = qc.data[j] index list j = gate info j[1] gate\_j = gate\_info\_j[0] g\_name\_j = gate\_j.name if index i not in index list j: continue if g name j == 'cz': if g name i == 'rz': elif g name i == 'rx': getattr(output qc, g name i) (gate i.params[0], index i) elif g name j == g name i: param\_sum = gate\_i.params[0]+gate\_j.params[0] getattr(output qc, g name i) (param sum, index i) check list.append(j) break else: getattr(output qc, g name i)(gate i.params[0], index i) else: getattr(output\_qc, g\_name\_i)(gate\_i.params[0], index\_i) elif g name i == 'cz': for j in range(i+1, num gates): gate info j = qc.data[j] index list j = gate info j[1] gate j = gate info j[0] g\_name\_j = gate\_j.name set i = set(index list i) set j = set(index list j) if set j.intersection(set i) == set(): continue if set i == set j: check list.append(j) break elif g name j == 'rz': pass else: getattr(output\_qc, g\_name\_i)(\*index\_list\_i) else: getattr(output qc, g name i) (\*index list i) return output qc def merge all(qc): # repeart the merging process untill the circuit is optimal old qc = qcnew qc = clean gates(merge once(old qc)) while len(new qc.data) < len(old qc.data):</pre>  $old_qc = new_qc$ new\_qc = clean\_gates(merge\_once(old\_qc)) return new qc III. Swapping  $R_x(\pi)$  and  $R_z(\pi)$ This part uses the property:  $R_x(\pi)R_z(\pi) \propto R_z(\pi)R_x(\pi)$ and tries to switch such pairs found in circuit. Each time it swaps it will merge the gates again, and see if swapping allow more gates to be merged together. The code splits into two parts as well. **swap\_once** swaps one pair each time, and **swap\_and\_merge\_all** is a combination of swapping and merging. In [8]: def swap once(qc, check list=[]): # swap the position of adjacent Rx(pi) and Rz(pi) once output\_qc = qc.copy() num\_gates = len(qc.data) checked = None for i in range(num\_gates): if i in check list: continue gate\_info\_i = output\_qc.data[i] index\_list\_i = gate\_info\_i[1] gate\_i = gate\_info\_i[0] g\_name\_i = gate\_i.name if g\_name\_i not in {'rz', 'rx'}: continue if gate\_i.params[0] == pi: index\_i = index\_list i[0] for j in range(i+1, num\_gates): gate\_info\_j = output\_qc.data[j] index\_list\_j = gate\_info\_j[1] gate j = gate info j[0] g\_name\_j = gate\_j.name if index\_i in index\_list\_j: if len(index\_list\_j) == 1 and g\_name\_i != g\_name\_j: if gate\_j.params[0] == pi: output\_qc.data[i], output\_qc.data[j] = output\_qc.data[j], output\_qc.data[i] checked = iif checked != None: break return output\_qc, checked def swap\_and\_merge\_all(qc): # try if the swap makes further optimization possible. Merge all if possible. current qc = merge all(qc)re\_qc, checked = swap\_once(current\_qc) check\_list = [] while checked != None: mer\_re\_qc = merge\_all(re\_qc) if len(mer\_re\_qc.data) < len(current\_qc.data):</pre> current\_qc = mer\_re\_qc re\_qc, checked = swap\_once(current\_qc) else: check\_list.append(checked) re\_qc, checked = swap\_once(current\_qc, check\_list) return current qc IV. Group Replacement This part finds particular groups of gates appeared in circuit and use two sets of identities to further optimize the circuit. The first is:  $R_z(\frac{\pi}{2})R_x(\pi)R_z(\frac{\pi}{2}) \propto R_x(\pi)$  $R_x(\frac{\pi}{2})R_z(\pi)R_x(\frac{\pi}{2}) \propto R_z(\pi)$ and the second is:  $R_z(lpha)R_x(rac{\pi}{2})R_z( heta)R_x(rac{\pi}{2})R_z(eta) \propto R_z(lpha-rac{\pi}{2})R_x( heta-\pi)R_z(eta-rac{\pi}{2})$  $R_x(\alpha)R_z(\frac{\pi}{2})R_x(\theta)R_z(\frac{\pi}{2})R_x(\beta) \propto R_x(\alpha-\frac{\pi}{2})R_z(\theta-\pi)R_x(\beta-\frac{\pi}{2})$ The funcion find\_next\_n\_gates is a helper function in this section, which finds the next n gates after a given position in a circuit. The main function group\_and\_merge\_all find the 4 particular groups from the above two equations and replace them by their simplified form. The extra 3 cases come from the fact that  $R_z(\alpha)$  commutes with CZ. For example, consider the group:  $R_{z}(\alpha)_{1}CZ_{0,1}[R_{x}(\frac{\pi}{2})R_{z}(\theta)R_{x}(\frac{\pi}{2})R_{z}(\beta)]_{1}$ which can be optimized to:  $R_z(lpha-rac{\pi}{2})_1CZ_{0,1}[R_x( heta-\pi)R_z(eta-rac{\pi}{2})]_1$ so we need to take into account how CZ can be inserted in between the group of gates.

In [9]:	<pre>def find_next_n_gates(qc, i, n):     # find the next n gates after a given gate in a circuit     gate_infos = qc.data     gate_list = []     for j in range(i+1, len(gate_infos)):         set_i = set(gate_infos[i][1])         set_j = set(gate_infos[j][1])         if set_j.intersection(set_i) != set():             gate_list.append(j)         if len(gate_list) == n:             return gate_list</pre>
	<pre>while len(gate_list) != n:         gate_list.append(None)     return gate_list  # 4 lists of replacing rules as tuples # name_tuple_list: search for matched gate names # param_tuple_list: search for matched parameters # r_name_tuple_list: replace macthed gates # r_param_tuple_list: index or parameter replacing rules # number: inherent index_list: index of param_tuple # tuple: inherent parameter: (index of param_tuple, shift)</pre>
	<pre>name_tuple_list = [('rx', 'rz', 'rx'),</pre>
	(None, None, pi/2, None, pi/2, None), (None, pi/2, None, pi/2, None, None), (None, None, pi/2, None, None)]  r_name_tuple_list = [('rz',),
	<pre>r_param_tuple_list = [((1, 0),),</pre>
	<pre># find macthed group of gates and replace according to the lists defined above  for r_index in range(len(name_tuple_list)):     check_set = set()     output_qc = qiskit.QuantumCircuit(qc.num_qubits)     num_gates = len(qc.data)  name_tuple = name_tuple_list[r_index]     param_tuple = param_tuple_list[r_index]</pre>
	<pre>r_name_tuple = r_name_tuple_list[r_index] r_param_tuple = r_param_tuple_list[r_index]  for i in range(num_gates):     if i in check_set:         continue      pos_list = [i]+find_next_n_gates(qc, i, len(name_tuple)-1)     if None in pos_list:</pre>
	<pre>pass else:  i_name_tuple = tuple(qc.data[p][0].name for p in pos_list)</pre>
	<pre>p = r_param_tuple[j] if type(p) == int and n == 'cz':</pre>
	<pre>cntinue  gate_info = qc.data[i] gate = gate_info[0] gate_name = gate.name index_list = gate_info[1]  if gate_name in {'rx', 'rz'}:     getattr(output_qc, gate_name) (gate.params[0], *index_list) elif gate_name == 'cz':     getattr(output_qc, gate_name) (*index_list)</pre>
	qc = merge_all(output_qc)  return output_qc  8. Transpilation and Optimization Tests In this section we will test our transpilation and optimization processes.
In [10]:	We want to first define two tests: one tests if transpiled circuits are equal to intial circuit up to global phase and have the correct set of gate, one tests how well our optimization processes do.  def transpilation_results(qc_list, process_list):     # compare the equality and gate set of a list of circuits  gate_set_list = [set(key for key in qc.count_ops()) for qc in qc_list]     equality_list = [equal_circuit_ng(qc_list[0], qc) for qc in qc_list]  print(tabulate(zip(process_list, equality_list, gate_set_list), \
	<pre>def optimization_results(qc_list, process_list):     # compare the depth and number of gates of a list of circuits  depth_list = []     num_gates_list = []  for qc in qc_list:     depth_list.append(qc.depth())     num_gates_list.append(len(qc.data))</pre>
In [11]:	<pre>print(tabulate(zip(process_list, depth_list, num_gates_list), \</pre>
	<pre># for rewriting test qc0.i(0) qc0.x(0) qc0.y(0) qc0.z(0) qc0.h(1) qc0.ry(pi, 1) qc0.rx(pi, 1) qc0.rz(pi, 1) qc0.rz(pi, 1) qc0.cx(1, 0) qc0.cz(0, 1)</pre>
	<pre># for merging test qc0.rx(pi-1, 2) qc0.rx(pi+1, 2) qc0.cz(3, 2) qc0.rz(pi-3, 2) qc0.cz(2, 3) qc0.rz(pi+3, 2)  # for swapping test qc0.rx(pi, 4) qc0.rz(pi, 4)</pre>
	<pre>qc0.rx(pi, 4) qc0.rz(pi, 4)  # for group replacement test qc0.rx(pi/2, 5) qc0.rz(pi, 5) qc0.rz(pi/2, 5)  qc0.rz(3.2, 6) qc0.rz(3.2, 6) qc0.rz(1.3, 6)</pre>
Out[11]:	qc0.rx(pi/2, 6) qc0.rz(5.6, 6) qc0.draw() q_0:
	q_4: RX(pi) RZ(pi) RX(pi) RX(pi) RX(pi) RX(pi) RX(pi/2) R
	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
<pre>In [12]: Out[12]:</pre>	qc1 = rewrite(qc0) qc1.draw() q_0:
	q_4:       RX(pi)       RZ(pi)       RX(pi)       RZ(pi)       RX(pi/2)       NX(pi/2)
	<pre> «q_3:  «q_4:  «q_5:  «q_6:  « After merging, gates on qubit_2 and qubit_3 should disapper: </pre>
In [13]: Out[13]:	q_0:
	q_4:
In [14]:	<pre> «q_4: ————————————————————————————————————</pre>
Out[14]:	q_0:
	q_6: -RZ(-3.0832) -RX(pi/2) -RZ(1.3) -RX(pi/2) -RZ(-0.68319)
In [15]:	<pre> «q_4:  «q_5:  «q_6:  «q_6:  «  After group replacement, gates on qubit_5 and qubit_6 shoud have number reduced:  qc4 = group_and_merge_all(qc3) qc4.draw()</pre>
Out[15]:	q_0:       RZ (pi/2)       RX (pi/2)       RX (pi/2)       RX (pi/2)       NX (pi/2)
	q_6: RZ(1.6292) RX(-1.8416) RZ(-2.254)    **  **  **  **  **  **  **  **  **
In [16]:	"q_5: "q_6: "The circuits above should be equivalent up to global phase, and the gate set used in the last 4 circuits should be $\{R_x,R_z,CZ\}$ :
	Process   Equal Up to GP   Gate Set
In [17]:	optimization_results([qc0, qc1, qc2, qc3, qc4], \
	9. Stress Test In this last section we will be stress testing our program. We will feed a huge random circuit with 1000 gates, and see how our program performs on such huge circuit. We'll verify that the final transpiled and optimized circuit is equivalent to the inital circuit.
	<pre>qc0 = random_qc(3, 1000, GATE_SET) #qc0.draw()</pre>
In [19]: In [20]: In [21]:	<pre>#qc0.draw()  qc1 = rewrite(qc0) #qc1.draw()  qc2 = merge_all(qc1) #qc2.draw()  qc3 = swap_and_merge_all(qc2) #qc3.draw()</pre> qc4 = group_and_merge_all(qc3)
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	<pre>#qc0.draw()  qc1 = rewrite(qc0) #qc1.draw()  qc2 = merge_all(qc1) #qc2.draw()  qc3 = swap_and_merge_all(qc2) #qc3.draw()  qc4 = group_and_merge_all(qc3) #qc4.draw()  transpilation_results([qc0, qc1, qc2, qc3, qc4], \</pre>
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	Qc1 = rewrite(qc0)   #qc1.draw()
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	Sqc0.draw()
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process
<pre>In [19]: In [20]: In [21]: In [22]:</pre>	\$qc0.draw()   \$qc1 = rewrite(qc0)   \$gc1.draw()   \$qc2 = merge_all(qc1)   \$gc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc2.draw()   \$qc3 = swap_and_merge_all(qc2)   \$gc3.draw()   \$qc4 = group_and_merge_all(qc3)   \$gc4.draw()   \$("Initial Circuit", "Circuit Rewriting", "Gate Merging", "Swapping", "Group Repal cement"])   Process