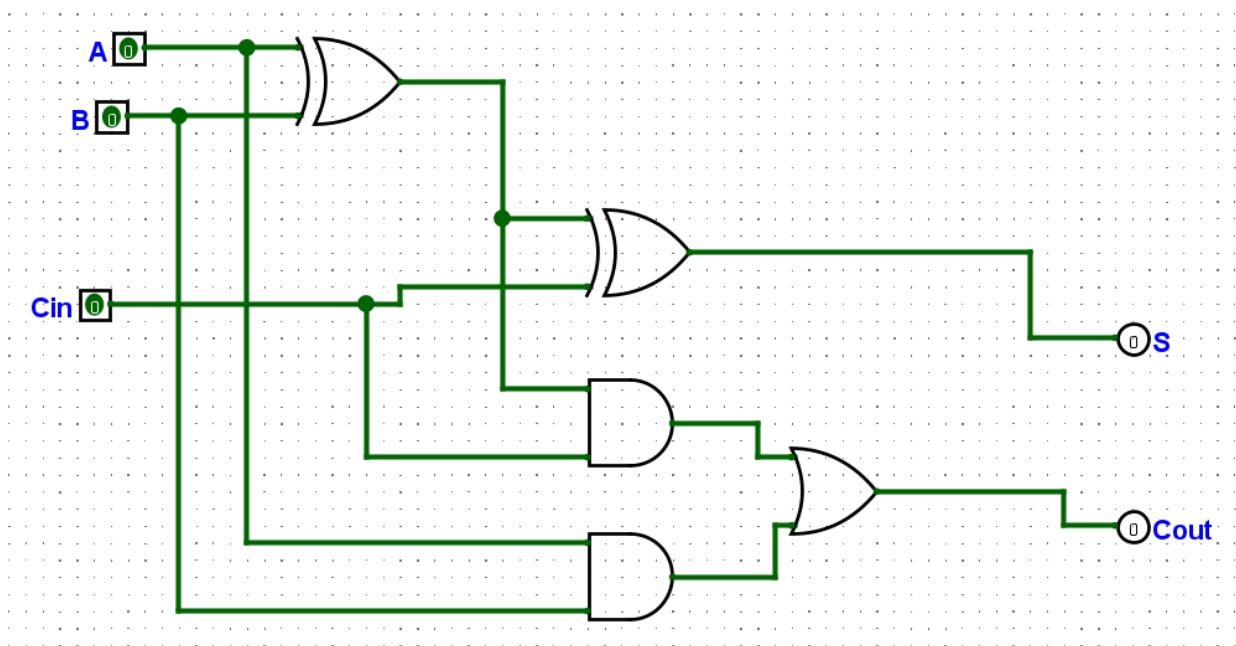


Computer Arch

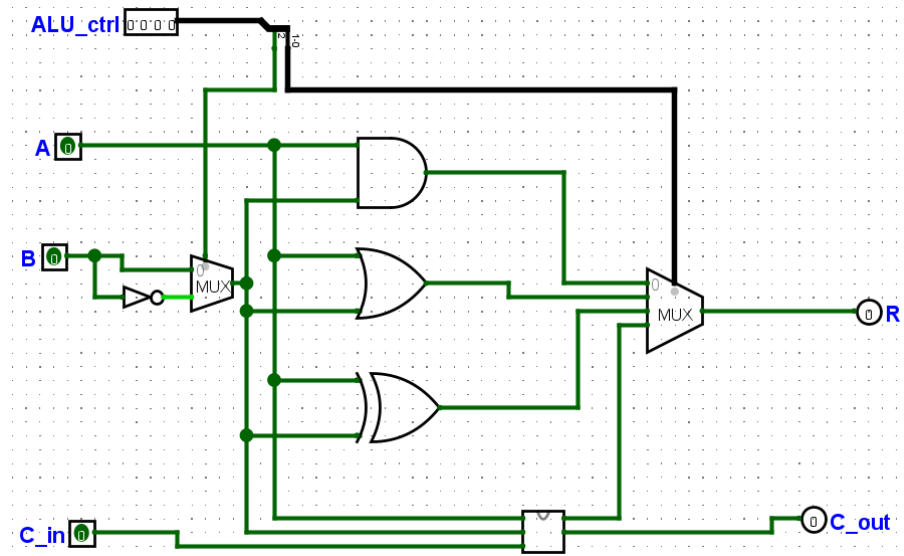
Christian Alberto Gomez

4/15/2022

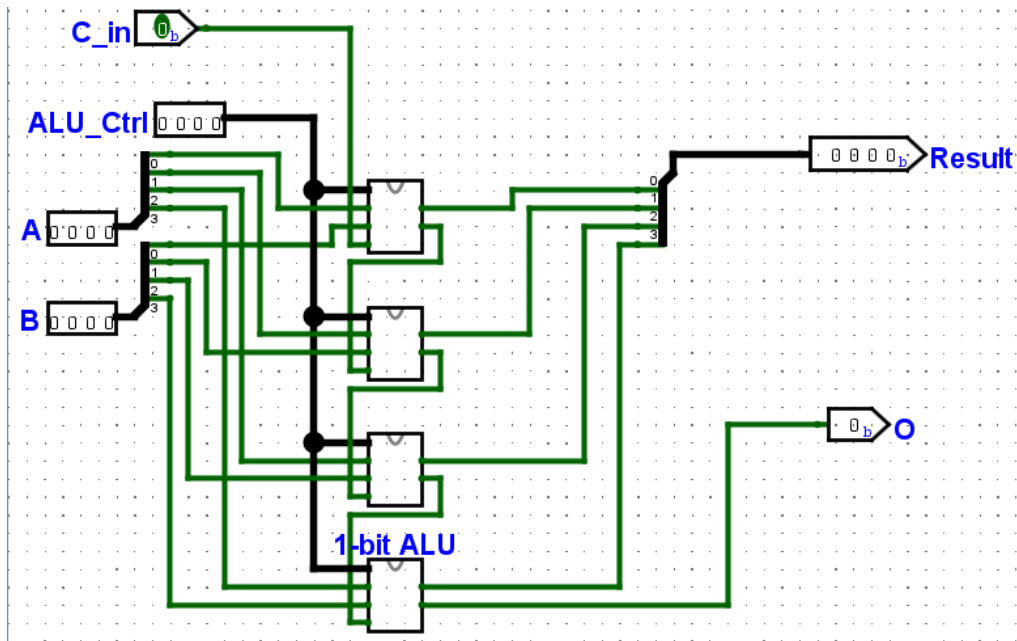
1-bit adder:



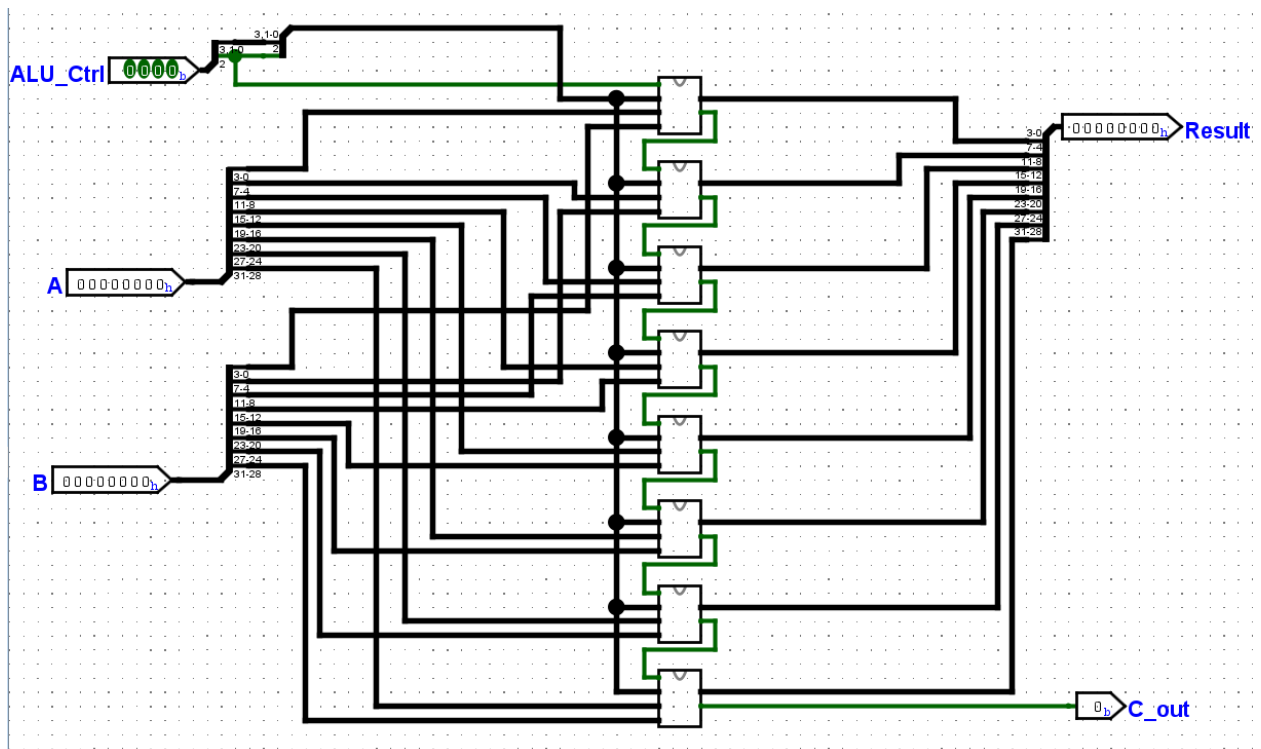
1-bit ALU:



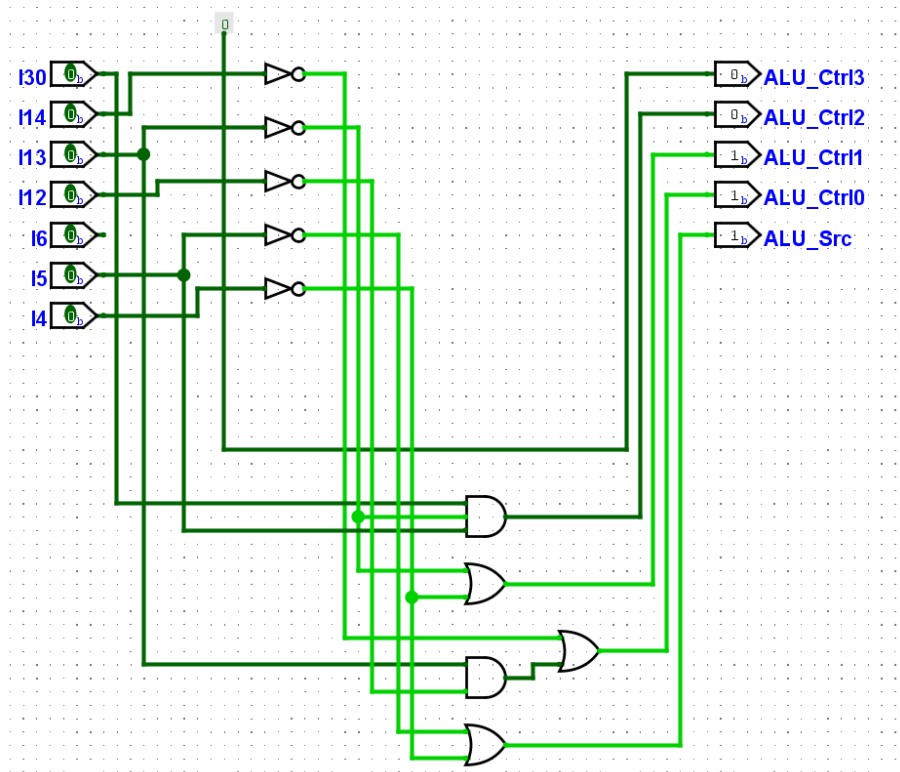
4-bit ALU:



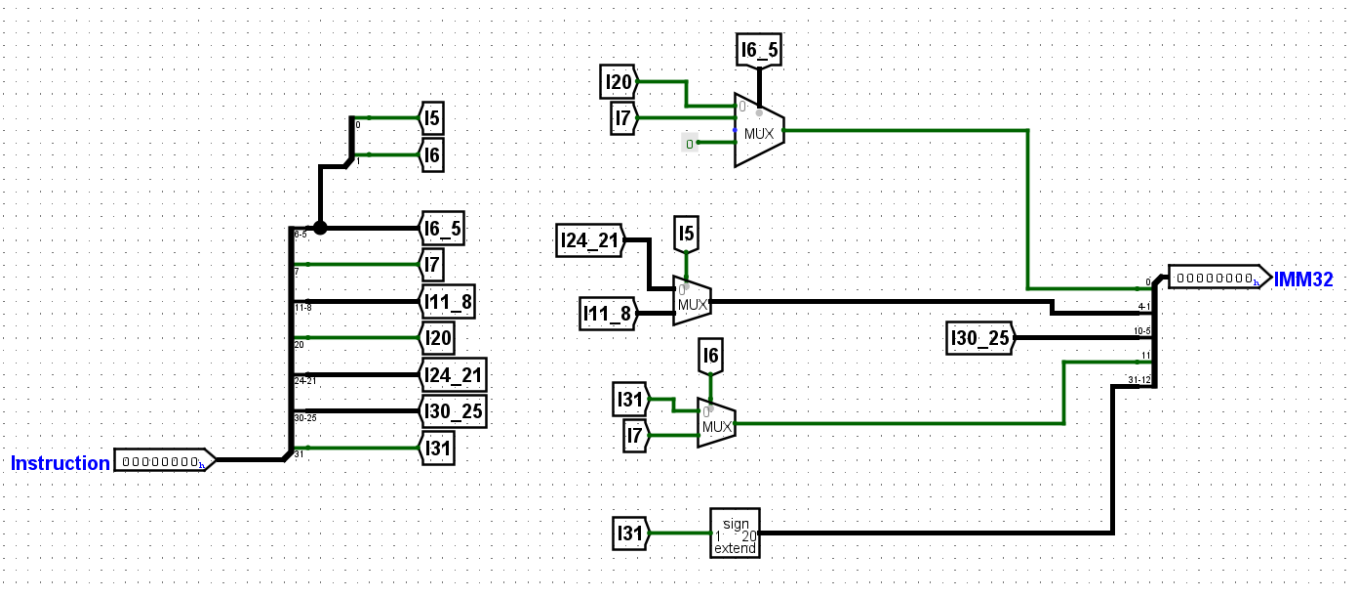
32-bit ALU:



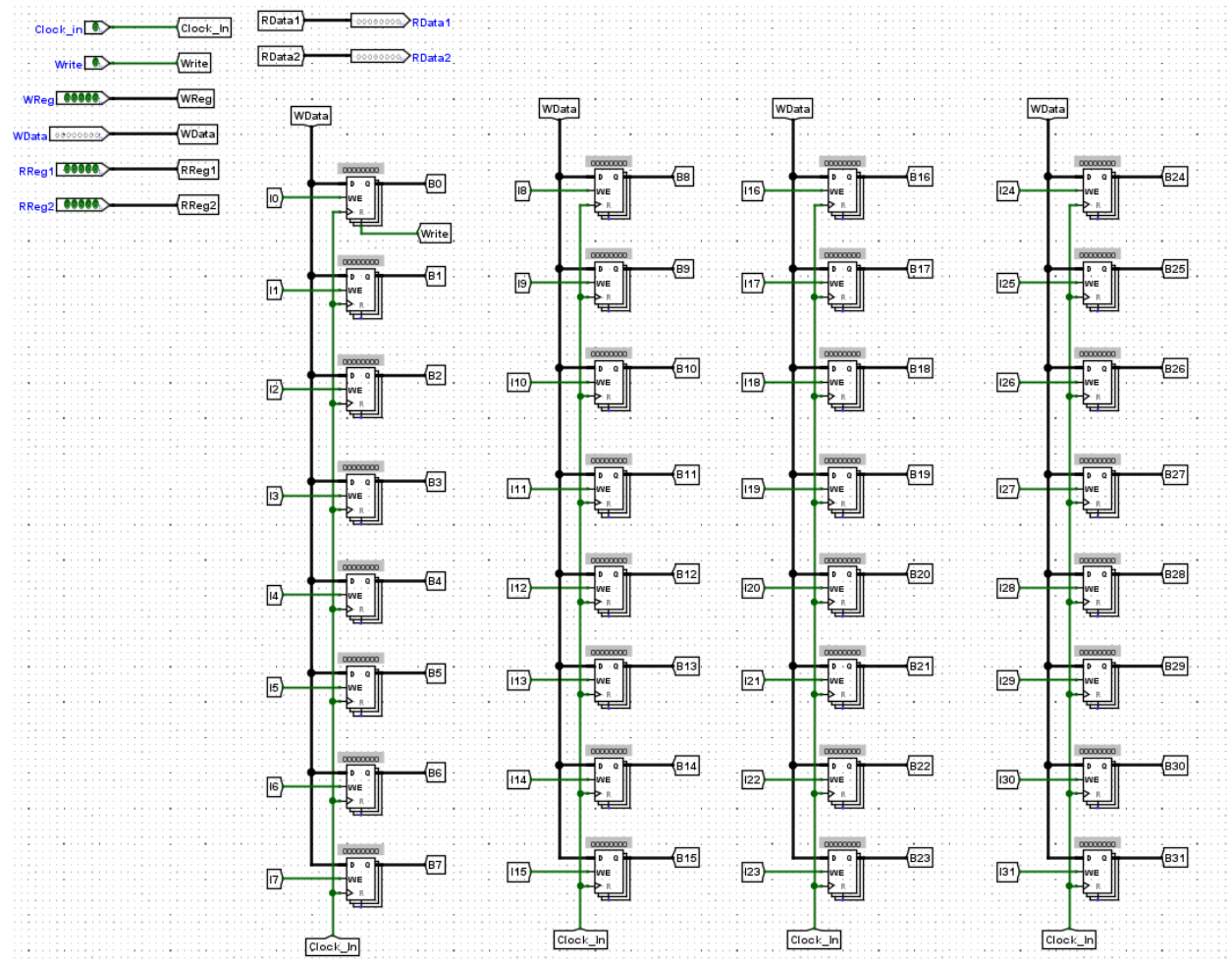
ALU_Ctrl:

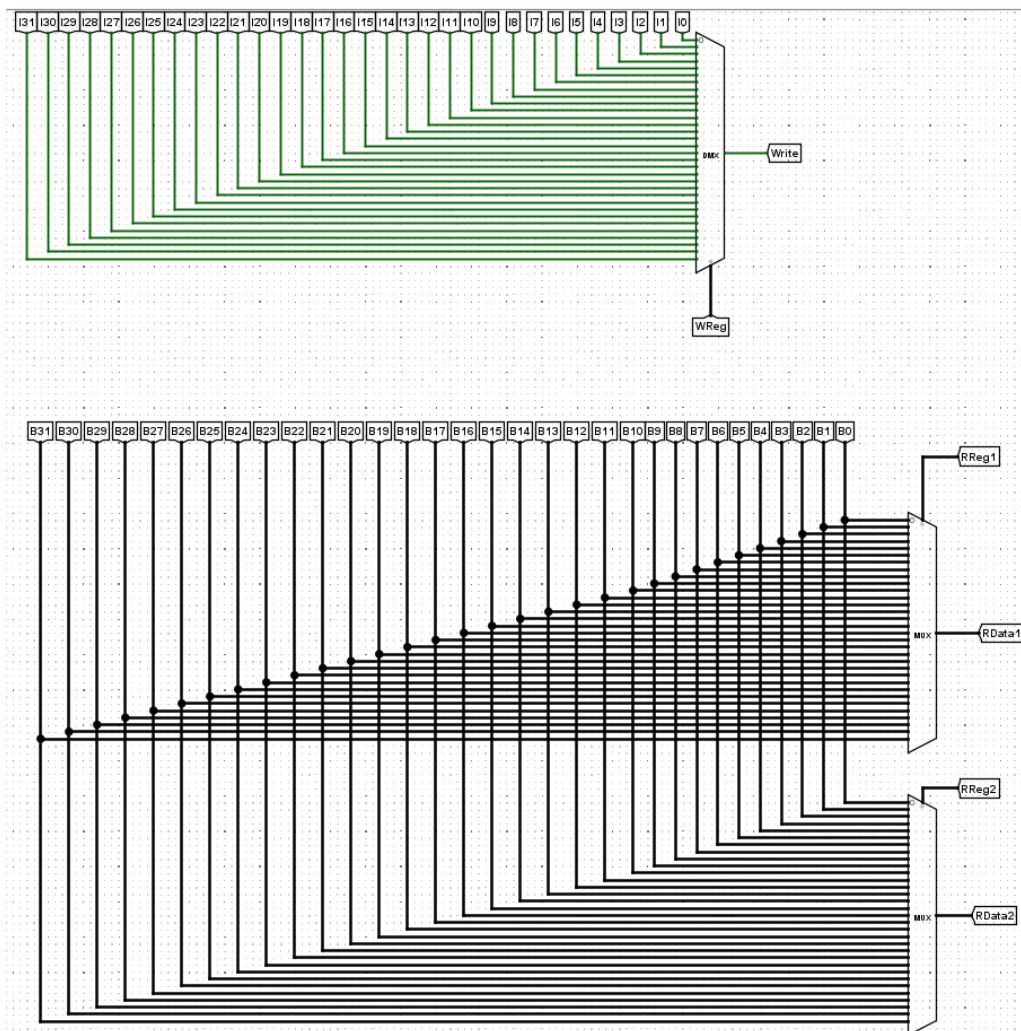


ImmGen:



32-bit Register File:





Datapath or Main circuit:

