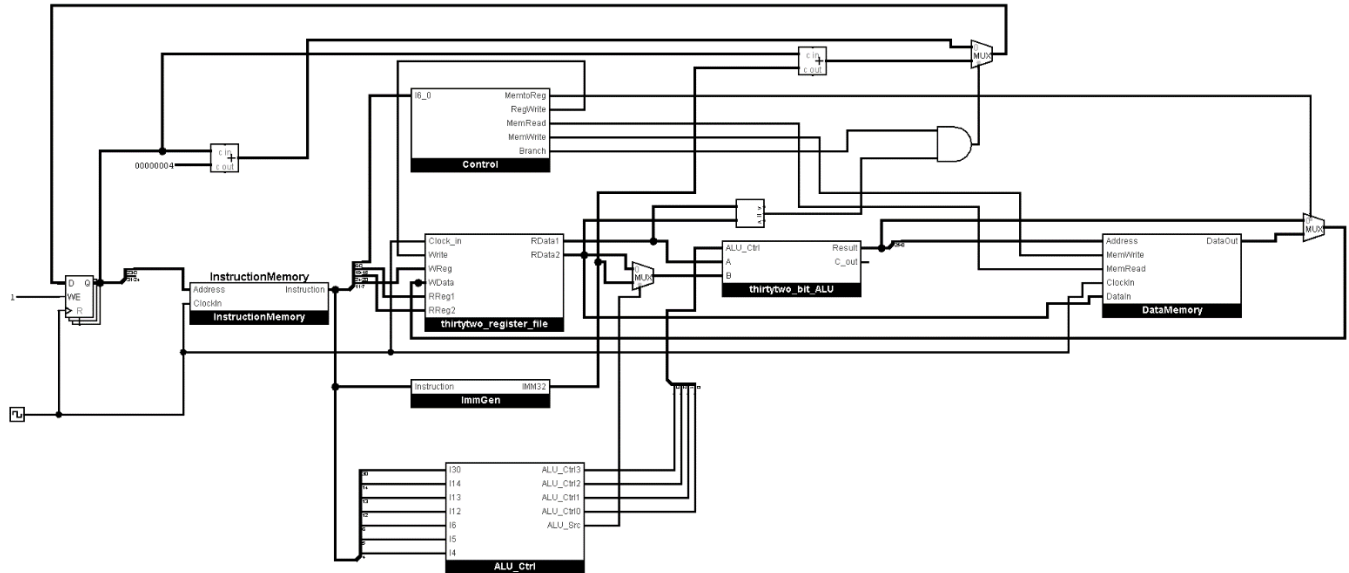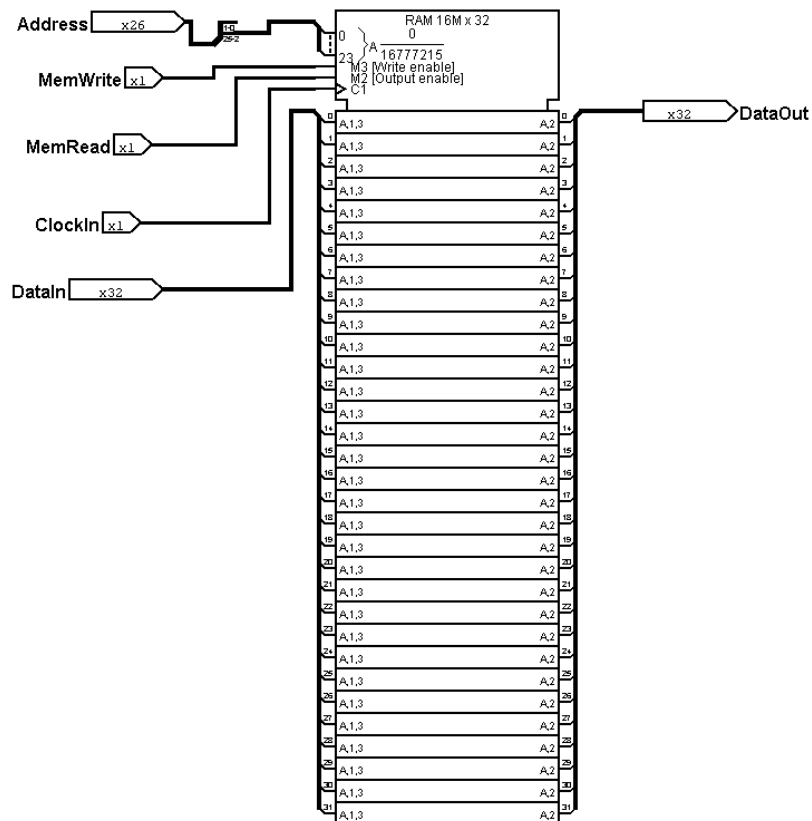Christian Gomez

Lab 5 – Milestone 2

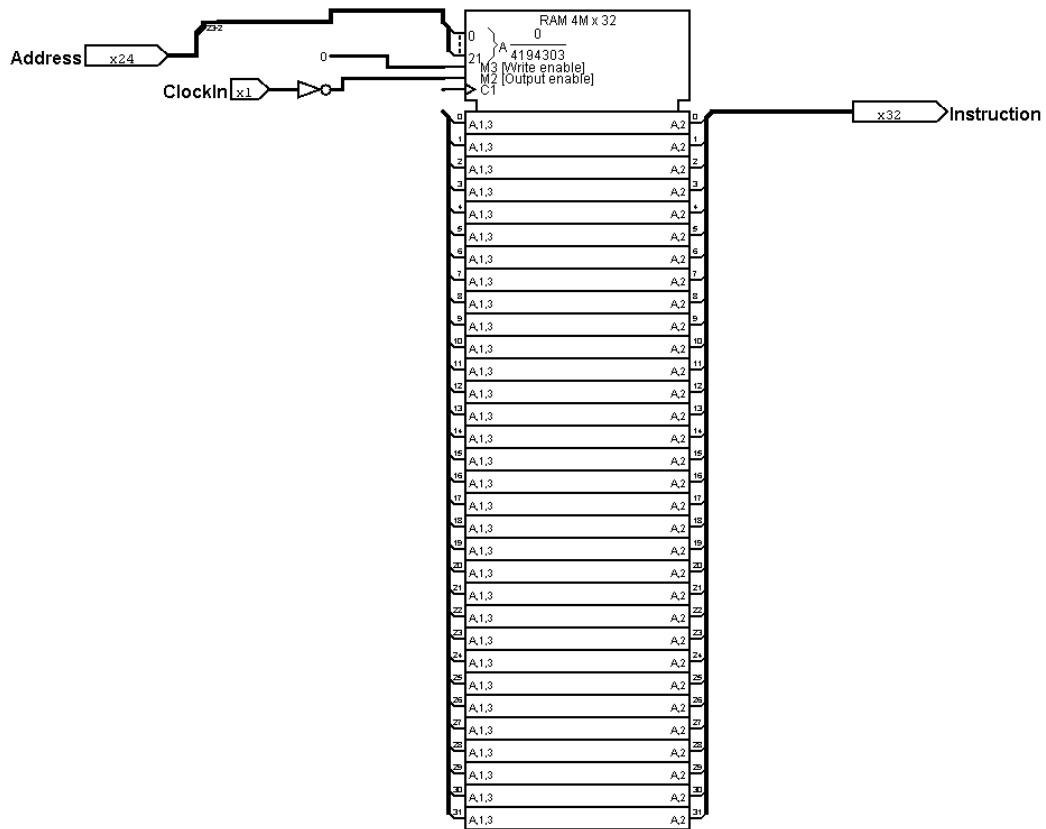-Main Circuit:

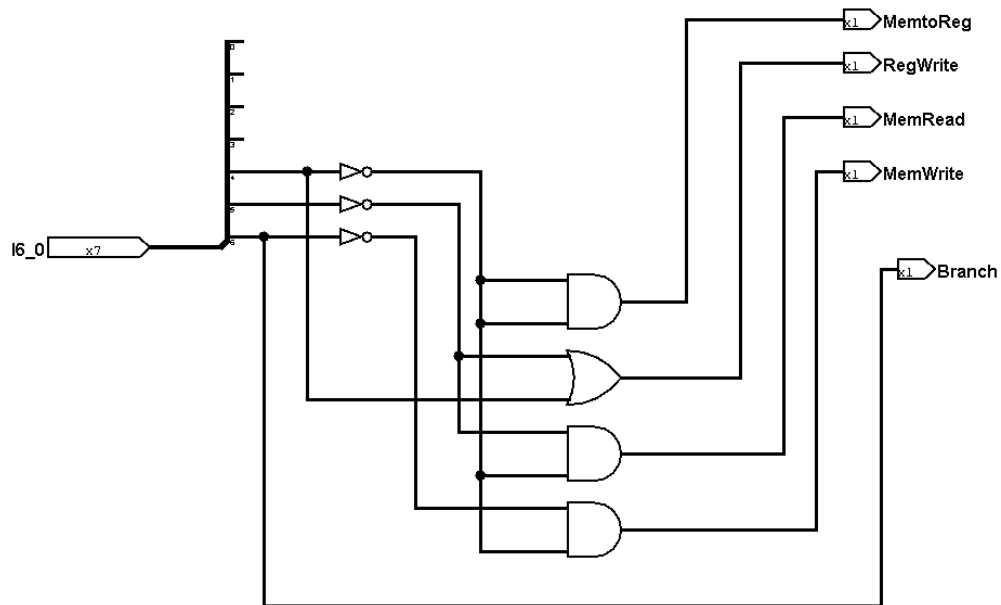

-DataMemory:

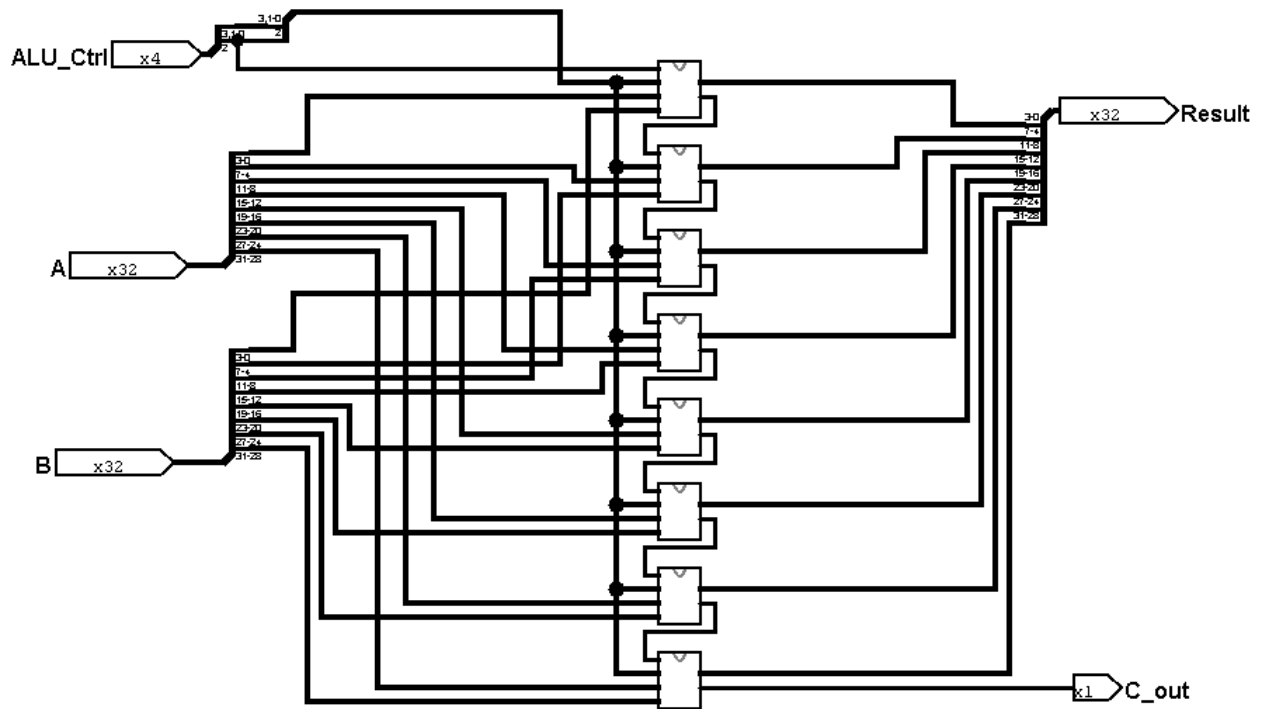-InstructionMemory:
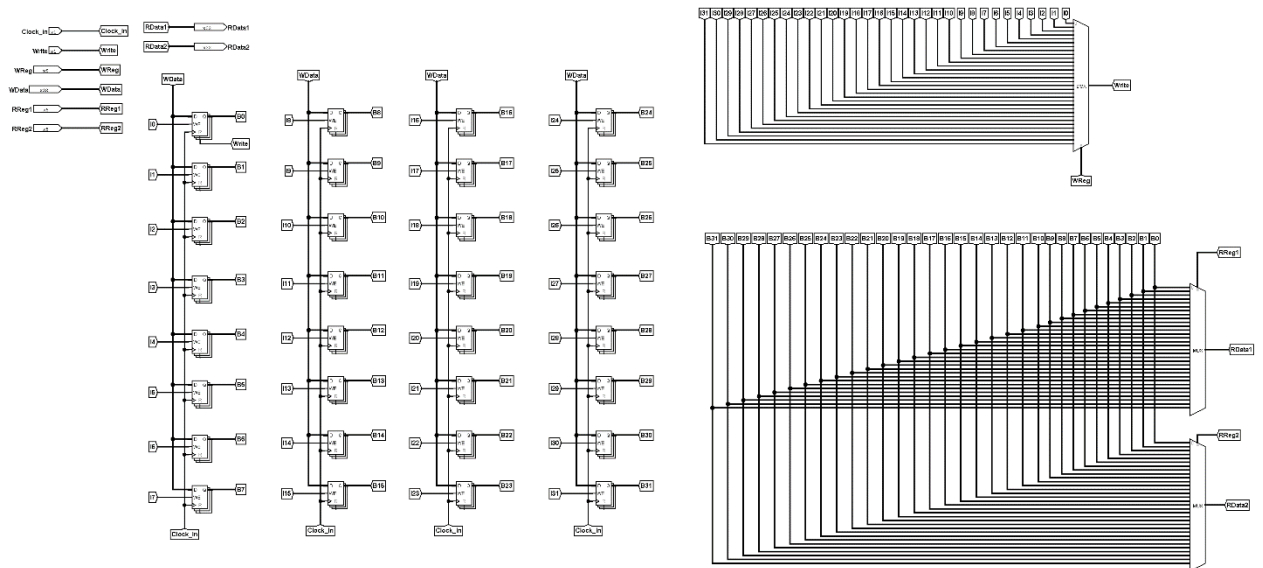
Address x24

RAM 4M x 32

0
4194303

M3 [Write enable]
M2 [Output enable]
C1

ClockIn x1
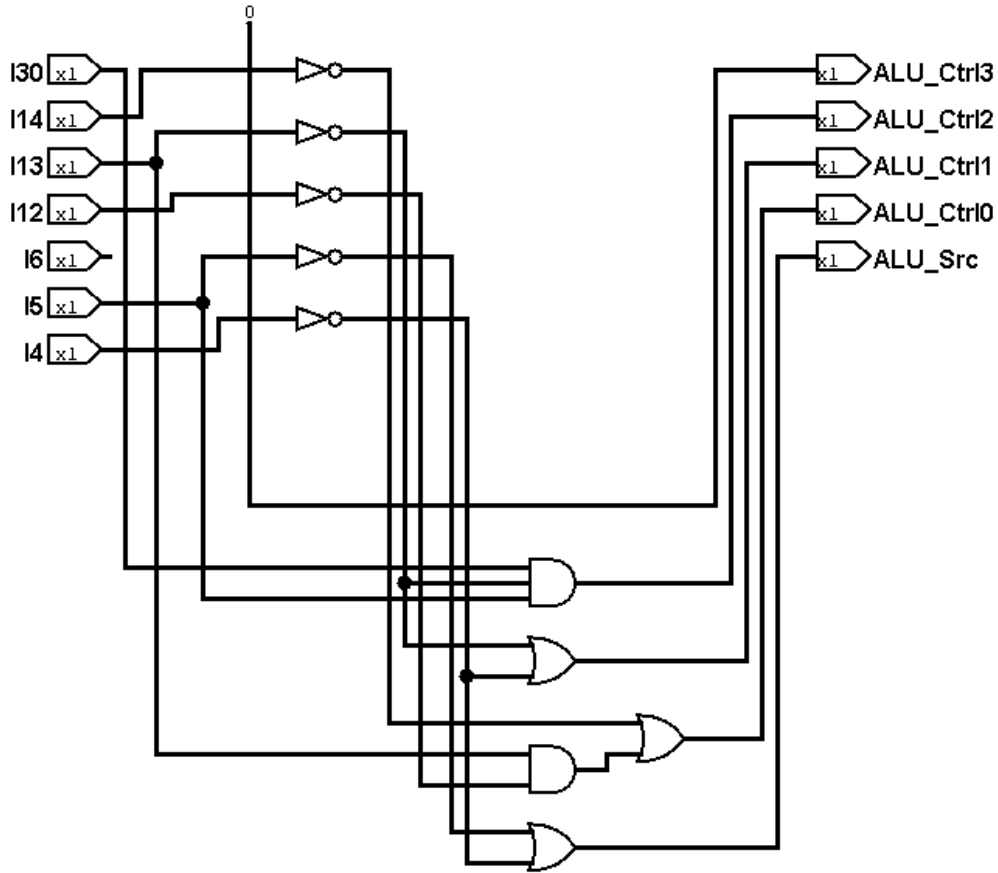
x32 Instruction

-Control:

I6_0 x7

MemtoReg
RegWrite
MemRead
MemWrite
Branch

-32-bit Alu:

-32-bit Register:

-Alu_Ctrl:

-ImmGen: