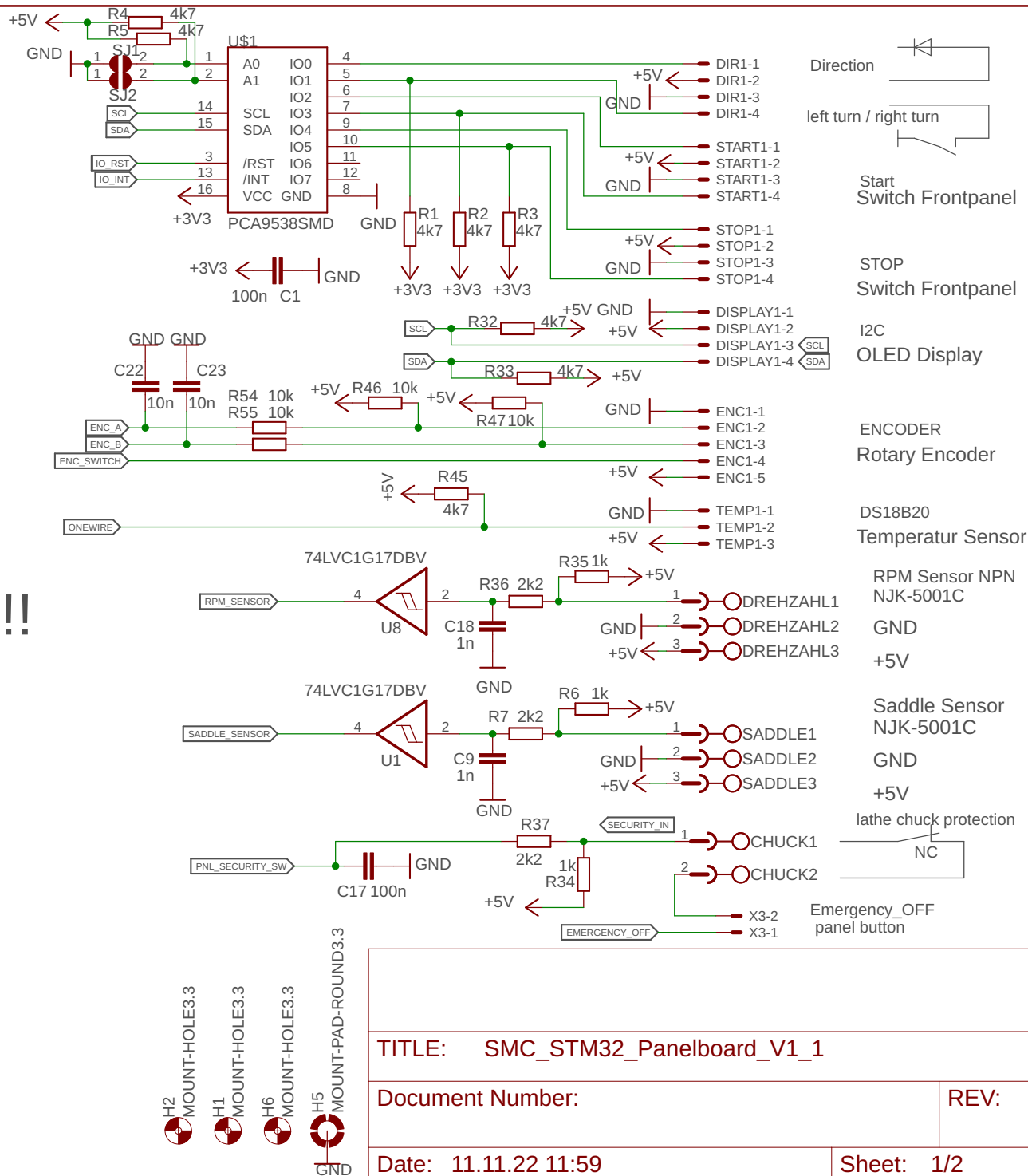
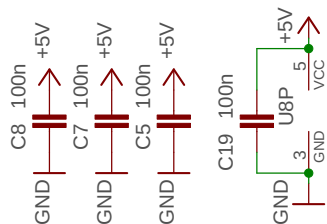
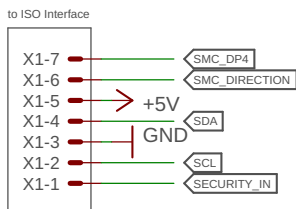


!!! in progress !!!



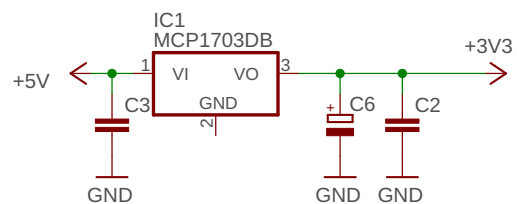
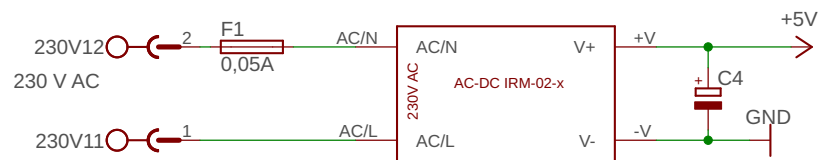
TITLE: SMC\_STM32\_Panelboard\_V1\_1

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