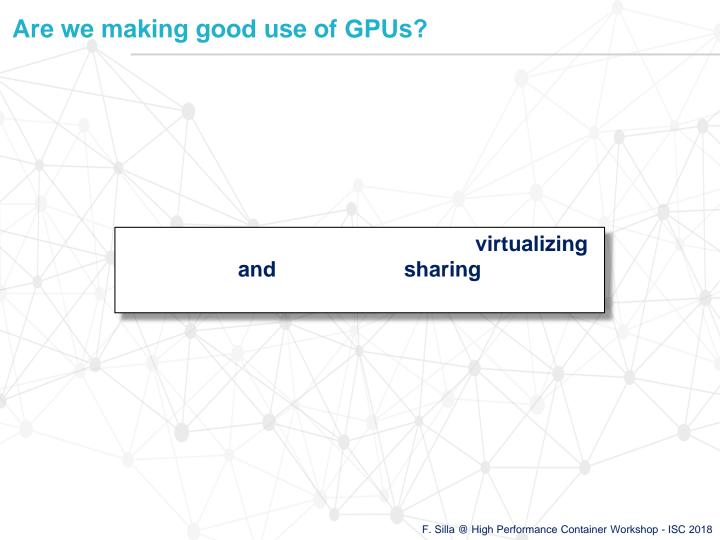
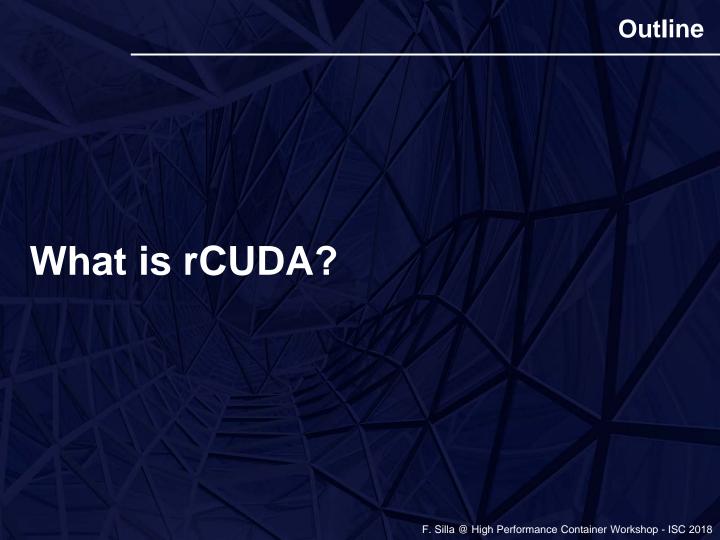


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GPU utilization can be increased by **virtualizing** the GPU **and** concurrently **sharing** it among several applications



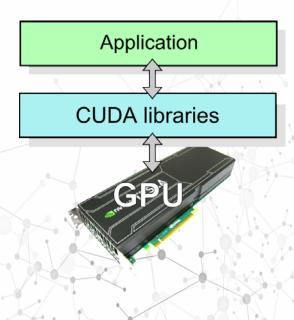


# **Basics of GPU computing**

#### Basic behavior of CUDA

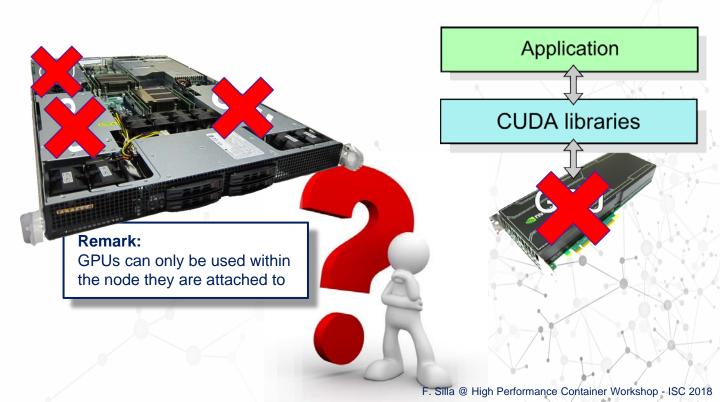


GPUs can only be used within the node they are attached to

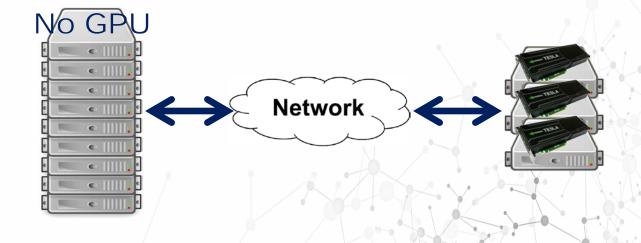


# **Basics of GPU computing**

#### Basic behavior of CUDA

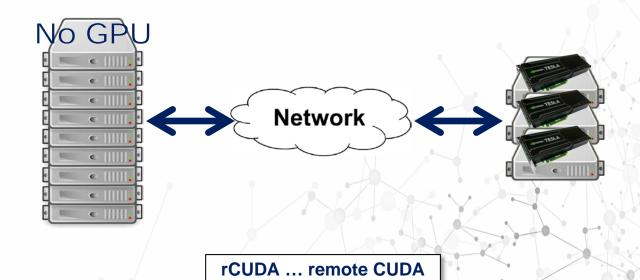


### A different approach: remote GPU virtualization



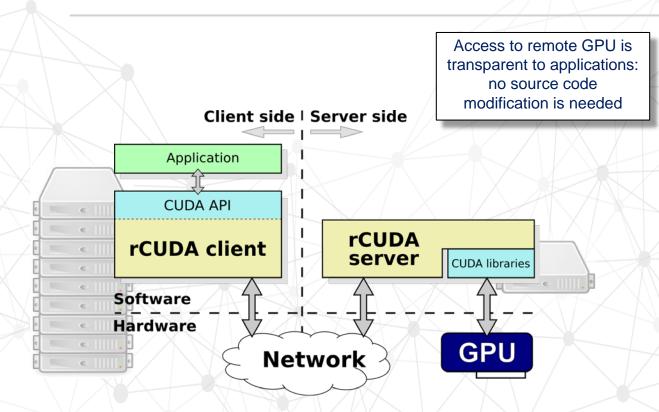
## A different approach: remote GPU virtualization

A software technology that enables a more flexible use of GPUs in computing facilities



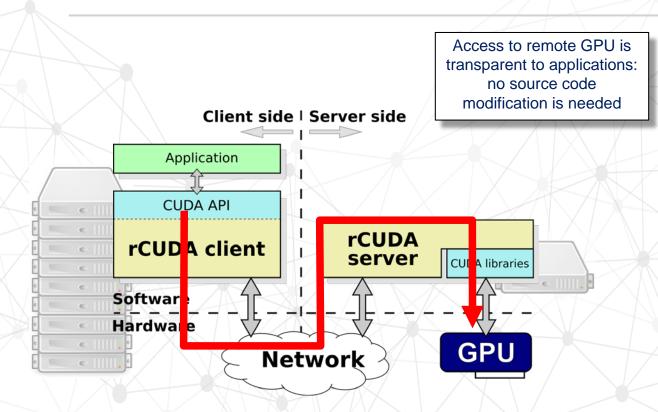


### **Basics or rCUDA**



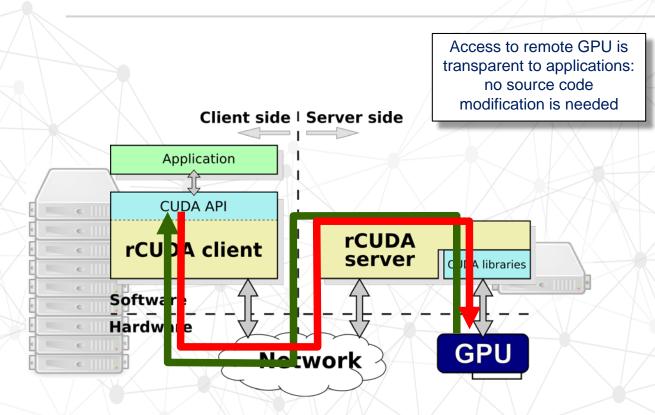


### **Basics or rCUDA**



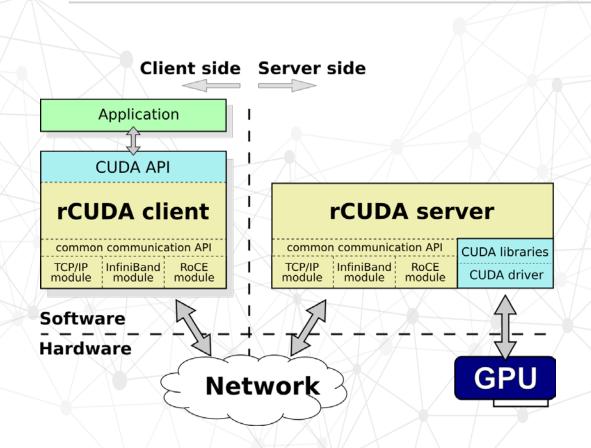


### **Basics or rCUDA**



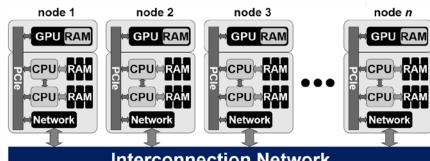


## rCUDA supports RDMA transfers



### rCUDA envision

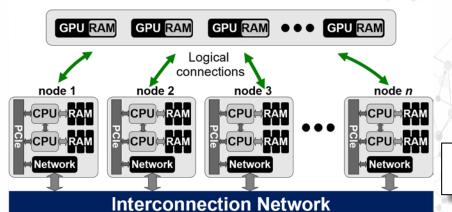
rCUDA allows a new vision of a GPU deployment, moving from the usual cluster configuration ...



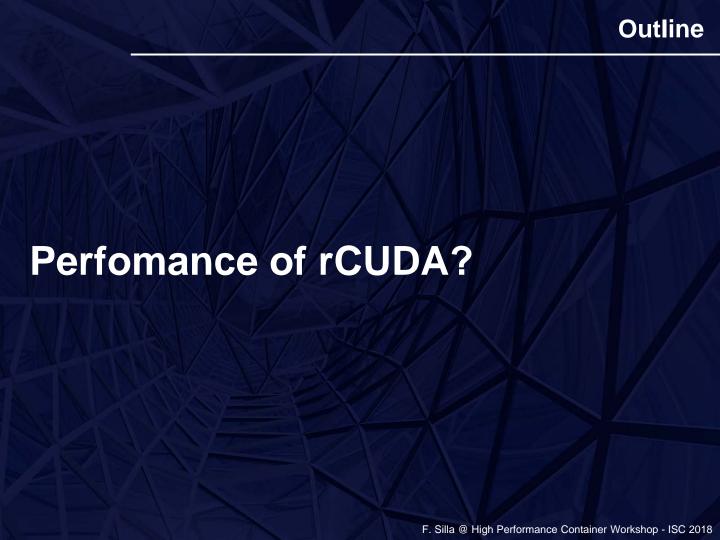
#### Interconnection Network

**Physical** configuration

... to the following one:

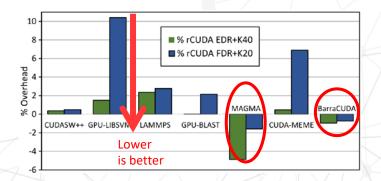


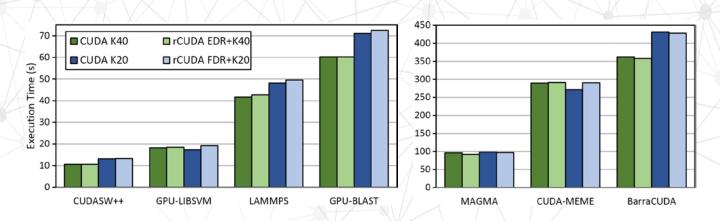
Logical configuration



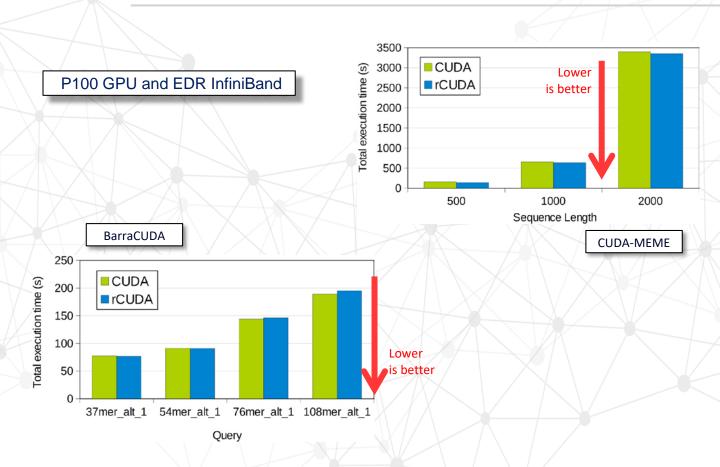
### **Performance of rCUDA**

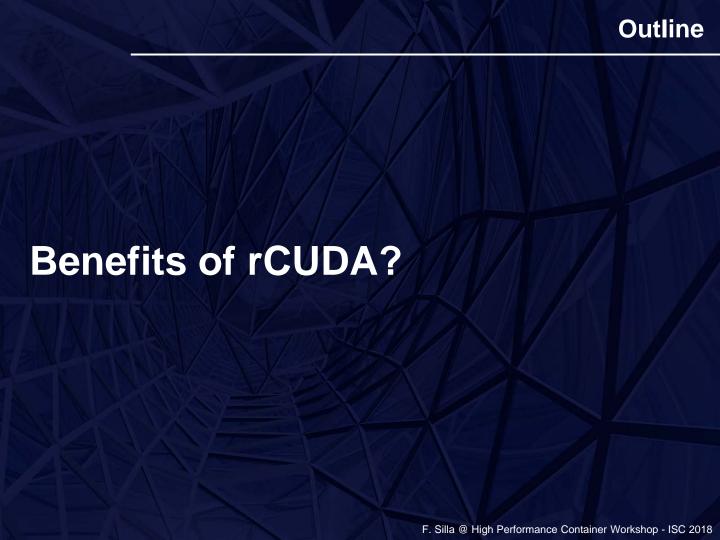
- K20 GPU and FDR InfiniBand
- K40 GPU and EDR InfiniBand





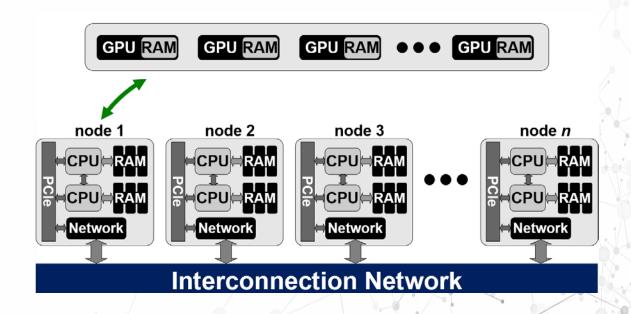
### Performance of rCUDA



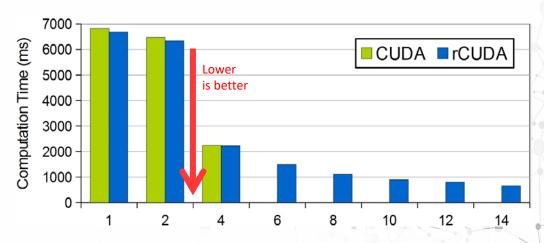


# **Benefits of rCUDA:**

- 1. Many GPUs for an application
- 2. Server consolidation
- 3. Increased cluster throughput



#### K20 GPUs and FDR InfiniBand

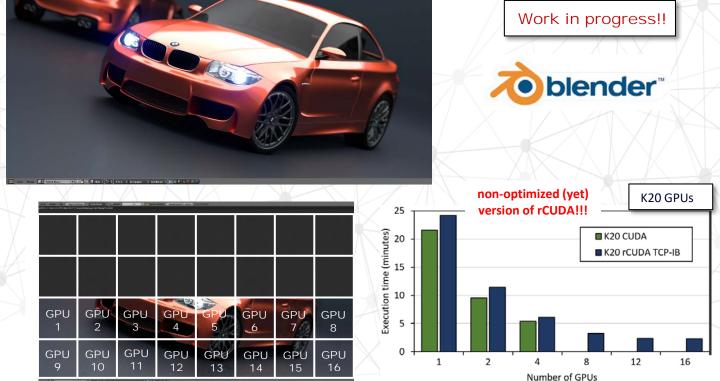


MonteCarlo multi-GPU program running in 14 NVIDIA Tesla K20 GPUs

64

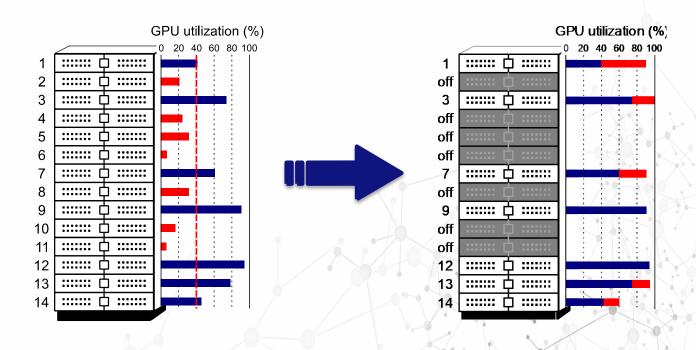
GPUs!!

```
■  bsc19421@nvb127:~
./deviceQuery Starting...
 CUDA Device Query (Runtime API) version (CUDART static linking)
Detected 64 CUDA Capable dev ce(s)
Devi 0: "Tesla M2090"
   JOA Driver Version / Runtime Version
                                                 5.0 / 5.0
  CUDA Capability Major/Minor version number:
                                                 2.0
 Total amount of global memory:
                                                 6144 MBytes (6442123264 bytes)
  (16) Multiprocessors x ( 32) CUDA Cores/MP:
                                                 512 CUDA Cores
  GPU Clock rate:
                                                 1301 MHz (1.30 GHz)
  Memory Clock rate:
                                                 1848 Mhz
  Memory Bus Width:
                                                 384-bit
  L2 Cache Size:
                                                 786432 bytes
  Max Texture Dimension Size (x,y,z)
                                                 1D=(65536), 2D=(65536,65535), 3D=(204<u>8,2048,2048</u>)
  Max Layered Texture Size (dim) x layers
                                                 1D=(16384) x 2048, 2D=(16384,16384) x 2048
  Total amount of constant memory:
                                                 65536 bytes
  Total amount of shared memory per block:
                                                 49152 bytes
  Total number of registers available per block: 32768
  Warp size:
                                                 32
  Maximum number of threads per multiprocessor: 1536
  Maximum number of threads per block:
                                                 1024
  Maximum sizes of each dimension of a block:
                                                 1024 x 1024 x 64
  Maximum sizes of each dimension of a grid:
                                                 65535 x 65535 x 65535
  Maximum memory pitch:
                                                 2147483647 bytes
  Texture alignment:
                                                 512 bytes
  Concurrent copy and kernel execution:
                                                 Yes with 2 copy engine(s)
  Run time limit on kernels:
                                                 No
  Integrated GPU sharing Host Memory:
                                                 No
  Support host page-locked memory mapping:
                                                 No
  Alignment requirement for Surfaces:
                                                 Yes
  Device has ECC support:
                                                 Disabled
  Device supports Unified Addressing (UVA):
                                                 Yes
  Device PCI Bus ID / PCI location ID:
                                                 2 / 0
  Compute Mode:
     < Default (multiple host threads can use ::cudaSetDevice() with device simultaneously) >
Device 1: "Tesla M2090"
  CUDA Driver Version / Runtime Version
                                                 5.0 / 5.0
```



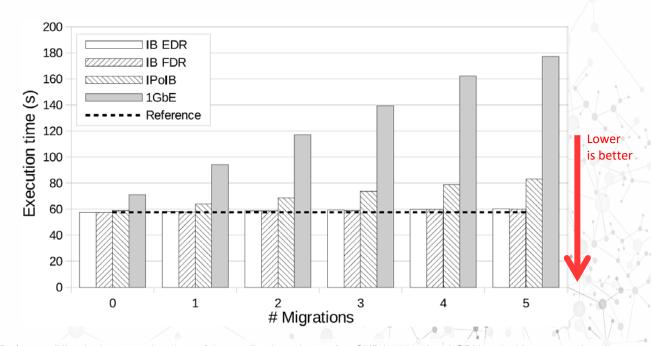
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### Server consolidation with rCUDA



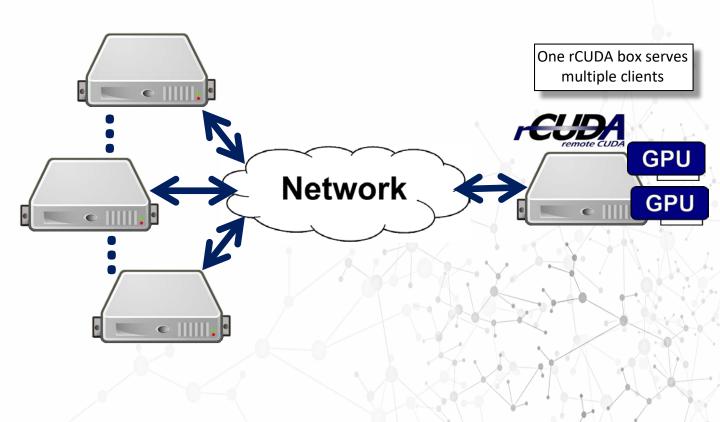
### Server consolidation with rCUDA

- The GPU-Blast application is migrated up to 5 times among K40 GPUs
  - The aggregated volume of GPU data is 1300 MB (consisting of 9 memory regions)

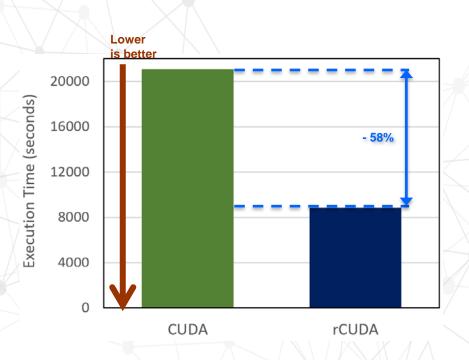


The "Reference" line is the execution time of the application when using CUDA with a local GPU and without any migration

## **Increased cluster throughput**

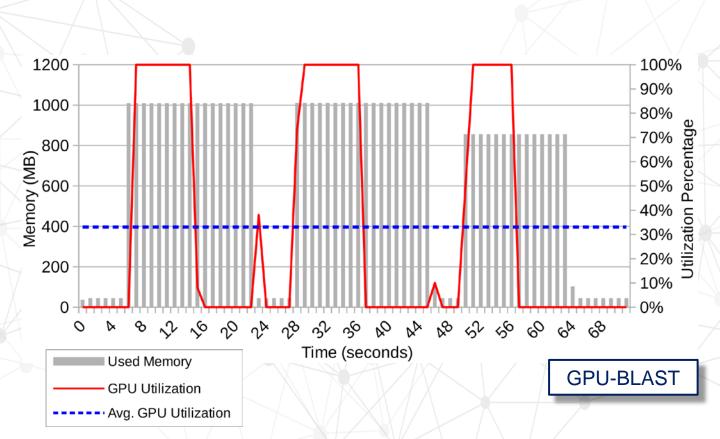


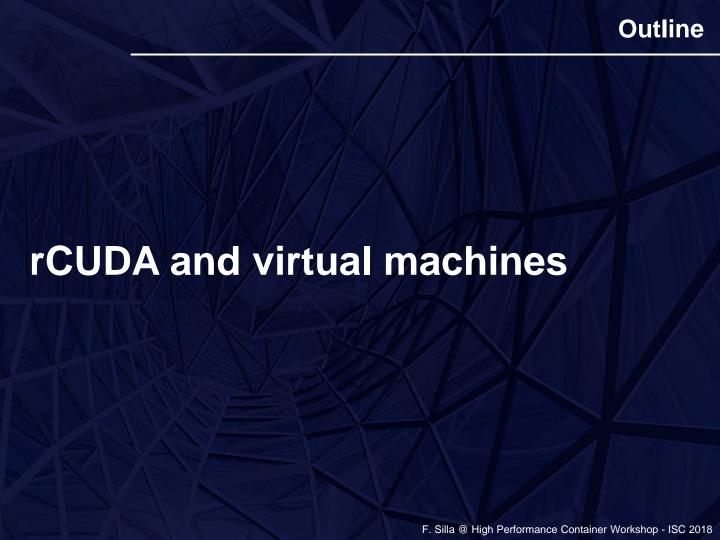
## **Increased cluster throughput**



- 1. BarraCUDA
- 2. CUDA-MEME
- 3. CUDASW++
- 4. GPU-Blast
- 5. Gromacs
- 6. Magma

### **Increased cluster throughput**



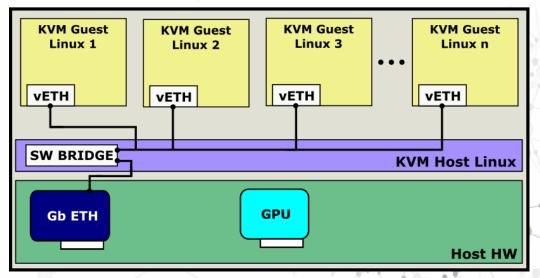




### Virtual machines may need access to GPUs

• How to access the GPU in the native domain from inside of virtual machines?

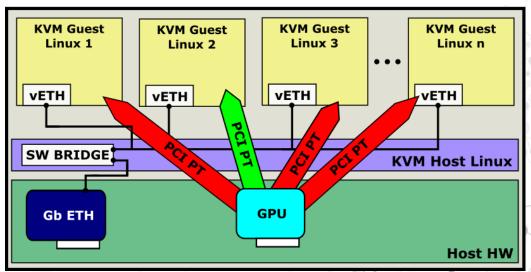
### Computer hosting several KVM virtual machines



### Virtual machines may need access to GPUs

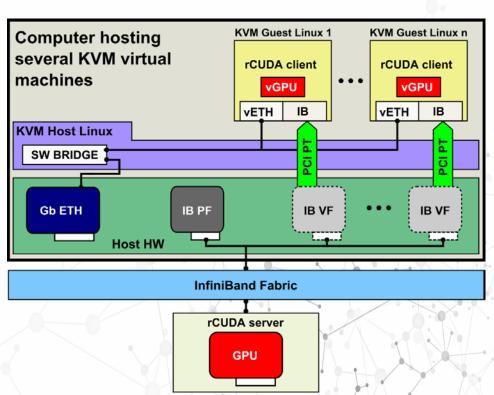
- The GPU is assigned by using PCI passthrough exclusively to a single virtual machine
- Concurrent usage of the GPU is not possible

### Computer hosting several KVM virtual machines

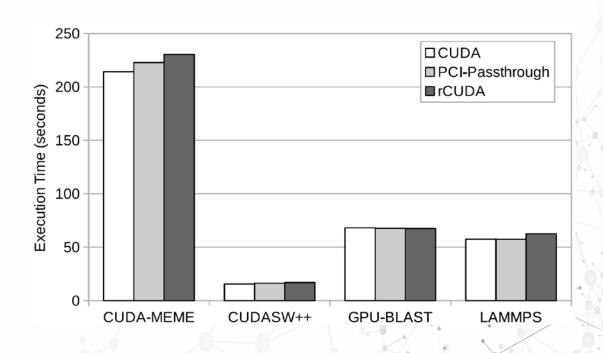


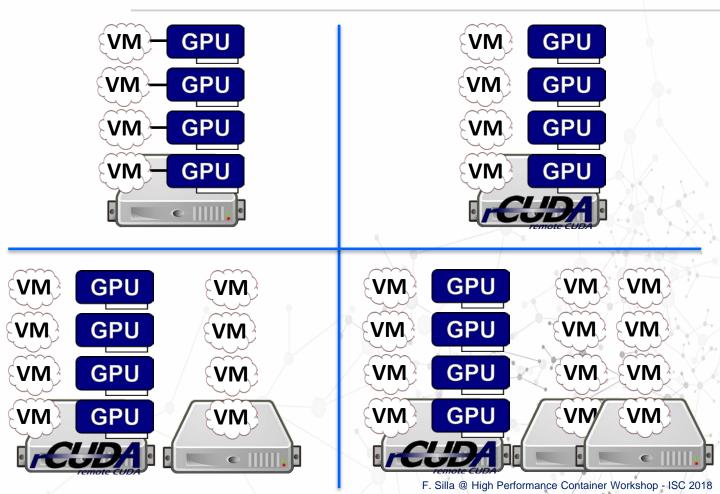
### Using rCUDA to access the GPU

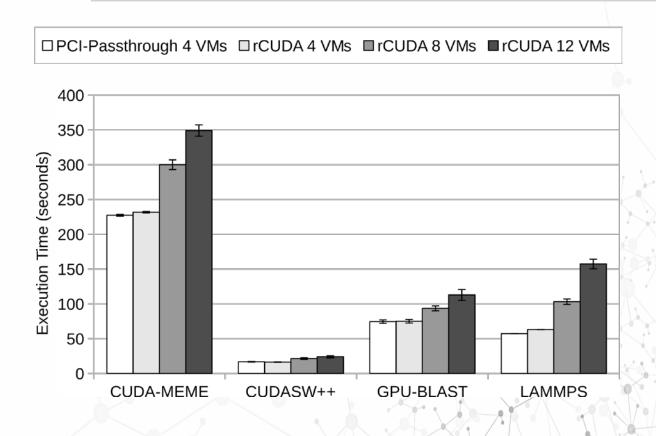
- If InfiniBand is available, the rCUDA server can be placed in another node
- Several GPUs can be provided to the VMs, either in a single remote node or in several remote nodes



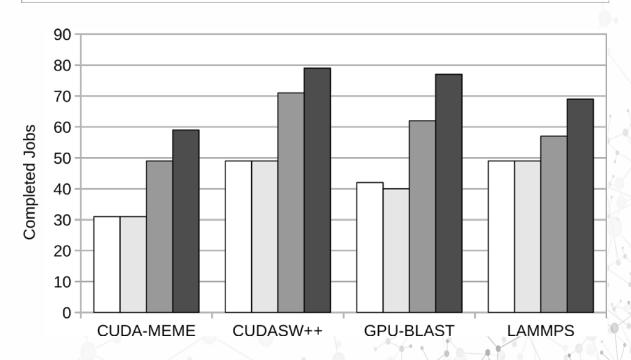
### Using rCUDA to access the GPU

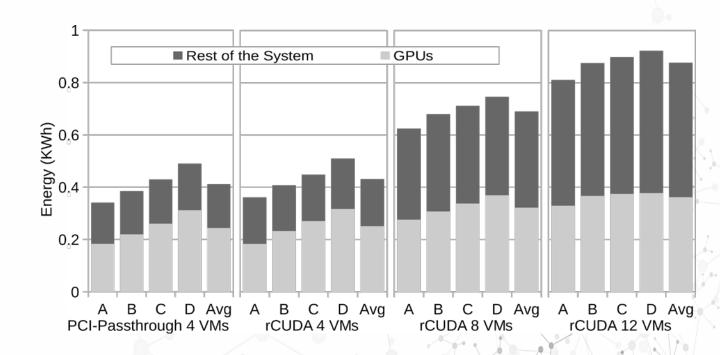














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