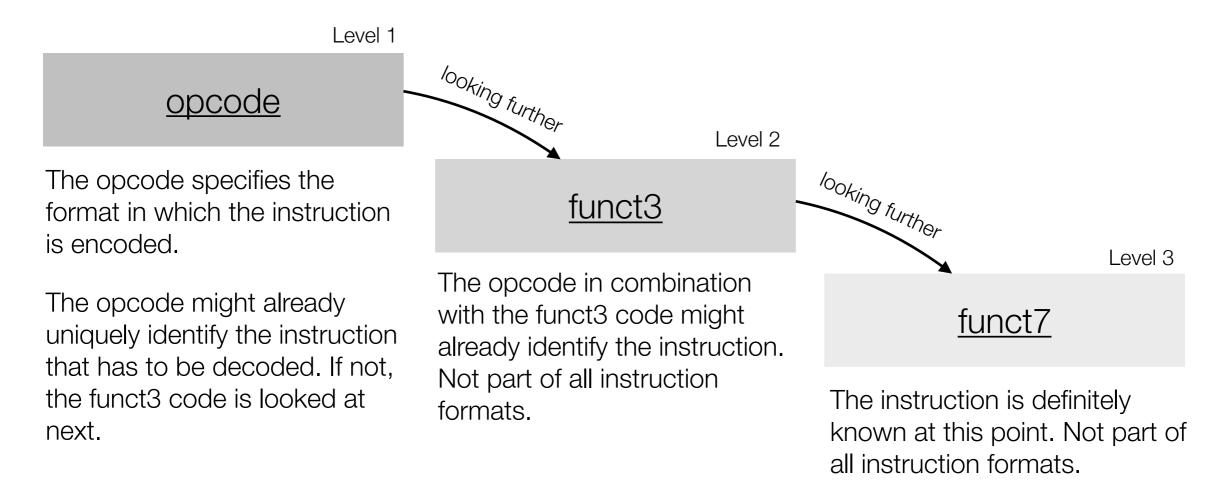
Semantics of RISC-U

 The semantics of an instruction is determined by how the processor implements it. • In selfie this implementation is done in the do .. 's, like do addi() or do sltu().

Decoding Instructions

- Each instruction can be uniquely identified by certain parts of the 32 bits called opcode, funct3 and funct7 code.
- When the instruction is known, the meaning of the remaining bits of the instruction becomes clear.



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