Syntax of RISC-U

- Different instructions have different binary encodings. Instructions are encoded using special formats.

- The format of an instruction specifies how the 32 bits are interpreted. It is designed in a way that allows fast decoding of the instructions.

- Selfie always stores binary code which can then be disassembled to obtain the assembly code.

Semantics of RISC-U

- The semantics of an instruction is determined by how the processor implements it.

- In selfie this implementation is done in the `do_..` 's, like `do_addi()` or `do_sltu()`.

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