## Instructions and Formats

# RISC-U

addi

add

sub

mul

divu

remu

beq

jal

jalr

Id

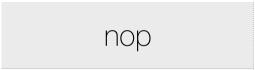
sd

sltu

ecall

**Formats** R-format **I-format** S-format **B-format** J-format

**U-format** 



### special case of addi

# Immediate Arithmetic Instructions

lui	\$rd	imm	
addi	\$rd	\$rs1	imm

- Load upper immediate loads imm value shifted by 12 bits into \$rd and add immediate adds imm to the content of \$rs1 and stores the result in \$rd.
- These two instructions are used to **initialize registers** (\$rs1 = \$zero) and to **load addresses** into a register in order to read values from memory.
- lui is used to load the upper and addi to load the lower bits see load integer(uint64 t value).
- A special case of addi is nop, with \$zero = \$zero + 0.

### Instructions and Formats

### **RISC-U**

lui	beq	
addi	jal	
add	jalr	
sub	ld	
mul	sd	
divu	sltu	
remu	ecall	

#### **Formats**

R-format
<u>I-format</u>
<u>S-format</u>
<u>B-format</u>
<u>J-format</u>
<u>U-format</u>

special case of addi

nop