Syntax of RISC-U

 At machine code level we distinguish further between the human readable <u>assembly</u> code and the <u>binary</u> code the CPU executes.

addi \$t02 \$t01 4





Syntax of RISC-U

- Different instructions have different binary encodings.
 Instructions are encoded using special formats.
- The format of an instruction specifies how the 32-bits are interpreted. It is designed in a way that allows fast decoding of the instructions.
- Selfie always stores binary code which can then be disassembled to obtain the assembly code.

Syntax of RISC-U

 At machine code level we distinguish further between the human readable <u>assembly</u> code and the <u>binary</u> code the CPU executes.

addi \$t02 \$t01 4

0000000001000110000001110010011