

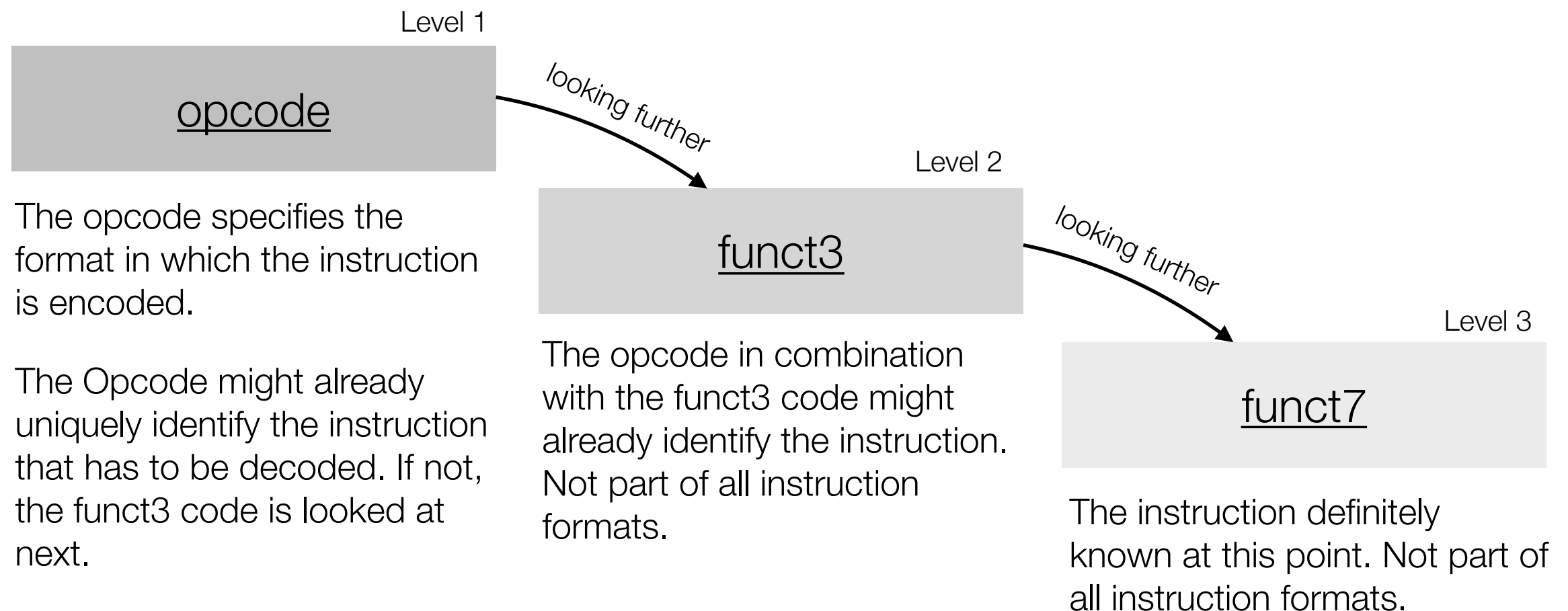


**Semantic Risk**

- The semantics of an instruction is determined by how the processor implements it.
- In selfie this implementation is done in the `do_` 's, like `do addi()` or `do sltu()`.

# Decoding Instructions

- Each instruction can be uniquely identified by certain parts of the 32 bits called **opcode**, **funct3** and **funct7** code.
- When the instruction is known, the meaning of the remaining bits of the instruction becomes clear.



# Semantics of RISC-U

- The semantics of an instruction is determined by how the processor implements it.
- In selfie this implementation is done in the `do_... 's`, like `do_addi()` or `do_sltu()`.