Introduction to RISC-U

- RISC-U is the RISC-V subset targeted, emulated, and virtualized by selfie.

- There are 14 instructions, each 32-bit wide, the processor knows and the compiler generates code for.

- When talking about formal languages it is important to distinguish between the syntax and the semantics of that language.

Syntax of RISC-U

- At machine code level we distinguish further between the human readable assembly code and the binary code the CPU executes.

```
addi $t02 $t01 4

00000000010000110000001110010011
```

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