OPERATING SYSTEMS - PART 2 NON-CONTIGUOUS MEMORY ALLOCATION AND VIRTUAL MEMORY

(Data Structure for free memory)

b) free list

1. Which of these data structure was used to represent free memory in the

lecture video?

a) free hash table

c) free tree		d) free heap						
2. In a computer s used for allocatir encountered:	-	h 4 CPUs		e, where		_		
Partit	ion sizes	in kB		4k	8k	20k	2k	
Jobs	2k	14k	3k	6k	10k	20	2k	
Time for Execution	4	10	2	1	1	8	6	
a) 19 units of time c) 18 units of time 3. Consider the relet there be two besize 350K. Assume Partitioning. Which of the followed requests? a) Best fit but not first to Both First fit & Best	equests frolocks of the memor owing part	om proce memory a y alloca	vailable (tion tech	iven order of size 19 nique to 19 schemes ca	oftime r 300K, 2 50K follo be used i	wed by a s Dynamic y the abo it.	block	
4. Select all the exhaustive searche		ategies	_	free space	ce in whi	ch we can	avoid	
a) Best Fit	b) Wo	rst Fit	·	c) First Fit		d)	Next Fit	
5.Fill in the blar Using the approach application has or a separate list ju	iks: of ie (or a f ist to mar	ew) popu	lar-sized	e memory a requests at size.	allocatio that it	makes, we	'll keep	
forwarded to a mor	e generat	. шешогу	attocator	•				

(Choose the correct option)

6. In the following problem description, consider holes as synonyms for free memory spaces.

Problem Description:

Consider allocation of memory to a new process. Assume that none of the existing holes in the memory will exactly fit the process's memory requirement. Hence, a new hole of smaller size will be created if allocation is made in any of the existing holes. Which one of the following statements is TRUE?

- a) The hole created by the first fit is always larger than the hole created by the next fit.
- b) The hole created by the worst fit is always larger than the hole created by the first fit.
- c) The hole created by the best fit is never larger than the hole created by the first fit.
- d) The hole created by the next fit is never larger than the hole created by best fit.

(Base of a Segment)

- 7. What does base of a segment contain?
 - a) The capacity or size of segment
 - b) Contains the starting address where process is loaded at physical memory
 - c) Stores the starting address of virtual address space of memory
 - d) None of these

(Segmentation Fault)

- 8. Which of the following can cause a segmentation fault?
 - a) The data of segment bases are corrupted
 - b) A process requesting an address which exist absolutely fine.
 - c) A process accessing stack segment instead of heap segment
 - d) A process accessing illegal address, which is beyond the bounds of address space assigned to process

(Code Sharing)

- 9. What are the bits introduced, per segment, to implement code sharing in memory management module of OS?
 - a) Protection bits

b) Read-write bits

c) Reading bits

d) Permission bits

(Mapping to Code Segment)

10. Select the correct option for the following statement:

To find which code segment the virtual address belongs to, we would always need 2 bits.

a) True

b) False

(Choose the correct option)

11. Select the correct option for the following statement:

a) True	b) False	
(Components of 12. Select all the components in which process is initially broken into:	of Virtual Address) h the virtual address gene	rated by a
a) the virtual page number (VPN)	b) physical frame number	r (PFN)
c) the offset within the page	d) page table entry (PTE)	
(Address	s Translations)	
13. Fill in the blank:		
A stores virtual-to-physic system know where each page of an add memory.		
a) page table entry (PTE) b) page table	c) page frame	d) page offset
(Physical 14. Which of the following refers to	Memory Divisions) the fixed-sized units that	the physical
memory is divided into?	the rixed sized unites that	the physical
a) Pages b) Page Tables	c) Page Frames	d) Segments
(Virtual Addres	s to Physical Address)	
15. For a process of 64 B virtual add	lress and 128 B physical ad	dress and
15. For a process of 64 B virtual add following page table:	ress and 128 B physical ad	dress and
	ress and 128 B physical ad Physical Pag	
following page table:		
following page table: Virtual Page Number	Physical Pag	
Virtual Page Number	Physical Pag	
Virtual Page Number 0 1	Physical Pag 3 7	
Virtual Page Number 0 1 2	Physical Pag 3 7 5 2	
Virtual Page Number 0 1 2 3	Physical Pag 3 7 5 2	
Virtual Page Number Virtual Page Number Under the page Number External Frage Number (External Frage Number Numb	Physical Pag 3 7 5 2 virtual address 37? c) 89 gmentation in Paging) following statement:	e Frame
Virtual Page Number Virtual Page Number What will be the physical address for a) 75 b) 85 (External Frage)	Physical Pag 3 7 5 2 virtual address 37? c) 89 gmentation in Paging) following statement:	e Frame
Virtual Page Number Virtual Page Number What will be the physical address for a) 75 b) 85 (External Fraguentation is avoided when a) True	Physical Pag 3 7 5 2 virtual address 37? c) 89 gmentation in Paging) following statement: en paging is used. b) False ow Paging!)	e Frame d) 95
Virtual Page Number Virtual Page Number What will be the physical address for a) 75 b) 85 (External Frage Number of the External fragmentation is avoided when a) True (Slot 17. What is the most accurate descripes of the state of the st	Physical Pag 3 7 5 2 virtual address 37? c) 89 gmentation in Paging) following statement: en paging is used. b) False ow Paging!)	e Frame d) 95

d) Too many memory references to access the actual data stored

(Fill the blank cells in table)

18. The following table has filled and unfilled cells. There are few unfilled cells that are marked with characters. Find the values of these characters, by completing the table.

Size of Virtual Address Space	Size of Physical Address Space	Number of bits required for Virtual Address	Number of bits required for Physical Address	Page Size	Number of bits required for Page offset	Number of Pages	Number of Frames
128 KB	128 KB	U		4 KB	W		
256 KB	1 MB		Х	4 KB	Υ		
1 MB			Z		10		2256

a) U=17, W=12, X=20, Y=10, Z=18

b) U=17, W=12, X=20, Y=12, Z=18

c) U=17, W=10, X=20, Y=12, Z=18

d) U=17, W=10, X=20, Y=10, Z=18

(What is TLB?)

19. "TLB is a hardware cache which stores the contents of a Page table. CPU always checks TLB first, before moving to the page table in case of page miss. If the memory reference is made for the first time in TLB, then it always results in Page Miss."

Select the correct option for the above statement.

a) True

b) False

(TLB Hit)

- 20. What is the most accurate description of TLB hit?
 - a) The TLB contains the mapping for the requested physical address
 - b) The TLB contains the mapping for the requested virtual address
 - c) The TLB checks and confirms whether this virtual address is accessible
 - d) None of the above

(Context Switching Support)

- 21. Which of the following addition is done to provide hardware support for sharing of TLB across context switching?
 - a) Valid bit

b)Protection bit

c) Permission bit

d) Address space identifier (ASID)

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(Absent in Physical Memory)

- 22. What is the term used for using swap space for accessing a page, which is not present in physical memory?
 - a) Segmentation Fault

b) Page Fault

c) Page Miss

d) Swap Space Hit

(Page Faults)

23. Consider the following page trace:

4,3,2,1,4,3,5,4,3,2,1,5

Determine the number of page fault that would occur if FIFO Page Replacement Algorithm is used with number of frames for the job m=4 will be

a) 8

b) 10

c) 12

d) 9

(LRU - I)

24. An LRU page Replacement is used with four page frames and eight pages. How many page faults will occur with reference string 0172237103 if the four frames are initially empty?

a) 5

b) 6

c) 7

d) 8

(LRU - II)

25. How many hits occur in LRU page replacement algorithm for the given reference string with four page frames

7,0,1,2,0,3,4,0,3,2,0,1?