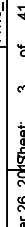
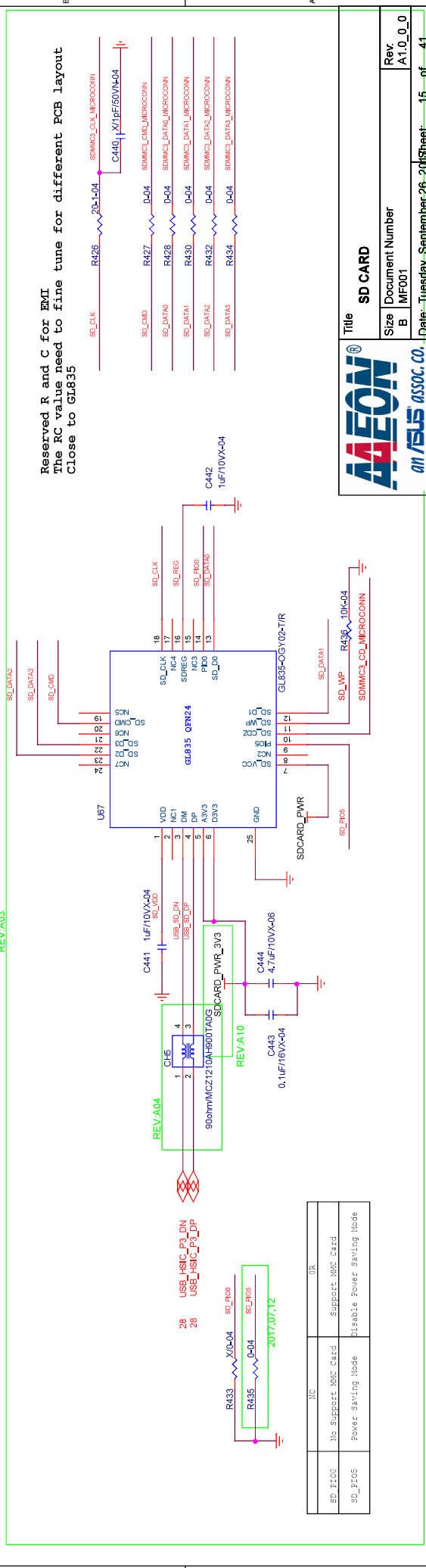
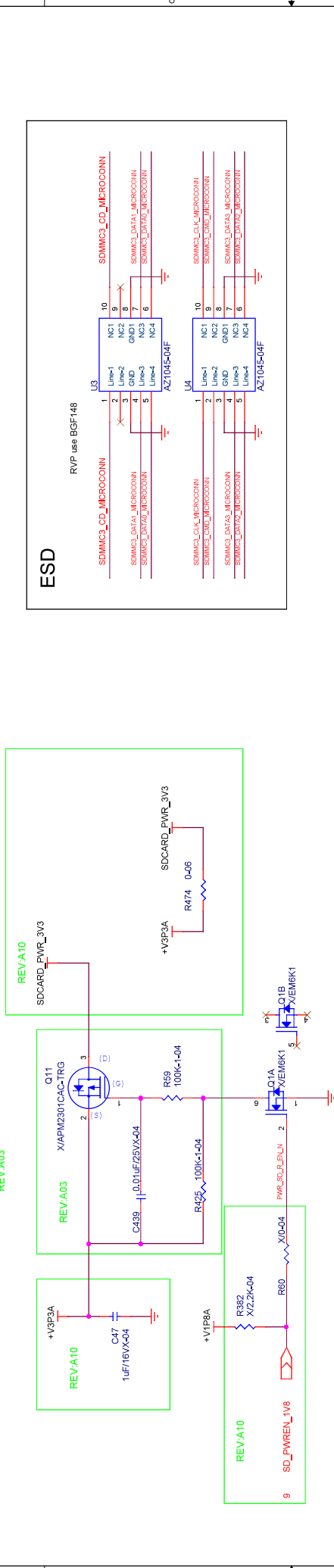
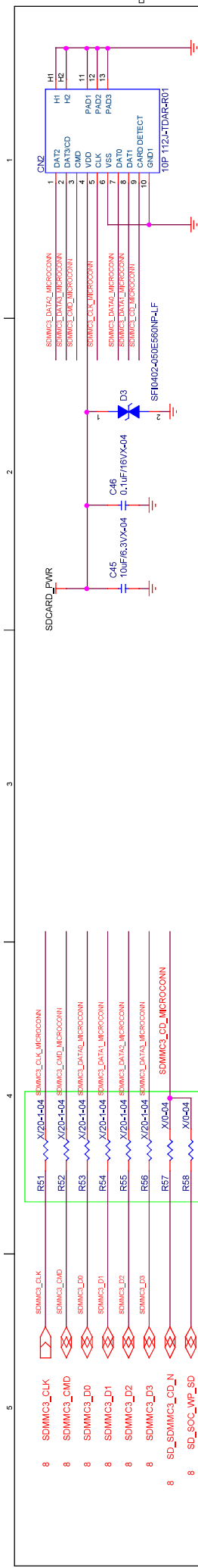


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01	INDEX	21										LEVEL SHIFT CIRCUITRY										41	HISTORY																																				
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03	POWER TREE	23										EMMC																																															
04	POWER SEQUENCE	24										BNO055																																															
05	SOC MEMORY	25										WIFI_RT5572N																																															
06	SOC POWER1	26										BT_CSR CRS8510																																															
07	SOC DISPLAY/CAMERA	27										USB3.0_TYPE-A																																															
08	SOC PCIE/SDMMC/I2S AUDIO	28										USB HSIC																																															
09	SOC USB/HSIC/I2C/ISH	29										RTL8111G-CG																																															
10	SOC CLK/SPI/GPIO/HSUART	30										EDP TO HDMI																																															
11	SOC GND	31										AUDIO1																																															
12	SOC POWER2	32										AUDIO2																																															
13	SOC DECOUPLING CAPS	33										FUNCTION CONN																																															
14	SOC POWER TRIBUTARIES	34										3V VOLTAGE REGULATOR																																															
15	SD CARD	35										PMIC 1of3/CONTROL																																															
16	HARDWARE STRAP	36										PMIC 2of3/SWITCHING POWER																																															
17	POWER & RESET PIN HEADER	37										PMIC 1of3/CLDO																																															
18	MEMORY 1/2	38										VTT VOLTAGE REGULATOR																																															
19	MEMORY 3/4	39										DC IN 26 PIN CONNECTOR																																															
20	DDR3L-RS VREF&Termination	40										MECHANICAL COMPONENT																																															
												 an ASUS ASSOC. CO.												Title												Index																							
																								Size												Document Number												Rev:											
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																								Date: Tuesday, September 26, 20												Sheet: 1												of 41											
5												4												3												2												1											







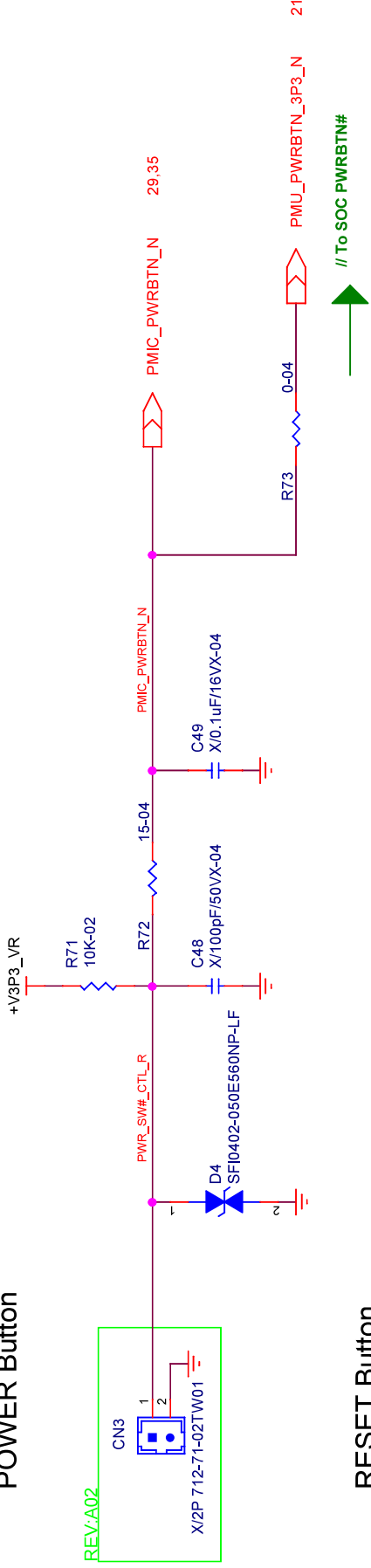


Reserved R and C for EMI  
The RC value need to fine tune for different PCB layout  
Close to GL835

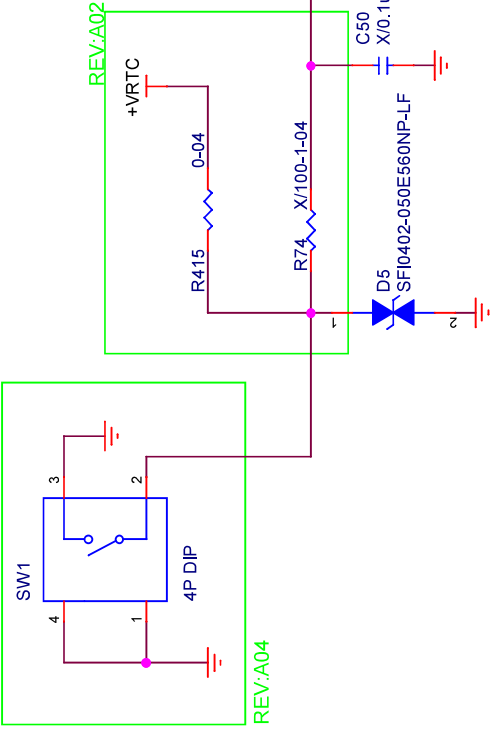
Title	
SD CARD	
Size	Document Number
B	MF001
Date	Rev.
2017/07/12	A1.0_0.0

Date	
Tuesday, September 26, 2017	
Page	Sheet
15	41

# POWER Button



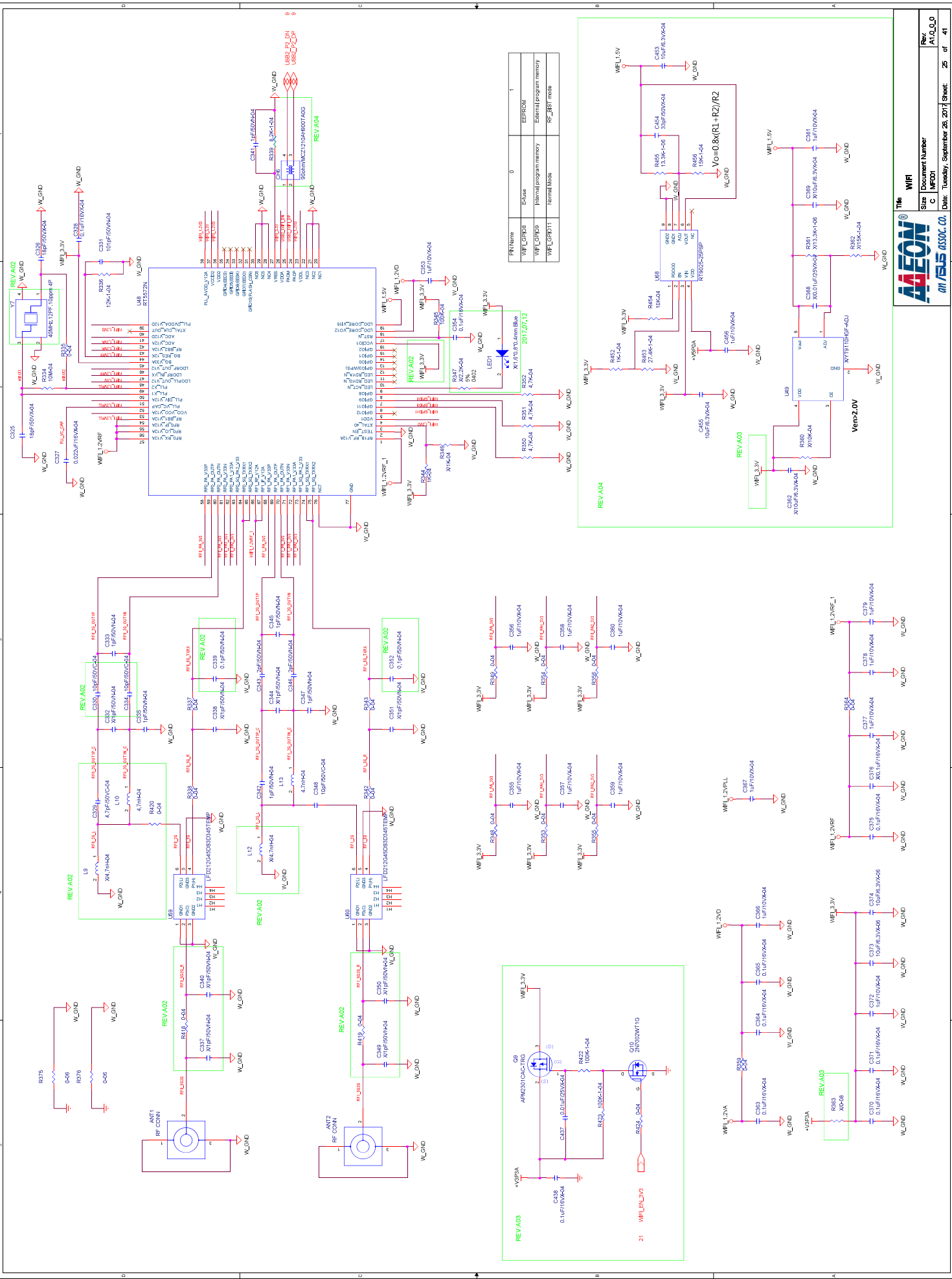
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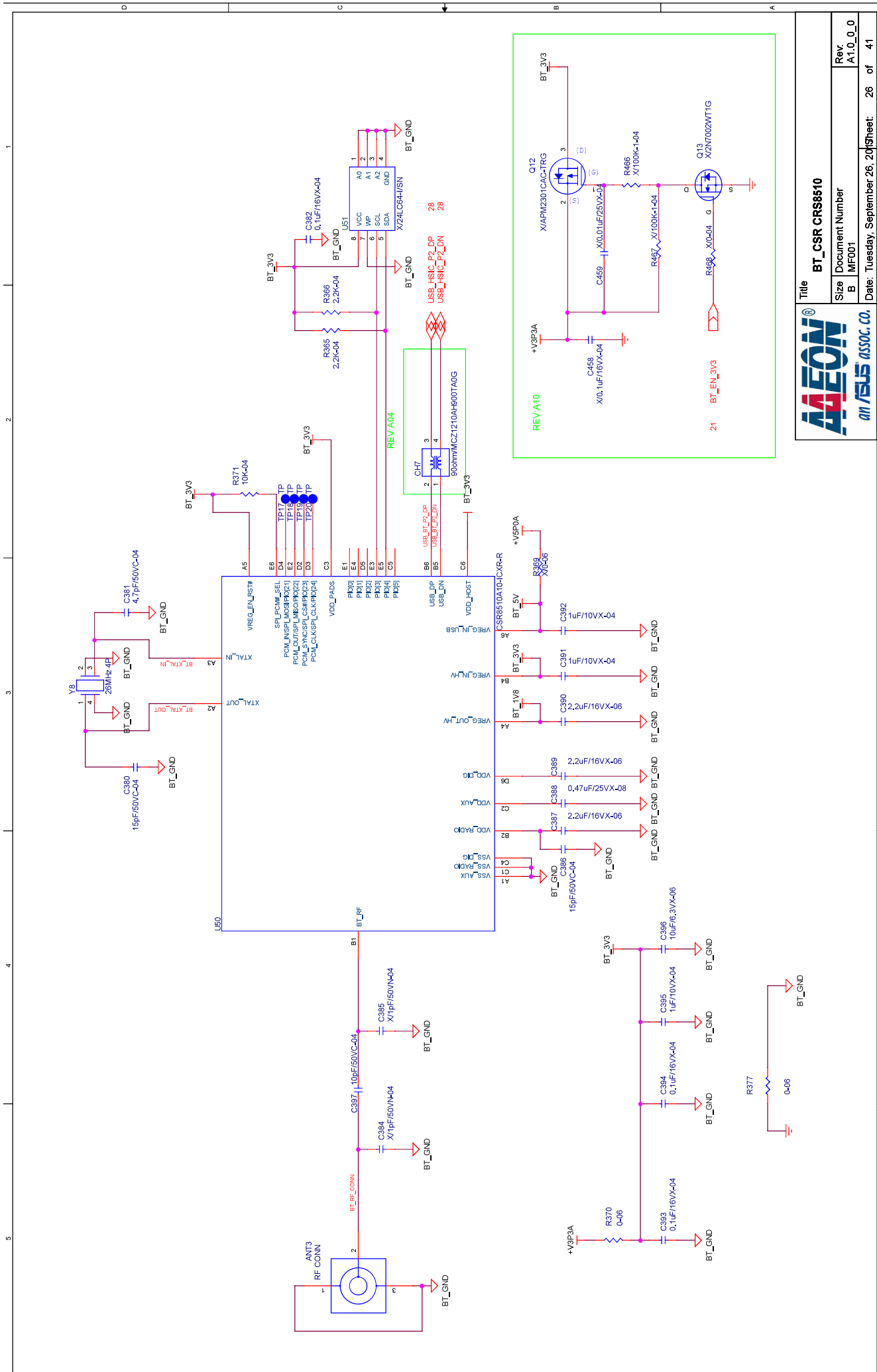
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Title	Size	Document Number	Rev:
	A	MF001	A1.0_0_0
Date: Tuesday, September 26, 20		Sheet: 17	of 41







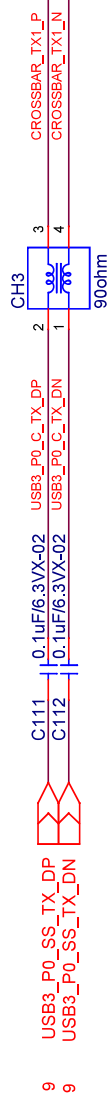
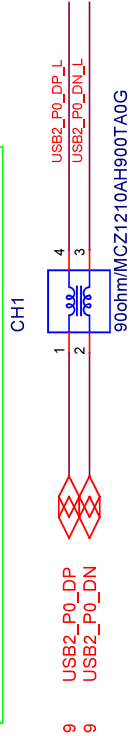
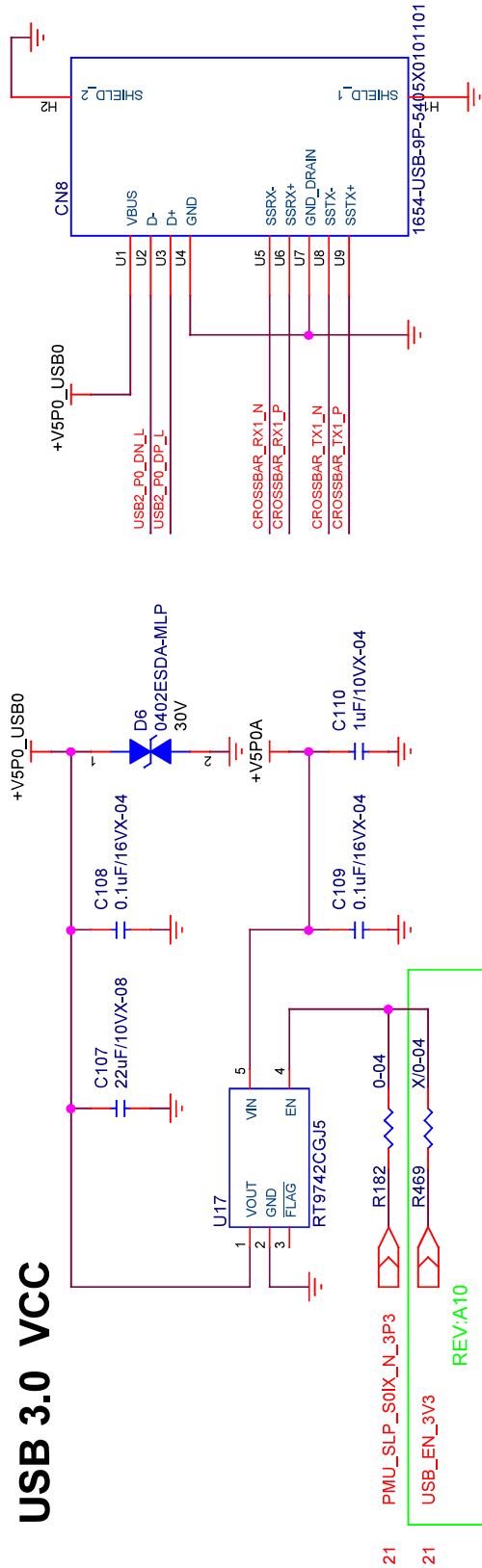




Title		BT_CSR CRS8510	
Size	Document Number	Rev.	
B	MF001	A1.0_0.0	
Date:	Tuesday, September 26, 20	Sheet	26 of 41



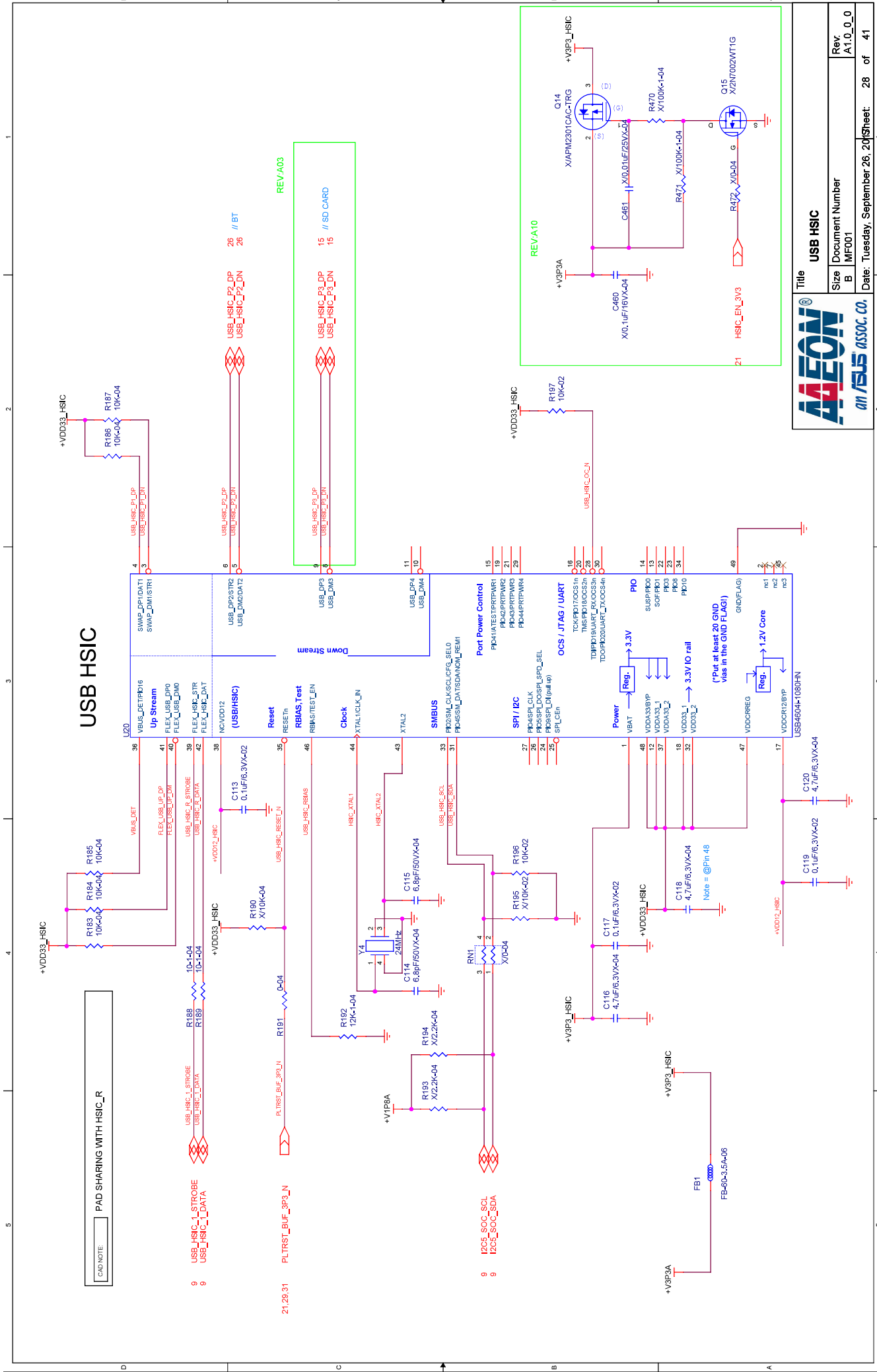
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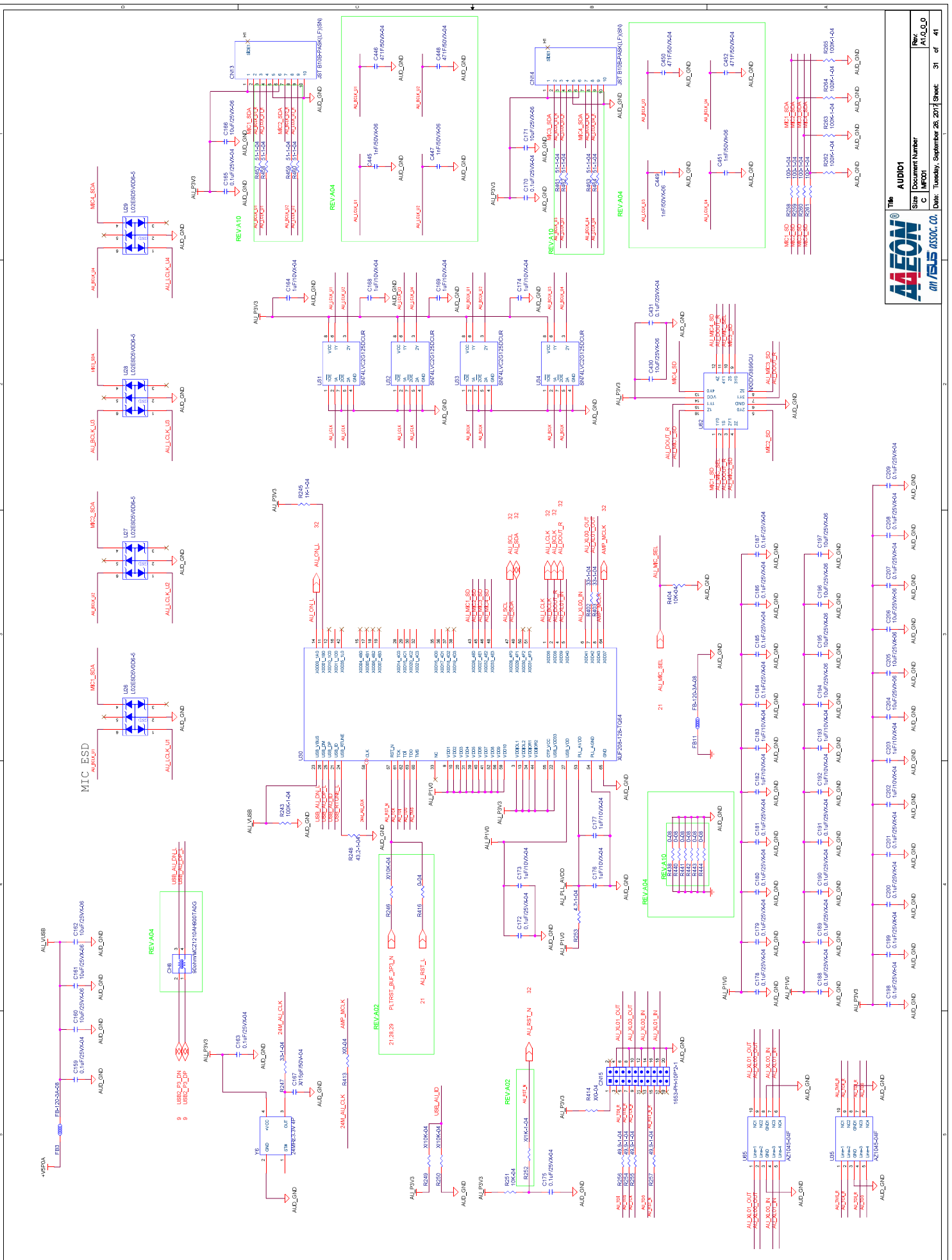


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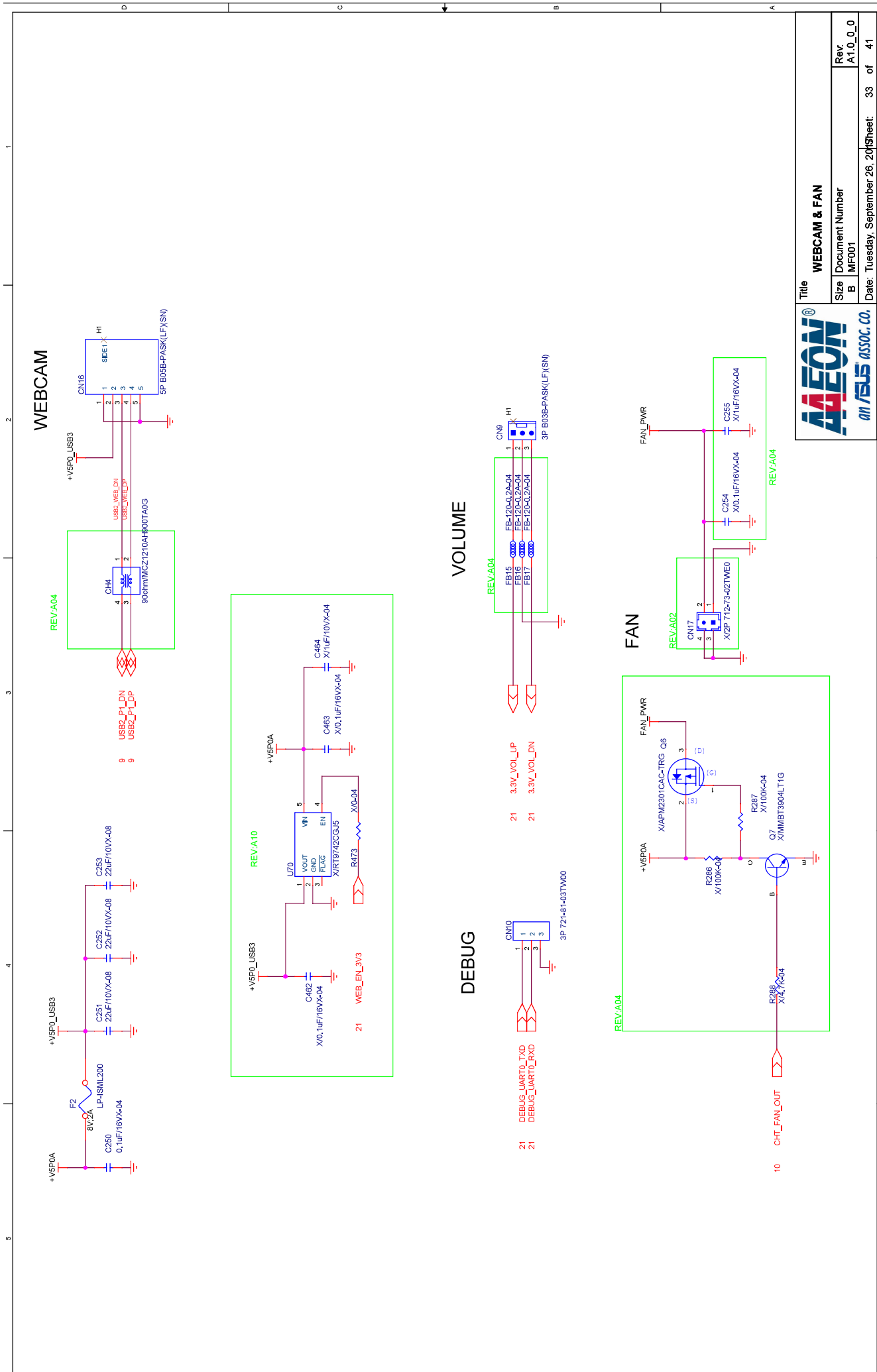
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Date: Tuesday, September 26, 2017 Sheet: 27 of 41



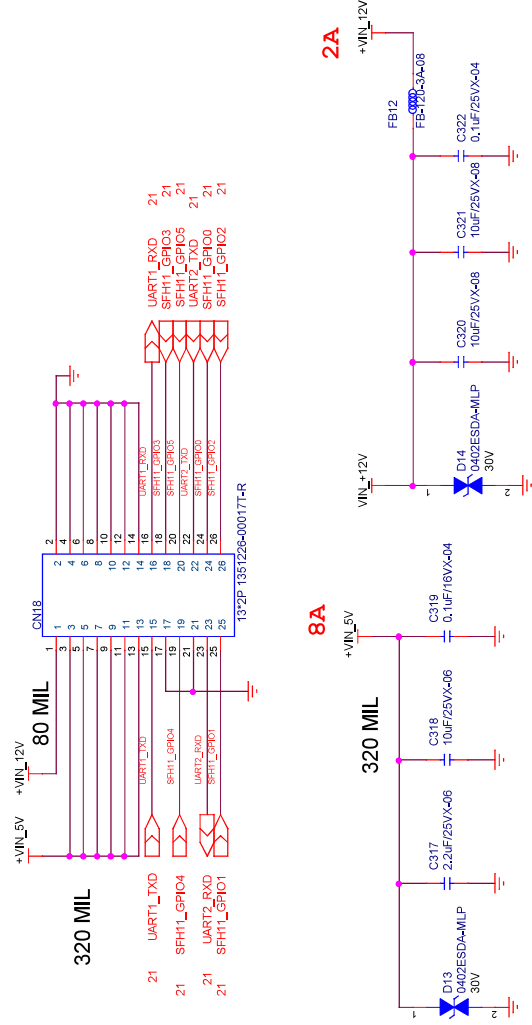




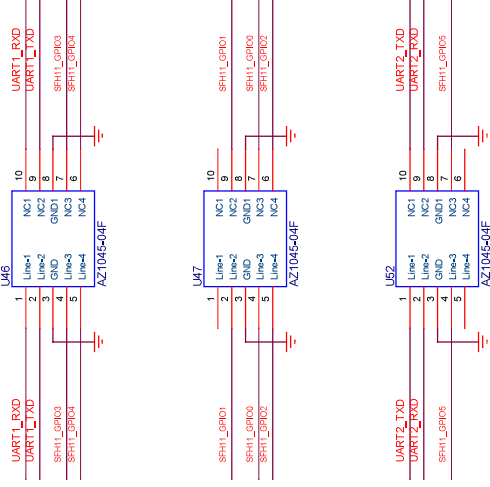


Title			WEBCAM & FAN		
Size	Document Number	Rev.			
B	MF001	A1.0_0.0			
Date: Tuesday, September 26, 20		Sheet	33	of	41





ESD



Title		DC IN 26 PIN CONNECTOR	
Size	B	Document Number	MF001
Date	Tuesday, September 26, 20	Sheet	39 of 41

