

NOTES:
1. THIS SCHEMATIC REFERENCES PCB VERSION: D

CHANGE LOG:
REV D:
1. LR SIGNAL NOW FIXED TO GND.
2. ADDED 470pF CAPACITOR C3 TO SDA OUTPUT.
3. ADDED 49.9 OHM RESISTOR R2 TO SDA OUTPUT.
4. RESISTOR R4 REMOVED FROM BOM.

A

A

B

B

C

C

D

D

