Instrução	Tipo	Formato	Opcode	Funct	AluOp	0xOpcode	0xFunct			ALUOperation	OperationPos		
	R	rd=rs+rt	000000	100000	0					ADD			
	R	rd=rs&rt	000000	100100	2					SUB	1		
	R	rd=rt< <sa< td=""><td>000000</td><td>000000</td><td>6</td><td></td><td></td><td></td><td></td><td>AND</td><td>2</td><td></td><td></td></sa<>	000000	000000	6					AND	2		
	R	rd=rt>>sa	000000	000010	7					OR	3		
	R		000000	100010	1						4		
		rd=rs-rt								XOR			
	R	rd=rs rt	000000	100101	3					NOR			
	R	rd=rs⊕rt	000000	100110	4					SLL	6		
Nor	R	rd=rs⊽rt	000000	100111	5	0	27			SRL	7	•	
Set Less Than	R	rd=rs <rt< td=""><td>000000</td><td>101010</td><td>8</td><td>0</td><td>2A</td><td></td><td></td><td>SLT</td><td>8</td><td>3</td><td></td></rt<>	000000	101010	8	0	2A			SLT	8	3	
Jump Register	R	pc=rs	000000	001000		0	8			EQUAL	9)	
Add Immediate	1	rt=rs+imm	001000	N/A	0	С	N/A						
	1	rt=rs&imm	001100	N/A	2								
	1	rt=rs imm	001101	N/A	3	D							
	1	rt=*(int*)(offset+rs)	100011	N/A	0								
	1	*(int*)(offset+rs)=rt	101011	N/A	0								
	1	if(rs==rt) pc+=offset*4	000100	N/A	9								
Branch on Not Equal	1	if(rs!=rt) pc+=offset*4	000101	N/A	9	5	N/A						
Set Less Than Immediate	I	rt=rs <imm< td=""><td>001010</td><td>N/A</td><td>8</td><td>Α</td><td>N/A</td><td></td><td></td><td></td><td></td><td></td><td></td></imm<>	001010	N/A	8	Α	N/A						
Jump	J	pc=pc_upper (target<<2)	000010	N/A		2	N/A						
	J	\$31=pc;pc +=i<<2	000011	N/A		3							
				,,,			.,,,,						
ALUOpDecode	Por Opcode							Por Funct					
Instrução	Opcode	bit3	bit2	bit1	bit0		Instrução	Funct	bit3	bit2	bit1	bit0	
Add Immediate	001000		0		0		Add	100000					
And Immediate													
	001100		0	1	0		And	100100					
Or Immediate	001101		0	1	1		Shift Left Logical	000000					
Load Word	100011		0	0	0		Shift Right Logical					1	
Store Word	101011	0	0	0	0		Sub	100010	(() (1	
Branch on Equal	000100	1	0	0	1		Or	100101	((1	1	
Branch on Not Equal	000101	1	0	0	1		Xor	100110		1		0	
Set Less Than Immediate	001010	1	0	0	0		Nor	100111	(1		1	
							Set Less Than	101010					
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