# Lecture 8 Learning outcomes

Upon successful completion of this module, you will be able to:

- Describe the hierarchical structure of memory
- Explain the rationale behind the memory hierarchy
- Describe the components and functionality of a direct-mapped cache
- Calculate cache size and cache performance

# Lecture 8 Activities

Lecture 8 is 1-day work. Use the last day for Test 3 and team project.

### Day 1

#### Session I:

View Lecture 8a: Basics of Cache Study zyBook 5.1, 5.2, 5.3

#### **Session II:**

View Lecture 8b: Cache Performance

Study zyBook 5.4

#### **Session III:**

Recap and practice

#### Day 2

Work on team project Review Lecture 6 and 7 Take Test #3

## **Assignment Checklist:**

P5 and Z10 (zyBook 5.1-5.4) -- due Tu 7/6
Test 3 (Th 7/1 – Tu 7/6)
Team project – due Tu 7/6