

# Mutley's Guide To The Wacky Racers, V.011E-3

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# Chapter 1

## Hardware

### 1.1 Introduction

An hour spent checking your schematic will avoid many hours of testing grief!

#### 1.1.1 Recommendations

1. Independently fuse the buck converter and H-bridge. This allows a fuse to be removed to isolate part of the circuit when finding a fault, such as a short across the power rails.
2. Have a zener diode to protect against overvoltage input from a bench power supply.
3. Have current limiting resistors for all off-board signals.
4. Have plenty of testpoints, especially for power supplies and signals. You will never have too many!
5. Have at least one grunty ground testpoint for attaching a scope ground clip.
6. Have a dedicated PIO pin to drive a testpoint that you can use to trigger an oscilloscope for debugging.
7. Have the SAM4S erase pin connected to a test point close to a 3.3 V testpoint. This is useful to completely erase the SAM4S flash memory when nothing works.

### 1.2 SAM4S MCU

The SAM4S MCU is overkill for this assignment but is typical of ARM processors used for bare-metal applications.

#### 1.2.1 Power pins

The SAM4S has four grounds. They must all be connected. There are also seven power pins. These must all be connected since they power different parts of the chip. Note, some pins require 3.3 V while others require 1.2 V. The 1.2 V is generated by an internal voltage regulator.

### 1.2.2 Peripheral pins

**Many of the peripheral pins are dedicated and cannot be reassigned in software**, e.g., SPI, TWI, and USB pins. Note, there are restrictions on the PWM pins.

By default the PB4 and PB5 pins are configured for the JTAG debugger. These can be used for general PIO after setting an internal bit. See disabling JTAG pins.

The logic levels are set by the voltage on the VDDIO pin (usually 3.3 V). PA12–PA14 and PA26–PA31 can sink/source 4 mA. The USB pins (PB10–PB11) can sink/source 30 mA. The rest can only sink/source 2 mA of current

### 1.2.3 USART/UART

The SAM4S has two USARTs and two UARTs. The USARTs can emulate a UART, have hardware flow control, and have a better driver so they are recommended if you need a UART interface.

### 1.2.4 PWM

The SAM4S can generate four independent PWM signals. There are restrictions on which SAM4S pins they come out on. Note, the PWMLx and PWMHx signals are complementary.

### 1.2.5 TWI

The SAM4S has two TWI peripherals (that can act as a master and a slave) with dedicated TWD and TWCK pins. External pull-up resistors are required. TWI1 shares pins with JTAG; you will need to disable JTAG in software.

### 1.2.6 SPI

The SAM4S has a single SPI peripheral with dedicated SCK, MISO, and MOSI pins. Any PIO pin can be used for the chip select<sup>1</sup>.

### 1.2.7 ADC

The SAM4S has a single ADC with a multiplexer to select one of a number of analogue inputs. It can sample at 1 MHz.

### 1.2.8 USB

The SAM4S has a single USB peripheral connected to the DDP and DDM pins. 27 ohm series termination resistors are required, placed close to the SAM4S.

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<sup>1</sup>For high speed operation, you should use one of the dedicated chip select pins.

## 1.3 Other chips

### 1.3.1 DRV8833 dual H-bridge

The capacitor connected to the bootstrap pin must be rated for 16 V. The datasheet recommends an X7R dielectric.

If you want control over fast decay and slow decay in both forward and reverse you will need four PWM signals. The SAM4S can provide four independent PWM signals but be careful that PWMxH and PWMxL are driven with the same PWM signal but are complementary.

You can drive the H-bridge with only two PWM signals but you will have fast decay in one direction and slow decay in the other. It appears that fast decay is better at slow speeds since the motor heats less.

### 1.3.2 NRF24L01+ radio

This operates around 2.4 GHz and has 128 programmable channels, each of 1 MHz. A 5 byte address is appended to the start of each transmission and the receiver will only respond when the address matches.

The radio interfaces to the SAM4S using the SPI bus. The IRQ pin is driven low to indicate a packet has been received.

A well filtered power supply is critical otherwise the range will be limited on reception. Preferably, the device should have its own 3V3 regulator with a low-pass RC filter comprised of a series resistor and large capacitor (say 22 uF) or a low-pass LC filter made with a ferrite bead and capacitor.

### 1.3.3 MPU9250 IMU

This contains a three axis accelerometer, a three axis gyroscope, and a three axis magnetometer. It appears that the magnetometer has been bolted on to the accelerometers/gyroscopes and requires more hoop jumping in software to make it work.

It has two different I2C addresses (0X68 and 0X69) depending on the state of the AD0 pin.

### 1.3.4 Voltage regulators

There are many flavours of voltage regulator. Some are better for digital applications, some are better for analogue applications, some are better for low power applications, etc.

If you are using a voltage regulator with an enable pin, do not forget to allow for the time for the output voltage to ramp up. This can be tens of milliseconds depending on the capacitive load and current draw.

Note, some regulators have pins that you must not connect. Some have multiple pins for the same purpose; these must all be connected.

## 1.4 PCB recommendations

### 1.4.1 Placement

1. Keep small signal analogue components (radio) well away from digital electronics and power electronics.
2. Place local decoupling capacitors to minimise the loop area.
3. Keep the crystal close to the MCU.
4. Place switches so they can be pushed.
5. Place LEDs so they can be seen.
6. Place USB connector so it can be connected to.
7. Place connectors on edge with wires going away from the board.

### 1.4.2 Check list

1. No planes under the radio antenna.
2. The SPI signals for the radio are connected to the correct MCU pins.
3. The PWM signals for the motor are connected to the correct MCU pins. Note, the PWMLx and PWMHx pins are complementary and cannot be driven independently.
4. All the MCU VDD pins need to be powered.
5. All the MCU GND pins need to be powered.
6. Avoid connecting to PB4 and PB5 (say for TWI1). If you do you will need to disable JTAG.

# Chapter 2

## Software

### 2.1 Preparation

Do not underestimate the effort required to flash your first LED. You require:

- A computer with git installed and a useful shell program such as bash. UC has a mirror for a variety of Linux distributions; we recommend Ubuntu or Mint.
- A working ARM toolchain (arm-none-eabi-gcc/g++ version 4.9.3 or newer).
- OpenOCD V0.8.0 or later.
- An ST-link programmer and 10-wire ribbon cable for programming. You can get the adaptor from Scott Lloyd in the SMT lab. You will need to make your own cable (For a grey ribbon cable, align the red stripe with the small arrow denoting pin 1 on the connector. For a rainbow ribbon cable, connect the brown wire to pin 1.). There are two variants of the ST-link programmer with **different pinouts** so you may need to customise your programming cable.
- Plenty of gumption.

#### 2.1.1 Toolchain

The required software is installed on computers in the ESL and CAE. It should run under both Linux and Windows. If there is a problem ask the technical staff.

If you are working from a personal Linux computer make sure to update your machine to the latest software versions. This is particularly important for the ARM toolchain (arm-none-eabi-gcc/g++) which should be at version 4.9.3 or newer. You can do this with the following command for an Ubuntu or Mint distribution:

```
$ sudo apt update && sudo apt upgrade}
```

For MacOS machines that have homebrew installed, you can use the following command:

```
$ brew install openocd git}
$ brew cask install gcc-arm-embedded}
```

### 2.1.2 Project forking

The example software is hosted on the eng-git Git server at <https://eng-git.canterbury.ac.nz/wacky-racers/wacky-racers-2021>. Your group leader should have forked this template project. This creates your own group copy of the project on the eng-git server that you can modify, add members, etc. To fork the project:

1. Go to <https://eng-git.canterbury.ac.nz/wacky-racers/wacky-racers-2021>.
2. Click the ‘Fork’ button. This will create a copy of the main repository for the project.
3. Click on the ‘Settings’ menu then click the ‘Expand’ button for ‘Sharing and permissions’. Change ‘Project Visibility’ to ‘Private’.
4. Click on the ‘Members’ menu and add group members as Developers.

### 2.1.3 Project cloning

Once your project has been forked from the template project, each group member needs to clone it. This makes a local copy of your project on your computer. **If inside the university, you will need to enable the internet** since two git submodules are required.

If you are using an ECE computer, it is advised that you clone the project on to a removable USB flash drive. This will make git operations and compilation a 100 times faster than using the networked file system.

There are two ways to clone the project. If you are impatient and do not mind having to enter a username and password for every git pull and push operation use:

```
$ git clone
↪ https://eng-git.canterbury.ac.nz/groupleader-userid/wacky-racers-2021.git
↪ wacky-racers
```

Otherwise, set up ssh-keys and use:

```
$ git clone git@eng-git.canterbury.ac.nz:groupleader-userid/wacky-racers-2021.git
↪ wacky-racers
```

Note, the recursive option should clone the submodules mmculib and mat91lib from github.com.

You can several different cloned copies of your project in different directories. Sometimes if you feel that the world, and git in particular, is against you, clone a new copy, using:

```
$ git clone
↪ https://eng-git.canterbury.ac.nz/groupleader-userid/wacky-racers-2021.git
↪ wacky-racers-new
```

### 2.1.4 Configuration

Each board has different PIO definitions and requires its own configuration information. The `wacky-racers/src/boards` directory contains a number of configurations; one for the hat and racer boards. Each configuration directory contains three files:

- `board.mk` is a makefile fragment that specifies the MCU model, optimisation level, etc.
- `target.h` is a C header file that defines the PIO pins and clock speeds.



- `config.h` is a C header file that wraps `target.h`. It's purpose is for porting to different compilers.

You will need to edit the `target.h` file for your board and set the definitions appropriate for your hardware. Here's an excerpt from `target.h` for a hat board:

```
/* USB */
#define USB_VBUS_PIO PA5_PIO

/* ADC */
#define ADC_BATTERY PB3_PIO
#define ADC_JOYSTICK_X PB2_PIO
#define ADC_JOYSTICK_Y PB1_PIO

/* IMU */
#define IMU_INT_PIO PA0_PIO

/* LEDs */
#define LED1_PIO PA20_PIO
#define LED2_PIO PA23_PIO
```

## 2.2 First program

Your first program to test your board should only flash an LED (the hello world equivalent for embedded systems). The key to testing new hardware is to have many programs that only do one simple task each.

### 2.2.1 OpenOCD

OpenOCD is used to program the SAM4S. For this assignment, we are using a ST-link programmer to connect to the SAM4S using serial wire debug (SWD). This connects to your board with a 10-wire ribbon cable and an IDC connector.

1. Before you start, disconnect the battery and other cables from your PCB.
2. Connect a 10-wire ribbon cable from the ST-link programmer to the programming header on your PCB. This will provide 3.3 V to your board so your green power LED should light.
3. Open a **new terminal window** and start OpenOCD.

```
$ cd wacky-racers
$ openocd -f src/mat91lib/sam4s/scripts/sam4s_stlink.cfg
```

All going well, the last line output from OpenOCD should be:

```
Info : sam4.cpu: hardware has 6 breakpoints, 4 watchpoints
```

Congrats if you get this! It means you have correctly soldered your SAM4S. If not, do not despair and do not remove your SAM4S. Instead, see troubleshooting.

### 2.2.2 LED flash program

For your first program, use `wacky-racers/src/test-apps/ledflash1/ledflash1.c`. The macros `LED1_PI0` and `LED2_PI0` need to be defined in `target.h` (see configuration). If your LEDs are active high, replace 1 with 0 in the LED config structures.

```
/* File:    ledflash1.c
   Author:  M. P. Hayes, UCECE
   Date:    25 Feb 2018
   Descr:
*/
#include "led.h"
#include "pacer.h"

#define PACER_RATE 10

int main (void)
{
    led_cfg_t led1 = {LED1_PI0, 1};
    led_cfg_t led2 = {LED2_PI0, 1};

    pacer_init(PACER_RATE);
    led_init(&led1);
    led_init(&led2);

    led_set (&led1, 1);

    while (1)
    {
        pacer_wait ();
        led_toggle (&led2);
    }
}
```

### 2.2.3 Compilation

Due to the many files required, compilation is performed using makefiles.

The demo test programs are generic and you need to specify which board you are compiling them for. The board configuration file can be chosen dynamically by defining the environment variable `BOARD`. For example:

```
$ cd src/test-apps/ledflash1
$ BOARD=racer make
```

If all goes well, you should see at the end:

text	data	bss	dec	hex	filename
19432	2420	2732	24584	6008	ledflash1.bin

To avoid having to specify the environment variable `BOARD`, you can define it for the rest of your session using:

```
$ export BOARD=racer
```

and then just use:

```
$ make
```

Note, if you compile with the `BOARD` environment variable set incorrectly, use:

```
$ make clean
```

```
$ make
```

to delete all the object files and rebuild.

### 2.2.4 Booting from flash memory

By default the SAM4S runs a bootloader program stored in ROM. The SAM4S needs to be configured to run your application in flash memory.

If OpenOCD is running you can do this with:

```
$ make bootflash
```

Unless you force a complete erasure of the SAM4S flash memory by connecting the `ERASE` pin to 3.3 V, you will not need to repeat this command.

### 2.2.5 Programming

If OpenOCD is running you can store your program in the flash memory of the SAM4S using:

```
$ make program
```

When this finishes, one of your LEDs should flash. If so, congrats! If not, see troubleshooting.

To reset your SAM4S, you can use:

```
$ make reset
```

## 2.3 USB interfacing

To help debug your programs, it is wise to set up USB CDC. For example, here's the code for `wacky-racers/src/test-apps/usb_serial_test1/usb_serial_test1.c`.

```
/* File:    usb_serial_test1.c
   Author:  M. P. Hayes, UCECE
   Date:    22 Feb 2018
*/
#include <stdio.h>
#include "usb_serial.h"
#include "delay.h"

static usb_serial_cfg_t usb_serial_cfg =
{
    .read_timeout_us = 1,
    .write_timeout_us = 1,
```

```

;}

int main (void)
{
    int i;

    // Create non-blocking tty device for USB CDC connection.
    usb_serial_init (&usb_serial_cfg, "/dev/usb_tty");

    // Redirect printf to USB serial
    freopen ("/dev/usb_tty", "a", stdout);
    freopen ("/dev/usb_tty", "r", stdin);

    for (i = 0; i < 100; i++)
    {
        printf ("Hello world %d\n", i);
        fflush (stdout);
        delay_ms (100);
    }
}

```

Note, the string `"/dev/usb_tty"` is used to name the USB serial device on the SAM4S so that it can be used by the C stdio function `freopen`.

To get this programme to work you need to compile it and program the SAM4S using:

```

$ cd wacky-racers/src/test-apps/usb_serial_test1
$ make program

```

You then need to connect your computer to the USB connector on your PCB. If you are running Linux, run:

```

$ dmesg

```

This should say something like:

```

Apr 30 11:03:50 thing4 kernel: [52704.481352] usb 2-3.3: New USB device found, idVendor=03eb, idPr
Apr 30 11:03:50 thing4 kernel: [52704.481357] usb 2-3.3: New USB device strings: Mfr=1, Product=2,
Apr 30 11:03:50 thing4 kernel: [52704.482060] cdc_acm 2-3.3:1.0: ttyACM0: USB ACM device

```

Ignore the errors for now<sup>1</sup>. If you see `ttyACM0: USB ACM device` congrats. If not, see USB debugging.

You can now run a serial terminal program. For example, on Linux:

```

$ gtkterm -p /dev/ttyACM0

```

All going well, this will repeatedly print 'Hello world'.

If you get an error 'device is busy', it is likely that the ModemManager program has automatically connected to your device on the sly. This program should be disabled on the ECE computers. For more about this and using other operating systems, search for USB CDC on [ecewiki](http://ecewiki.org).

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<sup>1</sup>They have popped up with new Linux kernels and we have yet to determine why.

## 2.4 Test programs

There are a number of test programs in the directory `wacky-racers/src/test-apps`. Where possible these are written to be independent of the target board using configuration files (see configuration).

### 2.4.1 PWM test

The program `wacky-racers/src/test-apps/pwm_test2/pwm_test2.c` provides an example of driving PWM signals. Notes:

1. This is for a different H-bridge module that requires two PWM signals and forward/reverse signals. You will need to generate four PWM signals or be clever with two PWM signals.
2. The `pwm_cfg_t` structure configures the frequency, duty cycle and alignment of the output PWM.

The most likely problem is that you have not used a PIO pin that can be driven as a PWM output. The SAM4S can generate four independent hardware PWM signals. See `wacky-racers/src/mat91lib/pwm/pwm.c` for a list of supported PIO pins. Note, PA16, PA30, and PB13 are different options for PWM2.

To drive the motors you will need to use a bench power supply. Start with the current limit set at 100mA maximum in case there are any board shorts. When all is well, you can increase the current limit.

### 2.4.2 IMU test

The program `wacky-racers/src/test-apps/imu_test1/imu_test1.c` provides an example of using the MPU9250 IMU. All going well, this prints three 16-bit acceleration values per line to USB CDC. Tip your board over, and the the third (z-axis) value should go negative since this measures the effect of gravity on a little mass inside the IMU pulling on a spring.

If you get 'Cannot detect IMU' the main reasons are:

1. You have specified the incorrect address. Use MPU\_0 if the AD0 pin is connected to ground otherwise use MPU\_1.
2. You are using TWI1. The PB4 and PB5 pins used by TWI1 default to JTAG pins. See disabling JTAG pins.
3. You do not have TWI/I2C pull-up resistors connected to 3V3 or the wrong resistor values. Use a scope to look at the signals TWCK/SCL and TWD/SDA.
4. Check that the auxiliary I2C bus signals are not connected (otherwise the magnetometer will not respond).
5. You have a soldering problem. Try pressing on a side of the IMU with a fingernail to see if it starts working.

If you reset the SAM4S in the middle of a transaction with the IMU, the IMU gets confused and holds the TWD/SDA line low. This requires recycling of the power or sending out some dummy clocks on the TWCK/SCL signal.

### 2.4.3 Radio test

The program `wacky-racers/src/test-apps/radio_tx_test1/radio_tx_test1.c` provides an example of using the radio as a transmitter.

The companion program `wacky-racers/src/test-apps/radio_rx_test1/radio_rx_test1.c` provides an example of using the radio as a receiver.

Notes:

1. Both programs must use the same RF channel and the same address. Some RF channels are better than others since some overlap with WiFi and Bluetooth. The address is used to distinguish devices operating on the same channel. Note, the transmitter expects an acknowledge from a receiver on the same address and channel.
2. The radio ‘write’ method blocks waiting for an auto-acknowledgement from the receiver device. This acknowledgement is performed in hardware. If no acknowledgement is received, it retries for up to 15 times. The auto-acknowledgement and number of retries can be configured in software.

If you cannot communicate between your hat and racer boards, try communicating with the radio test modules Scott Lloyd has in the SMT lab.

## 2.5 Your hat/racer program

There is skeleton code, see `wacky-racers/src/wacky-apps/hat/hat.c` and `wacky-racers/src/wacky-apps/racer/racer.c`. We recommend that build your programs incrementally and that you poll your devices with a paced loop and not use interrupts. It is a good idea to disable the watchdog timer until you have robust code.

```
#include <stdio.h>
#include <math.h>
#include "mcu.h"
#include "pacer.h"

// Set to the desired polling rate.
#define PACER_RATE 1000

int
main (void)
{
    // If you are using PB4 or PB5 (say for TWI1) uncomment the next line.
    // mcu_jtag_disable ();

    pacer_init (PACER_RATE);

    mcu_watchdog_enable ();

    while (1)
    {
        pacer_wait ();
```

```
    mcu_watchdog_reset ();

    /* Do your stuff here... */

}

return 0;
}
```

## 2.6 Debugging

If OpenOCD is running, a running program can be debugged using:

```
$ make debug
```

This starts GDB and attaches to the target MCU. GDB is a command line debugger but there are many GUI programs that will control it, for example, vscode and geany have plugins.

GDB allows you to inspect the CPU registers, memory, set breakpoints, set watchpoints, and much more. The only gnarly thing is that when compiling with optimisation, the compiler may reorder statements or even remove them. Debugging embedded systems is also made difficult by the asynchronous nature of I/O.

You can reset your program using the GDB ‘jump reset’ command. However, this does not reset the peripherals as with a power-on reset.

## 2.7 Howtos

### 2.7.1 Disabling JTAG pins

By default PB4 and PB5 are configured as JTAG pins. You can turn them into PIO pins or use them for TWI1 using:

```
#include "mcu.h"

void main (void)
{
    mcu_jtag_disable ();
}
```

### 2.7.2 Watchdog timer

The watchdog timer is useful for resetting the SAM4S if it hangs in a loop. It is disabled by default but can be enabled using:

```
#include "mcu.h"

void main (void)
{
    mcu_watchdog_enable ();
}
```

```

while (1)
{
    /* Do your stuff here. */

    mcu_watchdog_reset ();
}
}

```

## 2.8 Under the bonnet

**wackylib** is a library of C files that tries to hide some of the complexity of the underlying software.

**mmculib** is a library of C drivers, mostly for performing high-level I/O. It is written to be microcontroller neutral.

**mat91lib** is a library of C drivers specifically for interfacing with the peripherals of Atmel AT91 microcontrollers such as the Atmel SAM4S. It provides the hardware abstraction layer.

The building is controlled by **wacky-racers/src/mat91lib/mat91lib.mk**. This is a makefile fragment loaded by **wacky-racers/src/mmculib/mmculib.mk**. **wacky-racers/src/mat91lib/mat91lib.mk** loads other makefile fragments for each peripheral or driver required. It also automatically generates dependency files for the gazillions of other files that are required to make things work.

## 2.9 Git

To properly use git you should commit and push often. The smaller the changes and the more often you make per commit, the smaller the chance of the dreaded merge conflict.

### 2.9.1 Git pulling from upstream

To be able to get updates if the template project is modified you will need to:

```

$ cd wacky-racers
$ git remote add upstream https://eng-git.canterbury.ac.nz/wacky-racers/wacky-racers-2021.git

```

Again if you do not want to manually enter your password (and have ssh-keys uploaded) you can use:

```

$ cd wacky-racers
$ git remote add upstream
↪ git@eng-git.canterbury.ac.nz:wacky-racers/wacky-racers-2021.git

```

Once you have defined the upstream source, to get the updates from the main repository use:

```

$ git pull upstream master

```

If you enter the wrong URL make a mistake, you can list the remote servers and delete the dodgy entry using:

```

$ git remote -v
$ git remote rm upstream

```



Note, **origin** refers to your group project and **upstream** refers to the template project that origin was forked from.

### 2.9.2 Git merging

The bane of all version control programs is dealing with a merge conflict. You can reduce the chance of this happening by committing and pushing faster than other people in your group.

If you get a message such as:

```
From https://eng-git.canterbury.ac.nz/wacky-racers/wacky-racers-2021
```

```
* branch          master      -> FETCH_HEAD
```

```
error: Your local changes to the following files would be overwritten by merge:
```

```
src/test-apps/imu_test1/imu_test1.c
```

```
Please, commit your changes or stash them before you can merge.
```

what you should so is:

```
$ git stash
```

```
$ git pull
```

```
$ git stash pop
```

```
# You may now have a merge error. You will now have to edit the offending file, in this ca.
```

```
# Once the file has been fixed
```

```
$ git add src/test-apps/imu_test1/imu_test1.c
```

```
$ git commit -m "Fix merge"
```

Sometimes when you do a git pull you will be thrown into a text editor to type a merge comment. The choice of editor is controlled by an environment variable **EDITOR**. On the ECE computers this defaults to emacs. You can changed this by adding a line such as the following to the **.bash\_profile** file in your home directory.

```
$ export EDITOR=geany
```

By the way, to exit emacs type ctrl-x ctrl-c, to exit vi or vim type :q!

**Please do not edit the files in the mat91lib, mmculib, and wackylib directories** since this can lead to merge problems in the future. If you find a bug or would like additional functionality let MPH or one of the TAs know.



## Chapter 3

# Troubleshooting

Troubleshooting can be frustrating when you are tired. You need plenty of gumption and an open mind. Difficult problems are usually a combination of two or more problems. So **do not attempt if you are tired or in a hurry** .

### 3.1 Oscilloscope

The best tool for debugging an embedded system is an oscilloscope.

1. Use x10 probes.
2. Use the correct probes for the scope so that they can be automatically detected.
3. Compensate the probes by clipping to the probe test signal on the scope and adjusting the variable capacitor in the probe to ensure a square wave without undershoot or overshoot. This is one of the few times you are allowed to use the autoscale button!

#### 3.1.1 Normal mode

This mode is for measuring transient or non-repetitive signals. It will only refresh the display if a trigger event is detected.

It is useful to increase the *trigger holdoff* to prevent triggering on multiple edges of a waveform. By default, it is set to a ridiculously small value.

#### 3.1.2 Auto mode

This mode is for measuring AC or DC signals. It is like normal mode but if it does not detect a trigger it will automatically refresh the display.

### 3.2 SAM4S not detected by OpenOCD

If a program has not been loaded:

1. Check orientation of the SAM4S.

2. Check soldering of the SAM4S pins under a microscope. Giving each pin a push with a sharp spike can reveal a poorly soldered joint.
3. Check 3.3 V and 1.2 V power rails.
4. Check the serial wire debug (SWD) signals, see Debugging serial wire debug (SWD).

If a program has been loaded:

1. Check the crystal oscillator, see Checking the crystal oscillator.
2. Erase the flash memory by connecting the ERASE pin to 3.3 V, re-enable booting from flash, and reprogram the SAM4S with an LED flash program.

### Debugging serial wire debug (SWD)

OpenOCD periodically polls to see if the SAM4S is alive. These occur every 100 ms.

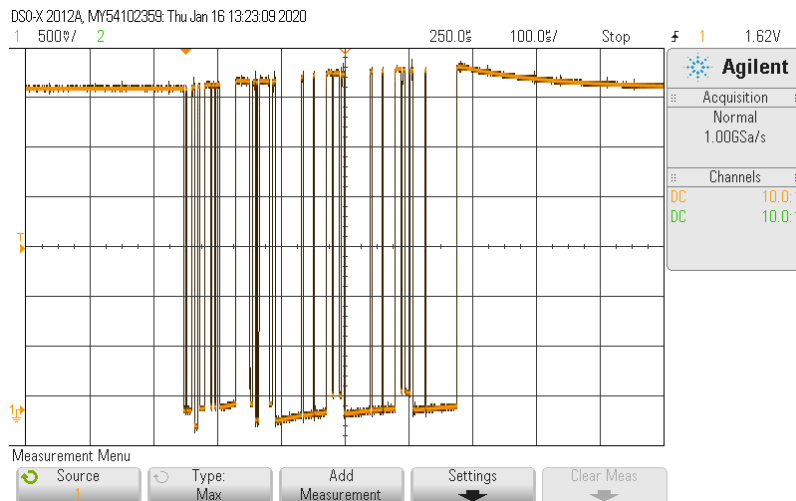


Figure 3.1: OpenOCD periodic poll when it gets a response from the SAM4S.

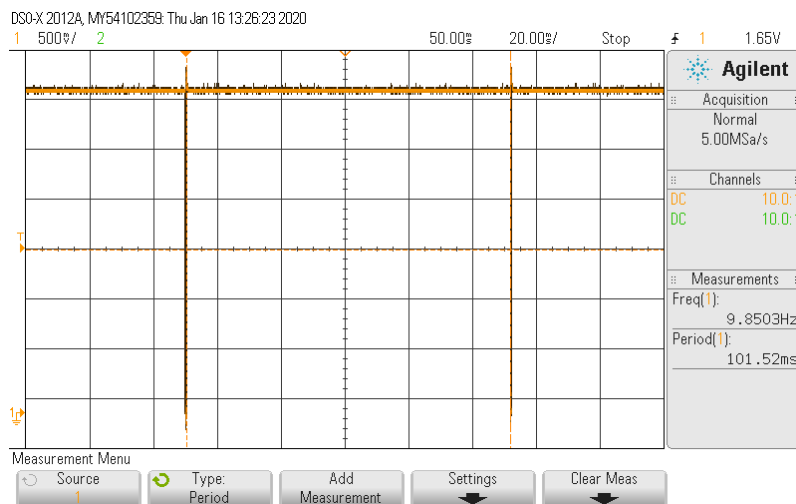


Figure 3.2: OpenOCD polls every 100 ms while connected.

OpenOCD polls less often when it does not get a response. The period increases to 6300 ms.

### 3.2.1 Checking the crystal oscillator

When the SAM4S is first powered up it uses an internal oscillator. However, when a program is loaded it switches to the main oscillator that uses the external crystal. If there is a problem with the crystal oscillator the SAM4S will not run since it has no clock. Moreover, OpenOCD will then fail to communicate with the SAM4S.

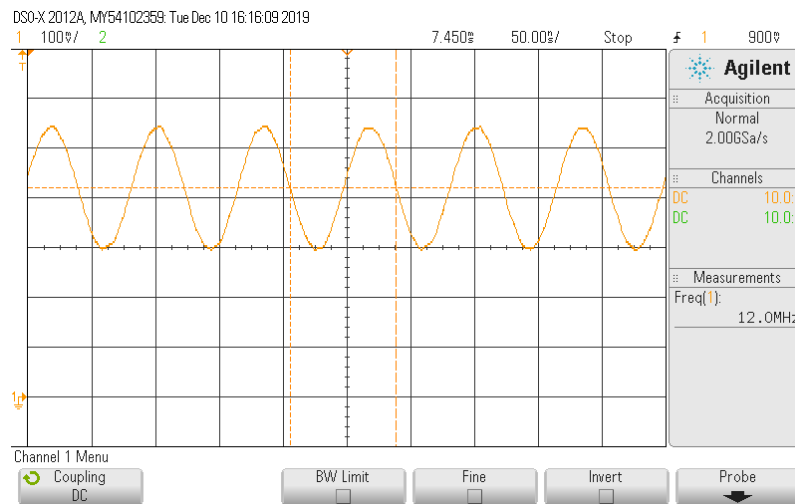


Figure 3.3: 12 MHz clock sine wave.

Add scope picture of CPU clock signals when not working

1. Connect a scope pin to the XOUT pin. A 12 MHz sinewave should be visible.
2. If there is no sinewave:
  - (a) Measure the voltage on the VDDPLL pin of the MCU with a scope. This should be 1.2 V to power the oscillator.
  - (b) Check the bypass capacitors across the crystal. These should be approx. 20 pF.

### 3.2.2 Checking the clock

The SAM4s has multiple clock sources:

1. Internal fast RC oscillator (this is selected when the SAM4S is first ever used).
2. Internal slow RC oscillator (this can be selected to save power).
3. Main oscillator using external crystal.

The SAM4S uses a phase-locked-loop (PLL) to multiply the frequency of the clock source to provide the CPU clock. Sometimes the PLL will run without a clock source and thus will generate an unexpected frequency.

The clock frequency can be checked by connecting a scope to a peripheral pin that generates a clock, e.g., PWM, SCK, TXD.

### 3.3 USB serial

USB is a complicated protocol and there are many possibilities why it does not work.

- Check that the termination resistors are 27 ohms.
- Check the SAM4S is running at the correct frequency of 96 MHz. Check the SAM4S XIN pin with an oscilloscope for a 12 MHz sinusoid. Note, the oscillator is not enabled unless a program has been loaded and running. If a 12 MHz signal is not found, check the MCU solder connections under a microscope. Also check that the VDDPLL pin has 3.3 V.

If the USB serial connection drops characters:

- Add a delay before sending data. This is because the driver takes a while to set up the connection after the USB cable is plugged in. If you try to send some data in this time, the data gets stored into a ring buffer for later transmission. However, the ring buffer is not large and once it is filled, the USB serial driver will drop characters.

### 3.4 Testing peripherals

#### 3.4.1 Motors

#### 3.4.2 Debugging PWM

If PWM does not work:

1. Check the SAM4S pin since not every pin can be a PWM signal
2. Check the definition in the configuration file `target.h`

If the PWM frequency is wrong:

1. Check the clock frequency, see Checking the clock.
2. Check your program.

If the PWM duty is wrong:

1. Check your program. The duty is specified as an integer in parts per thousand. (e.g. 1000 = 100% duty cycle; 50 = 5% duty cycle)

#### Testing the H-bridge

TODO

#### 3.4.3 IMU

#### 3.4.4 Debugging I2C

1. I2C/TWI requires external pull-up resistors for the clock (TCK) and data (TDA) signals.
2. TWI channel 1 (TWI1) uses PB4 and PB5. However, these are configured on boot as JTAG pins. You can disable this, see disabling JTAG pins.

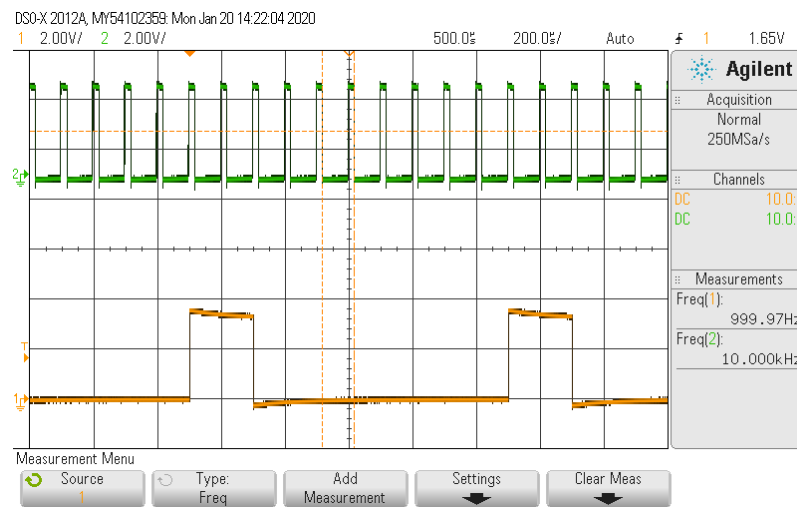


Figure 3.4: Two PWM signals at 1 kHz and 10 kHz.

Add scope picture of I2C signals

Add scope picture of I2C signals with no acknowledge

### 3.4.5 NRF24L01+ radio

For the radio to work you need:

1. A working SPI interface.
2. The correct channel and address.
3. A well filtered power supply for the radio.
4. A channel with little interference (note, some channels are shared with WiFi and Bluetooth and may be less reliable).

If nothing works, check:

1. For transmission the CE pin should be low; for reception the CE pin should be high.
2. Check that the SPI clock SCK, MOSI, and MISO signals are driven when the radio is configured (note, the MISO signal is tristate when CS is high).
3. Check that the SPI CS signal is driven low for each transmitted byte.

If the radio transmits but not receives, check:

1. The IRQ pin is driven low (this indicates that a packet has been received).
2. The power supply. The radio requires a well filtered power supply otherwise the range will be limited on reception. Preferably, the device should have its own 3V3 regulator with a low-pass RC filter comprised of a series resistor and large capacitor (say 22  $\mu$ F) or a low-pass LC filter made with a ferrite bead and capacitor.

**Note**, by default the radio waits for an auto-acknowledgement from the receiver device. This acknowledgement is performed in hardware. If no acknowledgement is received, it retries for up to 15 times. The auto-acknowledgement and number of retries can be configured in software.

### Checking the radio power supply

Add scope picture of expected radio power supply voltage

### Debugging SPI

Check:

1. The device chip select signal is driven low.
2. The **SCK** signal toggles while the chip select is low.
3. The **MOSI** signal does something while the chip select is low.

The **MISO** signal is usually tri-state and is driven by the device when the chip select is low.

The SPI standard is rather loose and there are four modes to confuse the unwary; these are due to two clock polarity modes and two clock phase modes. When using the wrong mode, the read data can be bit-shifted or unreliable.

Add scope picture of SPI signals

## 3.5 Other problems

- *OpenOCD does not run.* The most common problem is that the USB permissions are not correct.
- *A program does not correctly build after a file has been changed.* Every now and then the file timestamps are incorrect (this is a common problem with network drives due to a skew in the clocks) and make will not correctly rebuild a file. Running **make clean** will remove the existing dependency and object files so you can start afresh.
- *A program hangs.* This can be observed by an LED no longer flashing. There are a number of reasons:
  - The program is stuck in an infinite loop. Note, an embedded system should only have an infinite loop for the main loop; all other loops should have a timeout condition.
  - The program has crashed trying to access invalid memory. Usually this is due to buffer overflow or dereferencing uninitialised pointers (say by not calling **radio\_init**). Try running 'make debug'. This will start the debugger and attach to your MCU. If the debugger says that your program has stopped in '**\_hardfault\_handler**', then your program is likely to have accessed invalid memory. Use the 'bt' command to print a stack trace to see how your program went astray.
- *The output of the 3V3 regulator works fine when powered from USB but gives 6 V when powered from a 7.2 V battery.* Check that the 7.2 V is not connected directly to a SAM4S pin (say for battery monitoring) since this will cause an ESD protection diode inside the SAM4S to conduct.



# Appendix A

## OpenOCD

The Open On-Chip Debugger (OpenOCD) is an open-source on-chip debugging, in-system programming, and boundary-scan testing tool. It is able to communicate with various ARM and MIPS microprocessors via JTAG or SWD. It works with a number of different JTAG or SWD interfaces/programmers. User interaction can be achieved via telnet or the GNU debugger (GDB).

### A.1 Configuration files

OpenOCD needs a configuration file to specify the interface (USB or parallel port) and the target system. Unfortunately, these change with every new release of OpenOCD.

### A.2 Running OpenOCD

OpenOCD runs as a daemon program in the background and can be controlled from other programs using TCP/IP sockets. This means that you can remotely debug from another computer. The socket ports it uses are specified in the OpenOCD configuration file supplied when it starts. By default, OpenOCD uses port 3333 for gdb and port 4444 for general interaction using Telnet.

#### A.2.1 Communicating with OpenOCD using telnet

If OpenOCD is running, commands can be sent to it using telnet. For example,

```
$ telnet localhost:4444
> monitor flash info 0
```

#### A.2.2 Communicating with OpenOCD using GDB

If OpenOCD is running, commands can be set to it with gdb. There are two steps: connecting to OpenOCD with the target remote command, and then sending a command with the GDB monitor command. For example,

```
$ gdb
(gdb) target remote localhost:3333
(gdb) monitor flash info 0
```

## A.3 OpenOCD commands

All the gory OpenOCD details can be found in the OpenOCD manual. If you are getting strange errors see OpenOCD errors.

## A.4 Flash programming

OpenOCD can program the flash program memory from a binary file.

## A.5 FTDI support

Many JTAG interfaces use chips made by Future Technology Devices International (FTDI) to translate between the USB and JTAG protocols. FTDI provide a library to communicate with these devices; however, this is not open-source and hence cannot be distributed. There is an open-source equivalent, libftdi, which in turn uses the open-source libusb. This can be temperamental to get working on a Windows system.

## A.6 Getting OpenOCD

The developers of OpenOCD release source packages (and provide subversion access) but do not provide official builds for any operating system. However, there are various sites which provide prebuilt versions of OpenOCD.

### A.6.1 Windows

Windows installers for release versions of OpenOCD can be found [here](#). Some development versions can be found [here](#). Unless you need a feature not present in the release version, avoid getting the development versions as they can be less stable.

Alternatively, the WinARM toolchain contains a version of OpenOCD, albeit one that appears (from the description on the project page) to be out of date. It is also possible to build your own copy of OpenOCD using either MinGW or Cygwin; instructions for this are posted in various places online.

Realistically, it's probably easier to set up a virtual machine with Linux and use that instead.

### A.6.2 Linux

Many recent distributions of Linux have OpenOCD in their software repositories. However, this is often out of date and (in some cases) buggy. In this case it is straightforward to build the latest version.

### A.6.3 Mac OSX

The easiest way to install OpenOCD on Mac OSX is to use Homebrew. If you wish to use JTAG with an adaptor based on a FTDI chip (for example, the UCECE USB to JTAG adaptor or a Bus Blaster), make sure you include libftdi support by installing with the command:

```
$ brew install openocd --enable-ft2232_libftdi
```

## A.7 Errors

See [http://ecewiki.elec.canterbury.ac.nz/mediawiki/index.php/OpenOCD\\_errors](http://ecewiki.elec.canterbury.ac.nz/mediawiki/index.php/OpenOCD_errors).