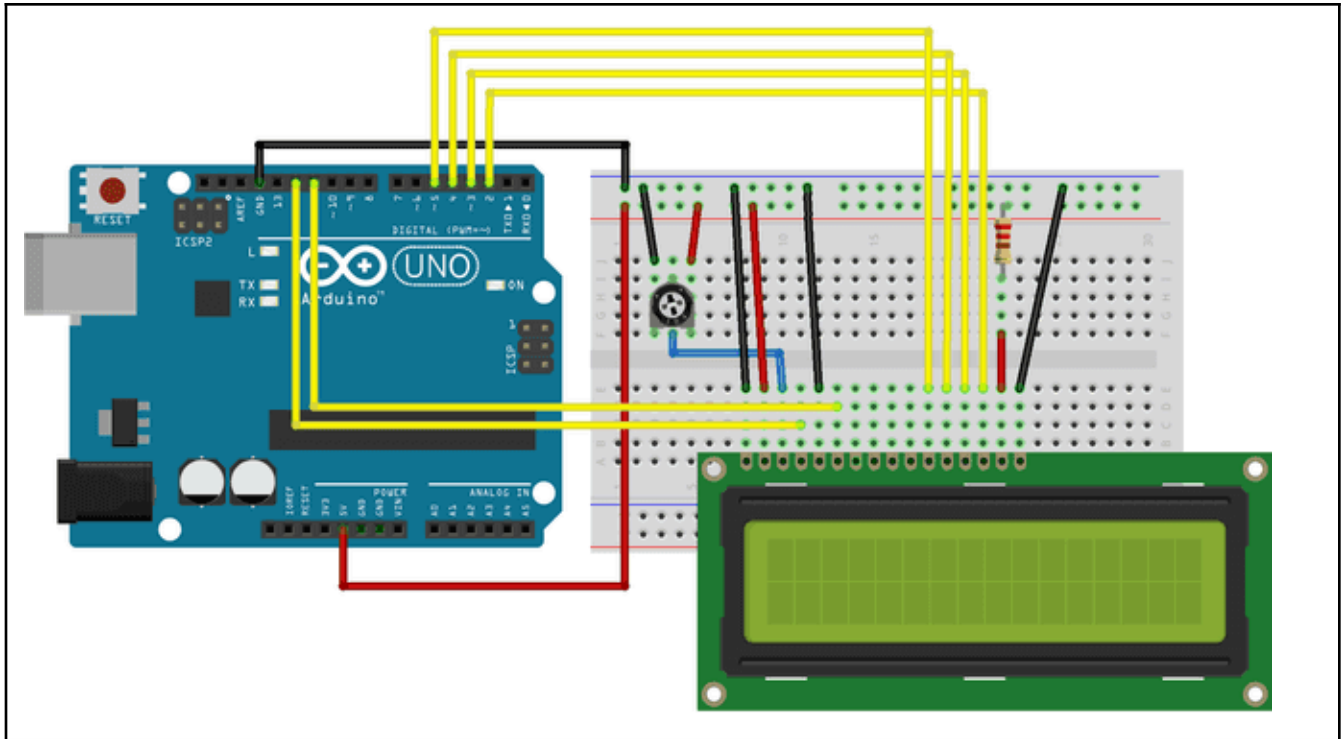


## Setup



With the additional connections:

One leg of LDR	5V
Other leg of LDR	Analog pin A0
resistor	between A0 and GND