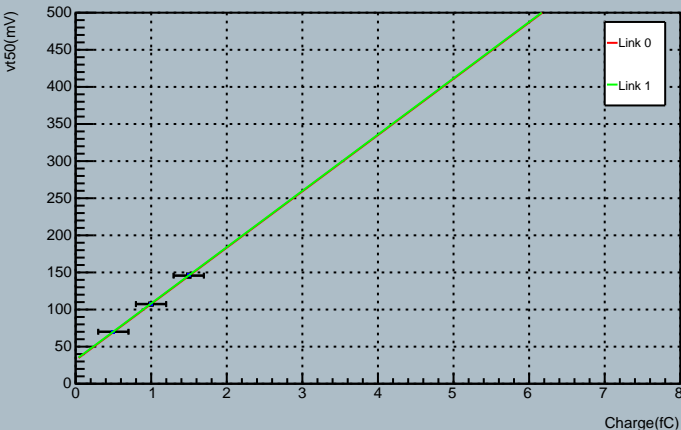
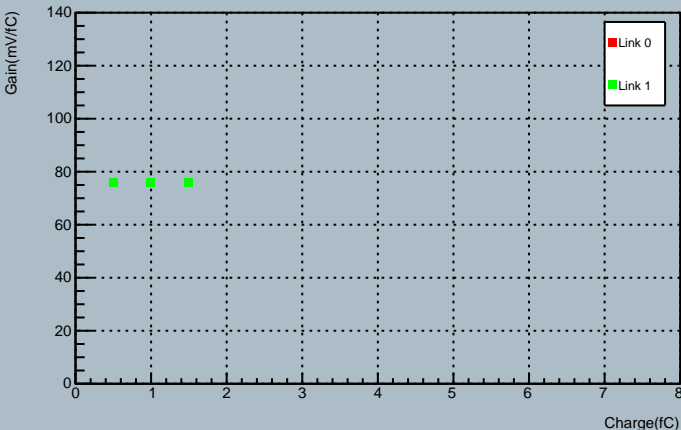


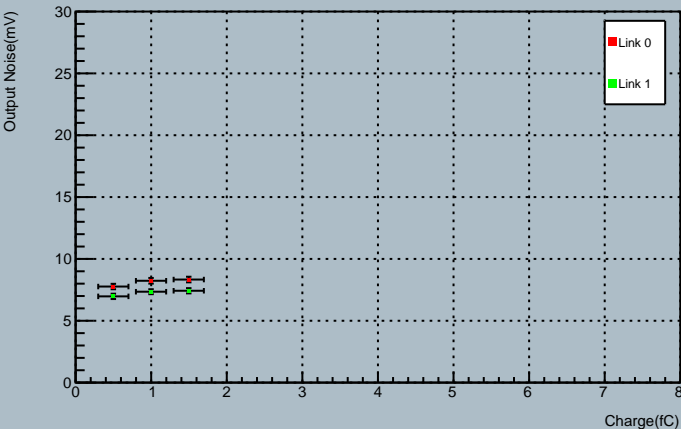
Chip 0 Response Curve



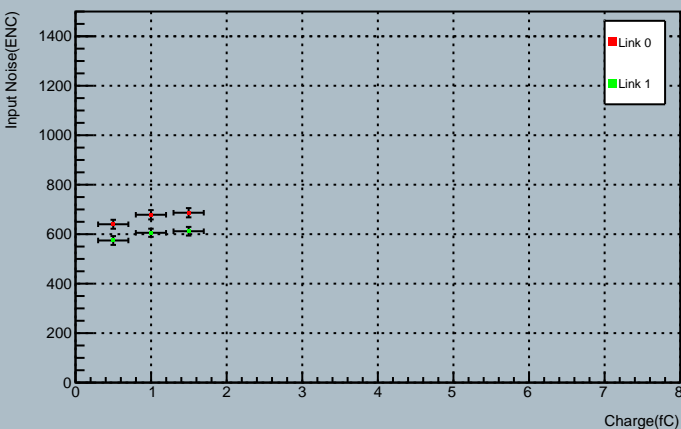
Chip 0 Gain



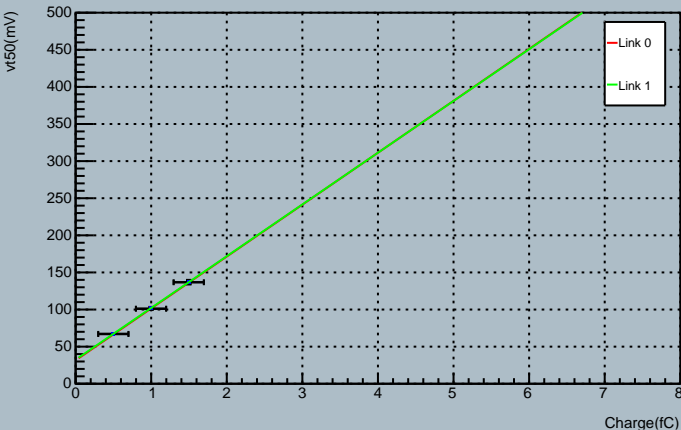
Chip 0 Output Noise



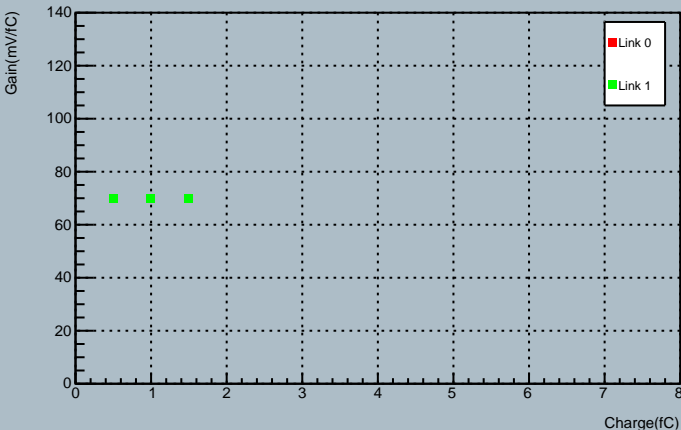
Chip 0 Input Noise



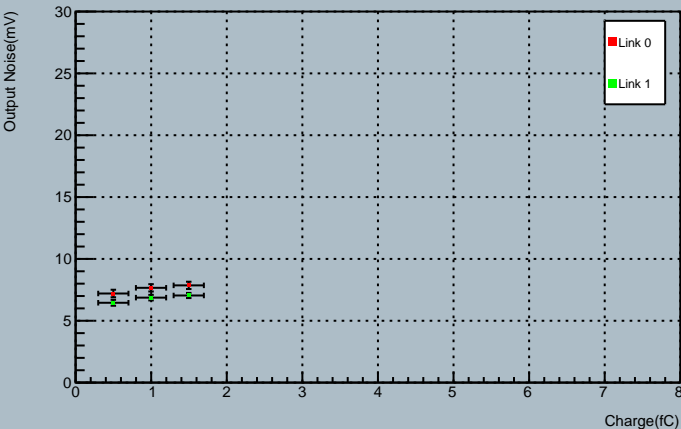
Chip 1 Response Curve



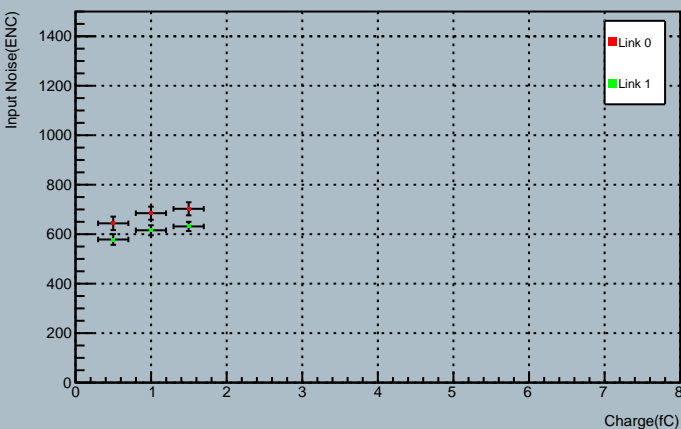
Chip 1 Gain

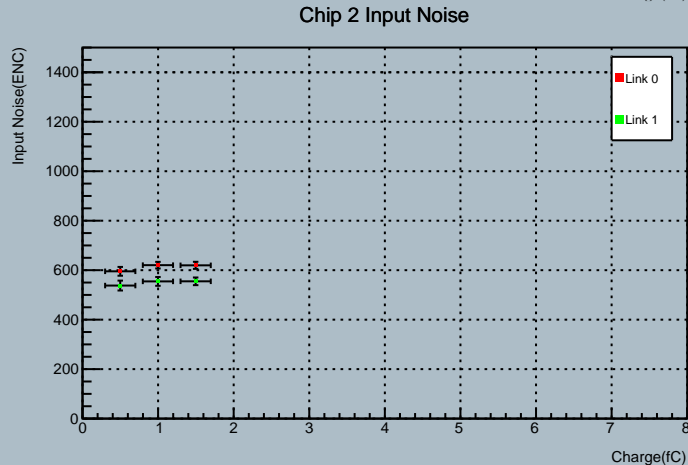
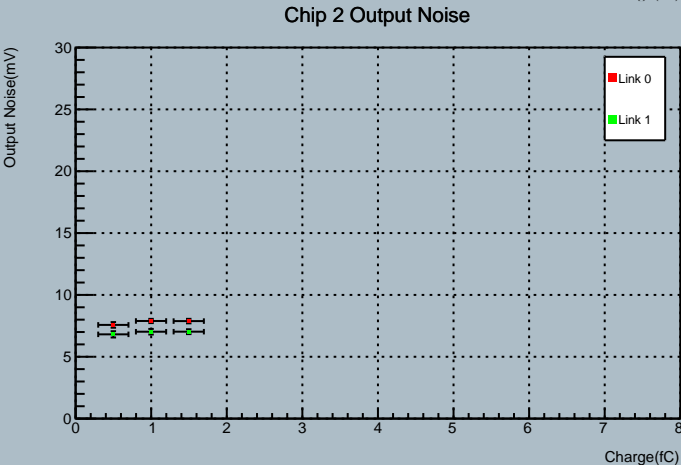
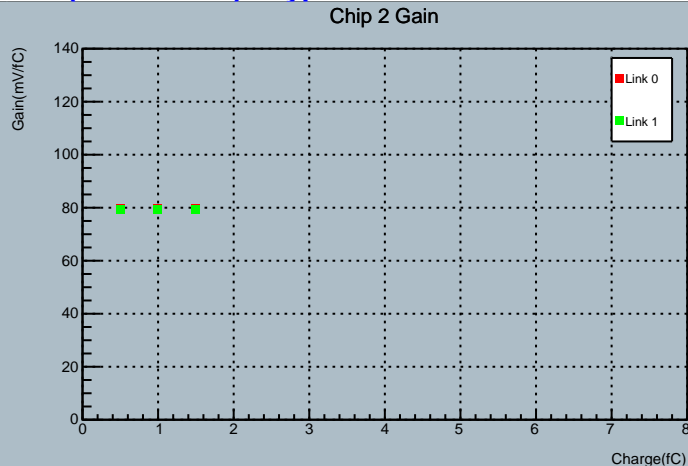
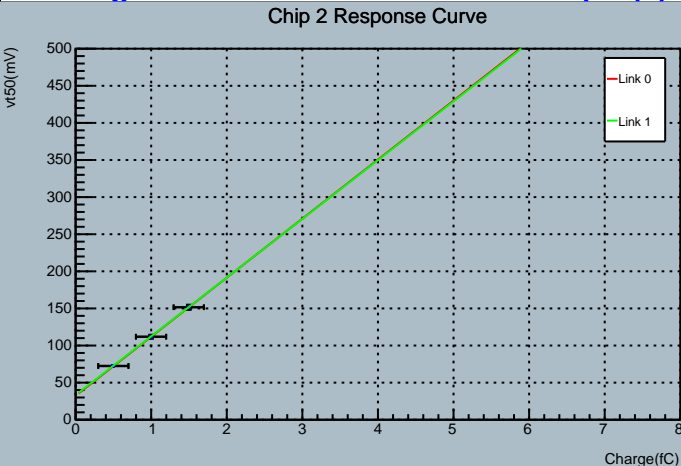


Chip 1 Output Noise

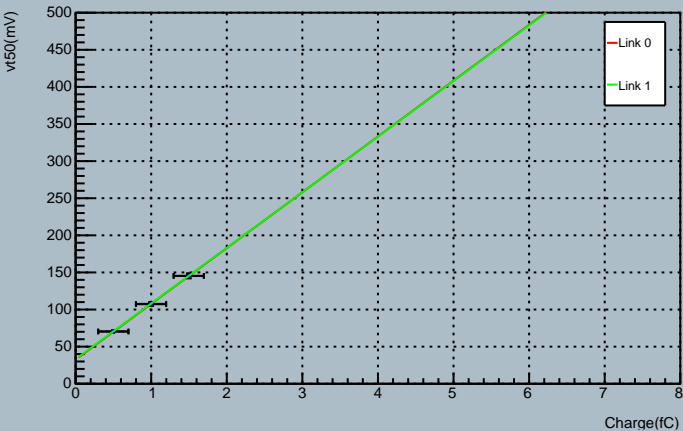


Chip 1 Input Noise

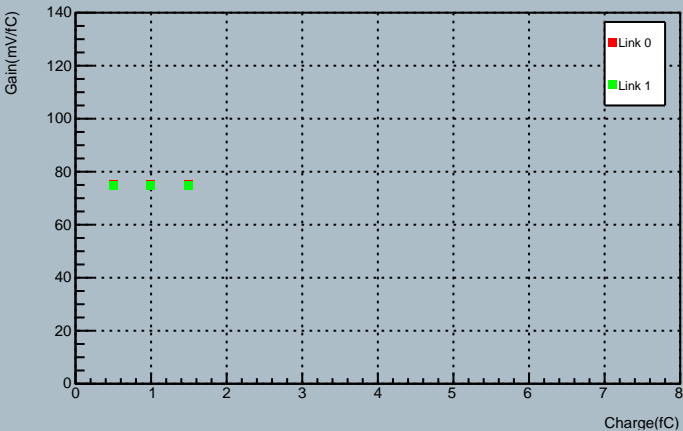




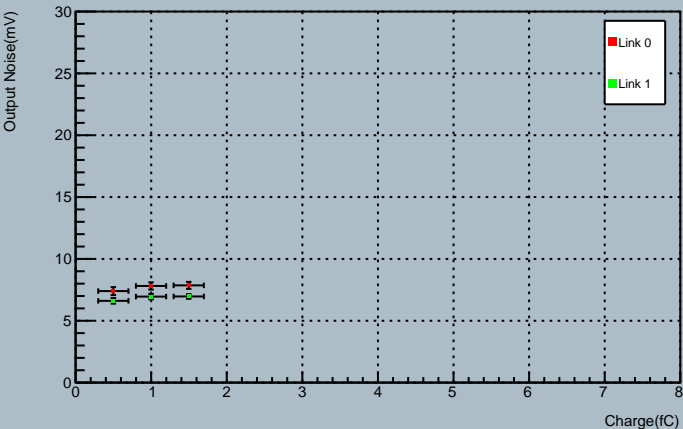
Chip 3 Response Curve



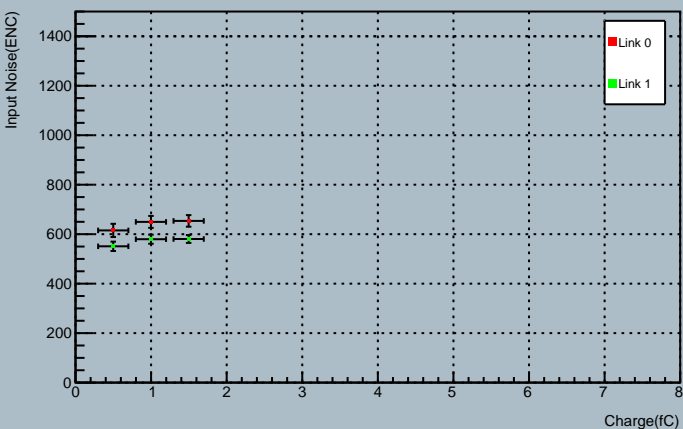
Chip 3 Gain



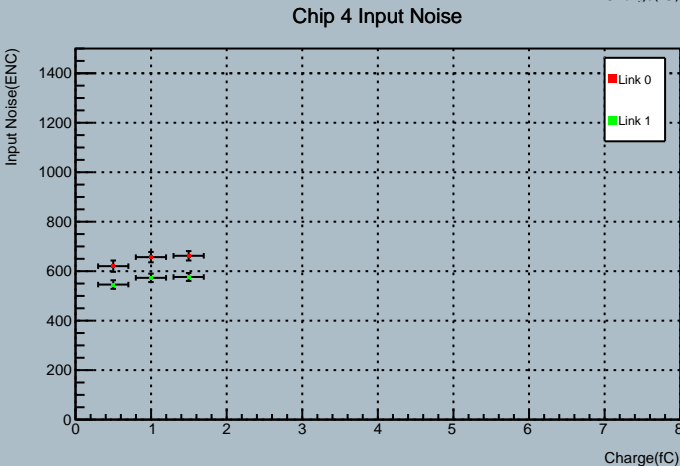
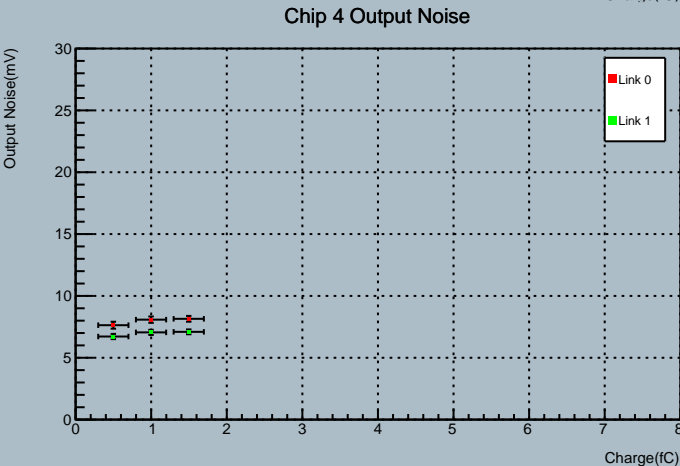
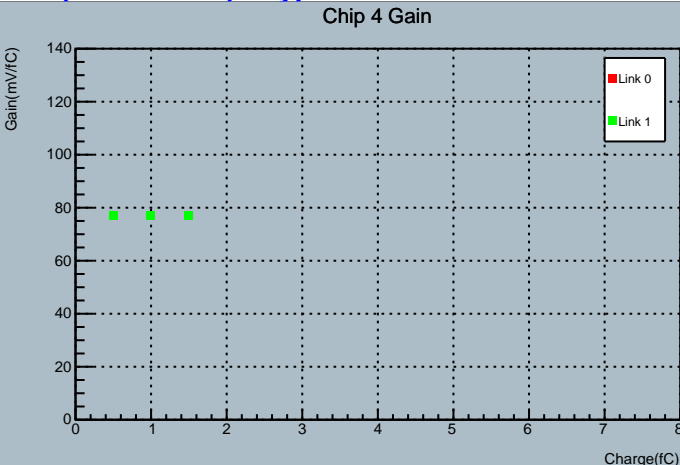
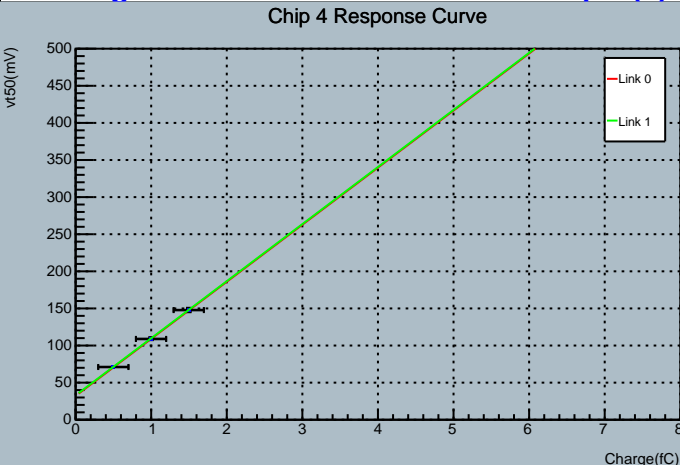
Chip 3 Output Noise

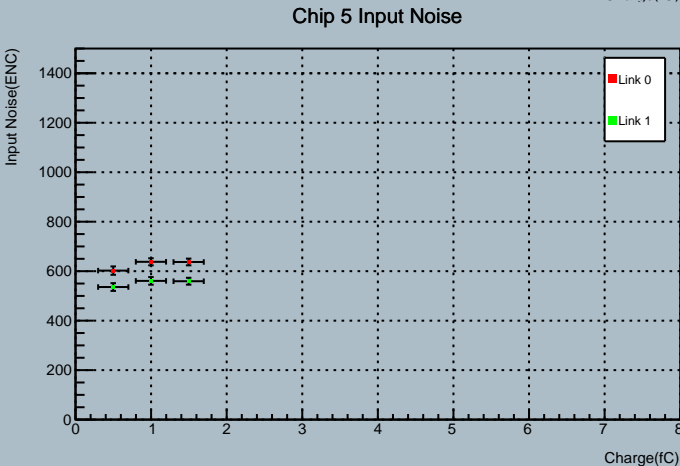
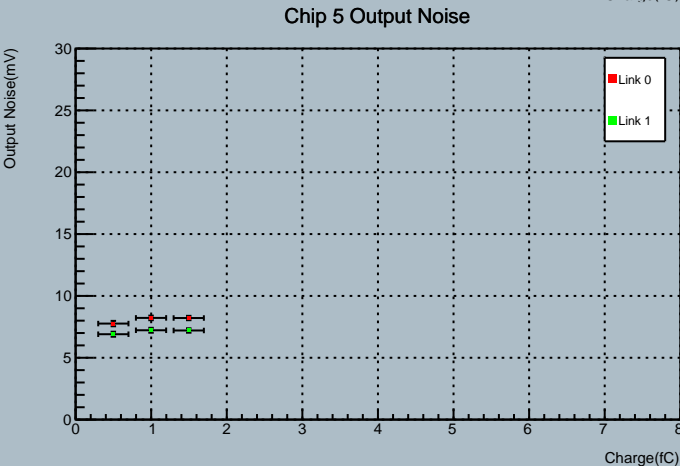
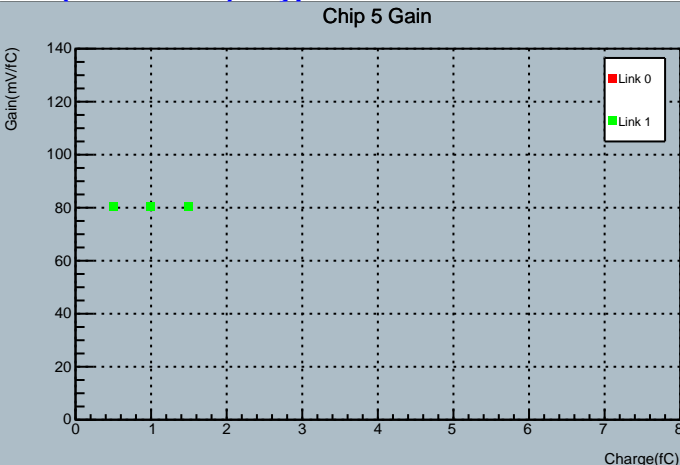
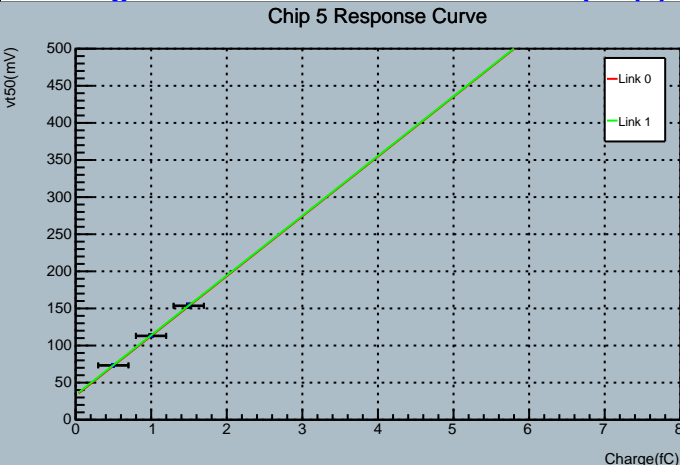


Chip 3 Input Noise

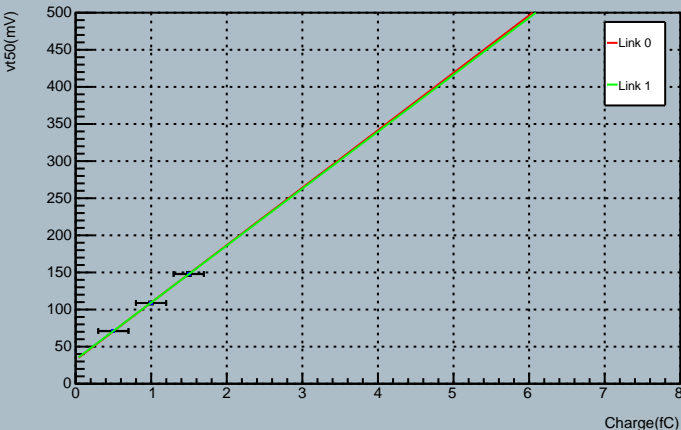




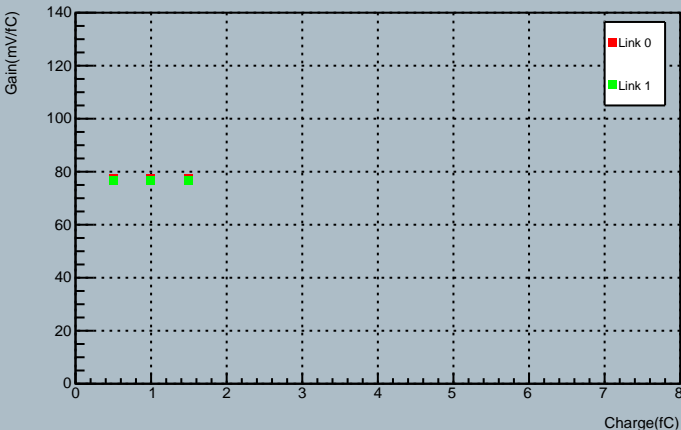




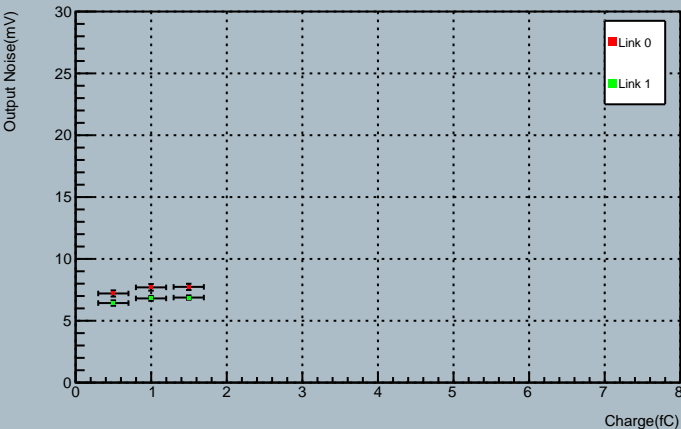
Chip 6 Response Curve



Chip 6 Gain



Chip 6 Output Noise



Chip 6 Input Noise

