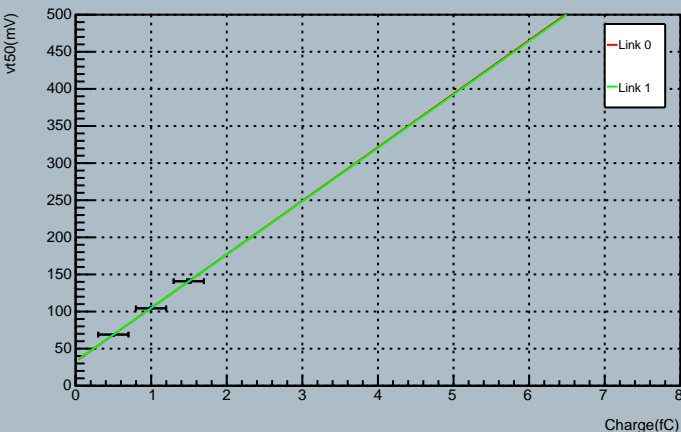
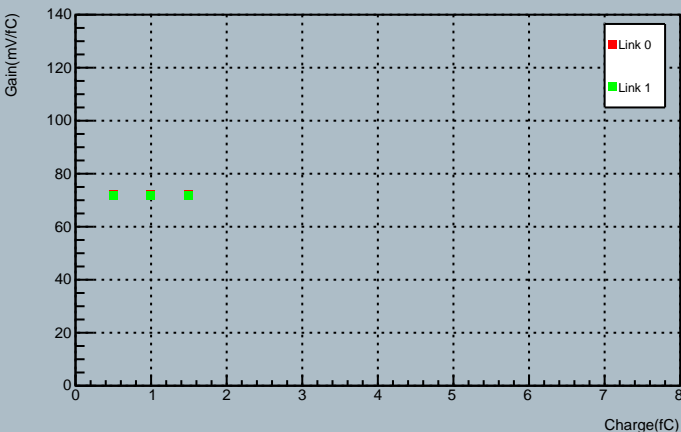


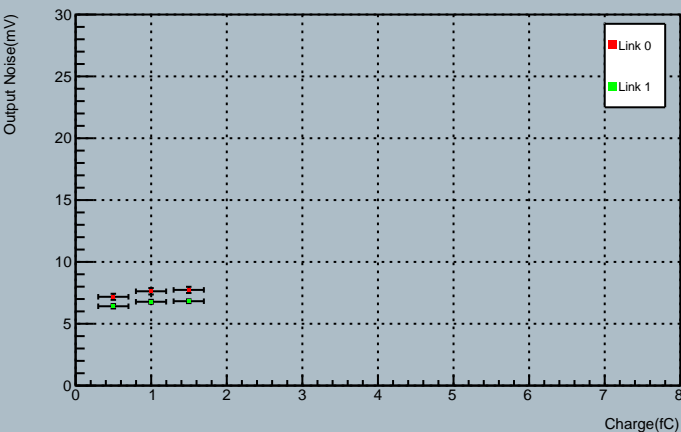
Chip 0 Response Curve



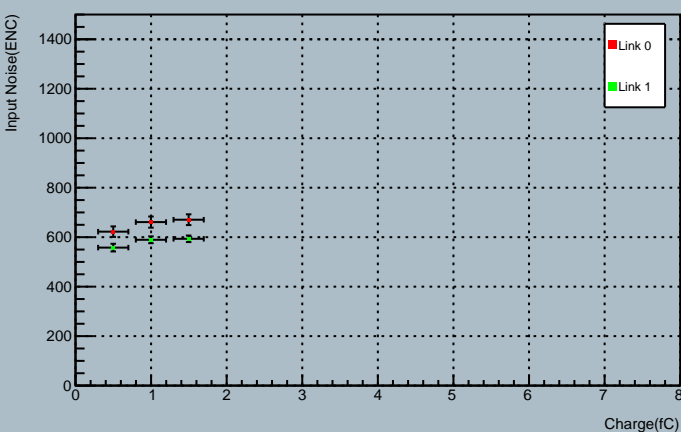
Chip 0 Gain



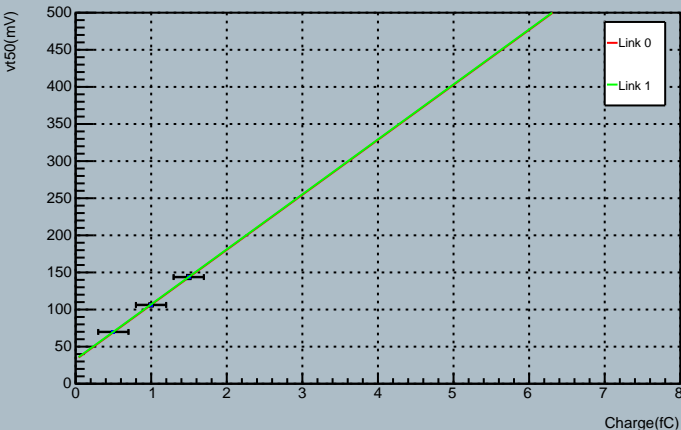
Chip 0 Output Noise



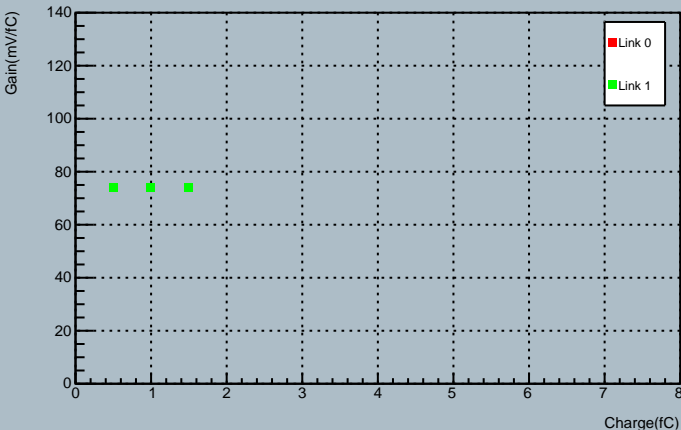
Chip 0 Input Noise



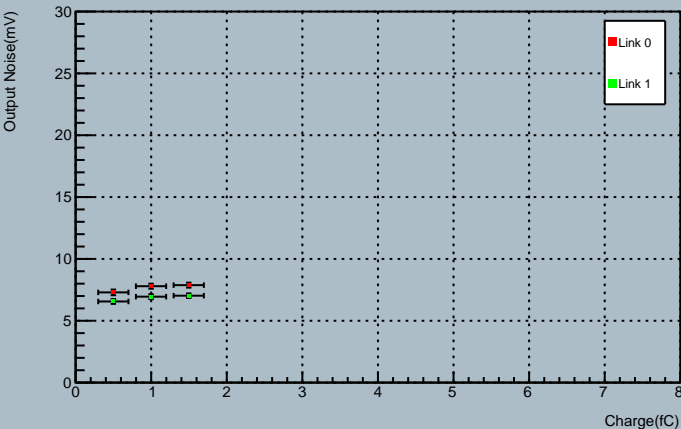
Chip 1 Response Curve



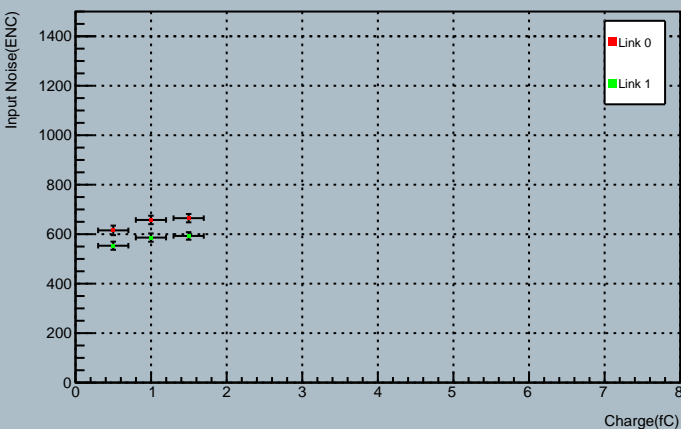
Chip 1 Gain

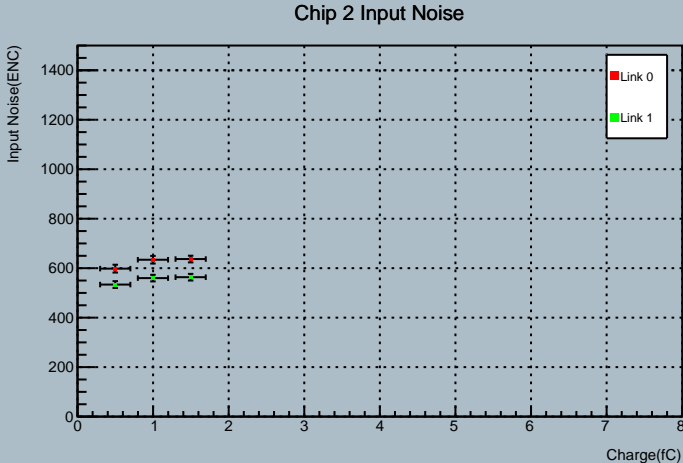
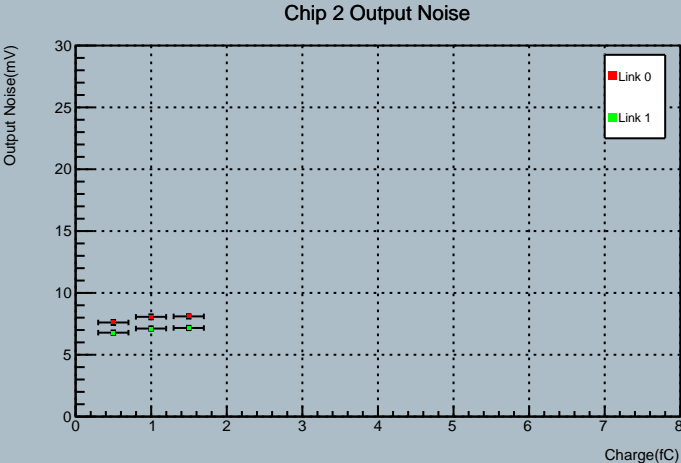
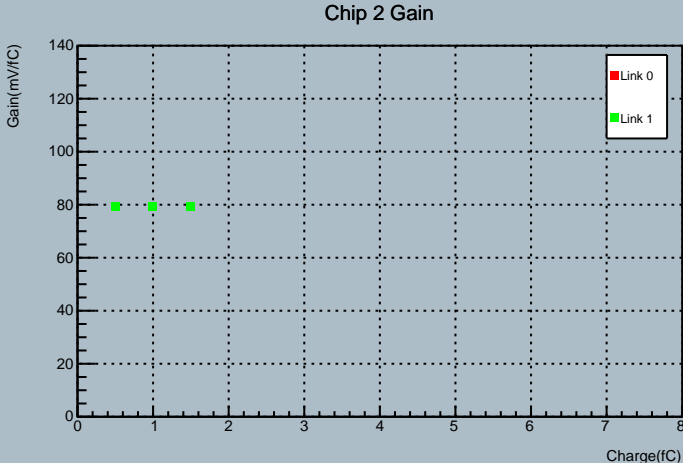
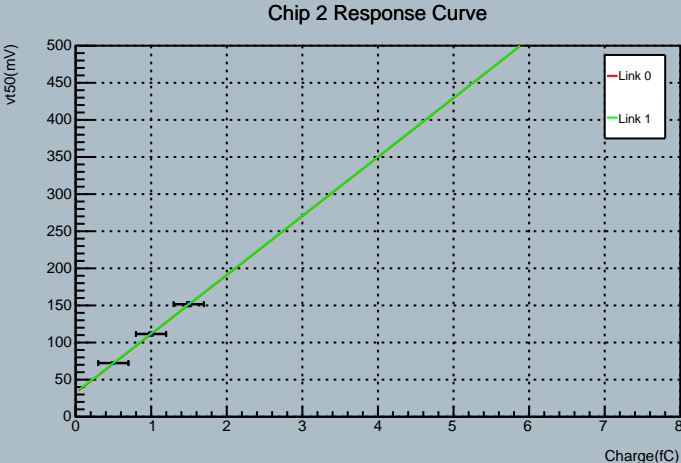


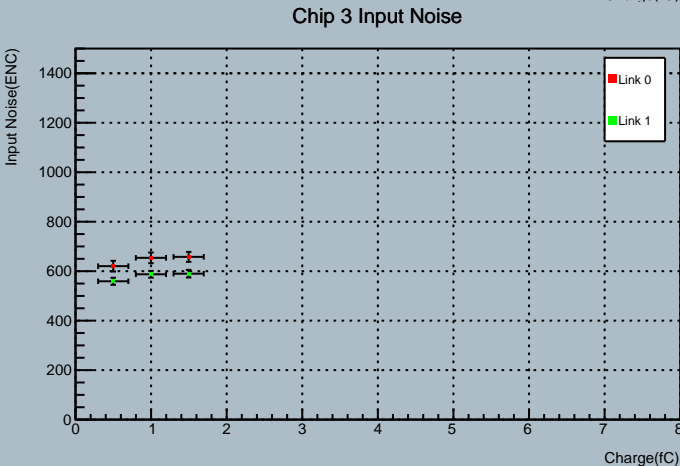
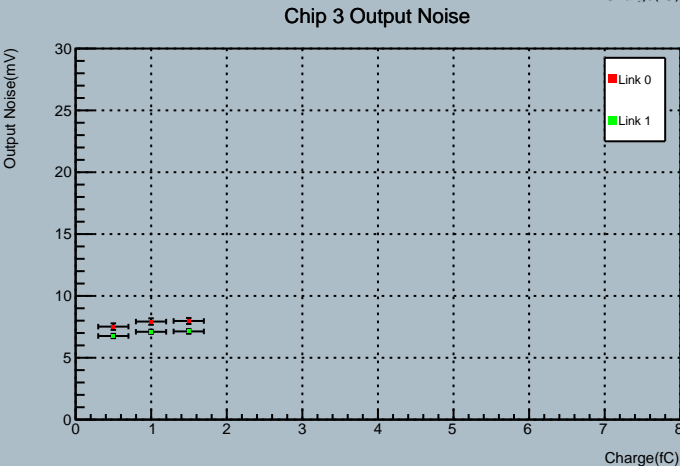
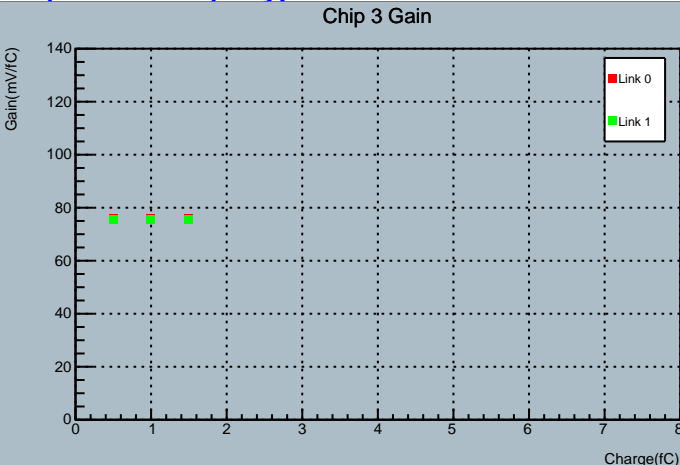
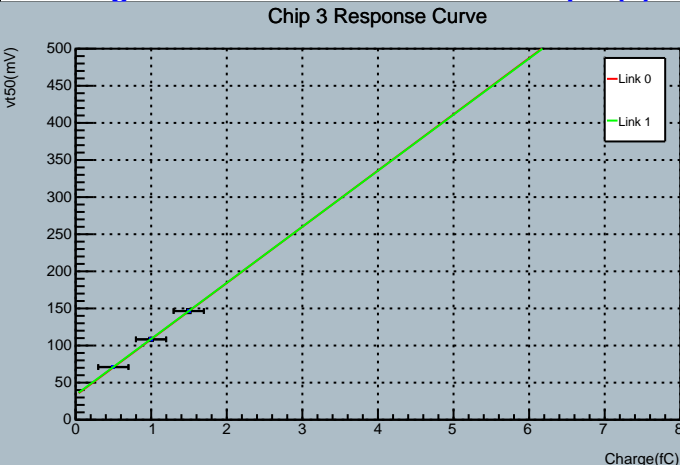
Chip 1 Output Noise



Chip 1 Input Noise

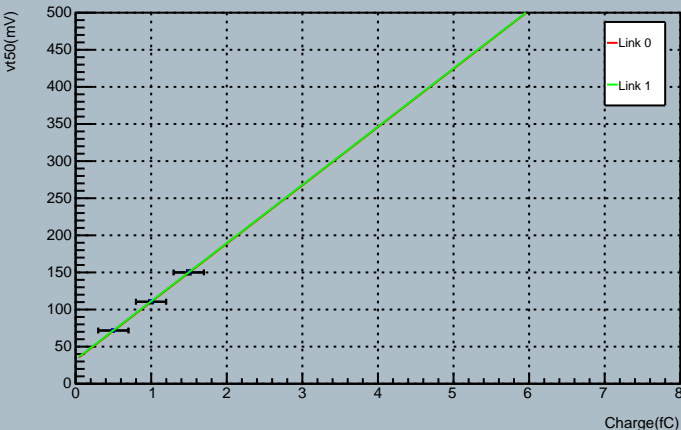




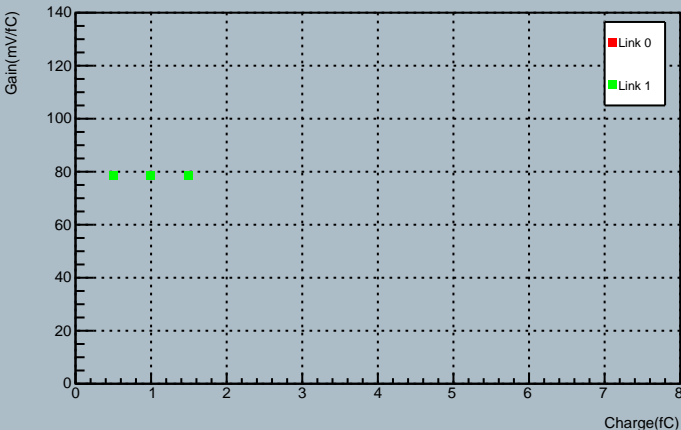




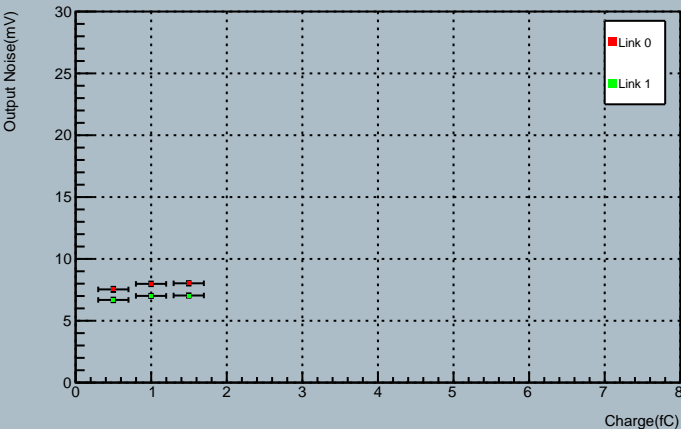
Chip 4 Response Curve



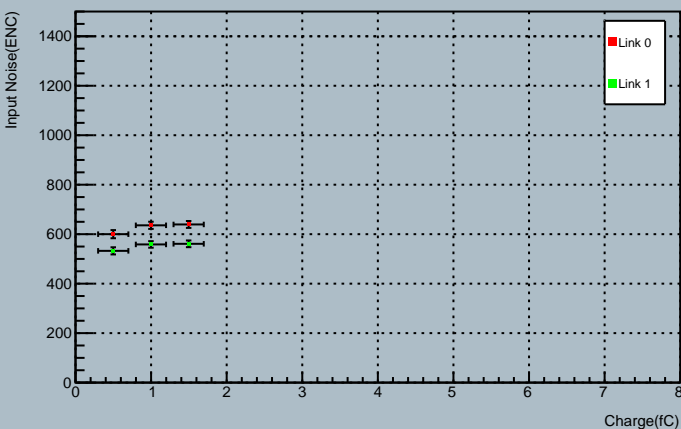
Chip 4 Gain



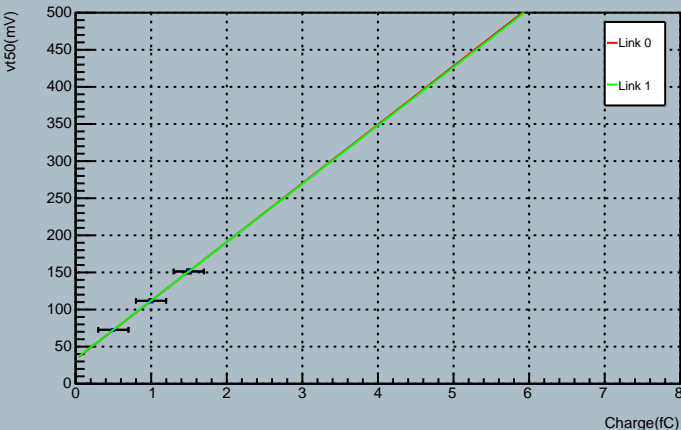
Chip 4 Output Noise



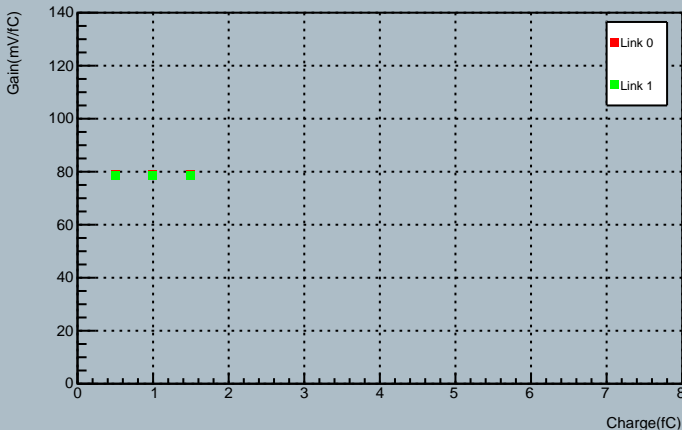
Chip 4 Input Noise



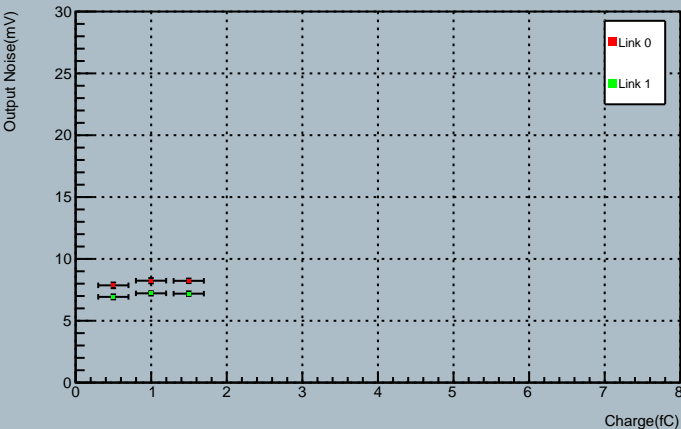
Chip 5 Response Curve



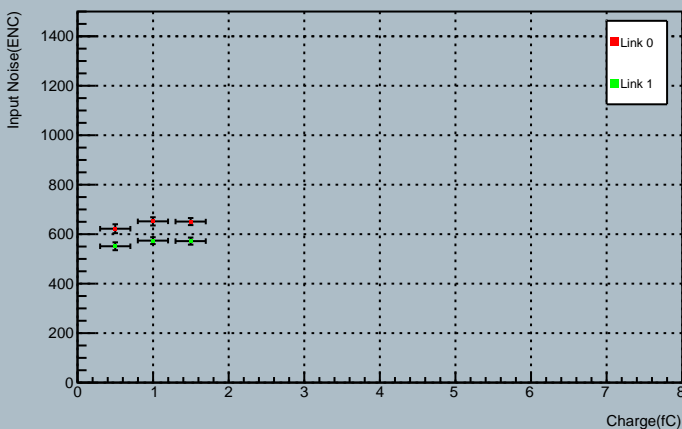
Chip 5 Gain

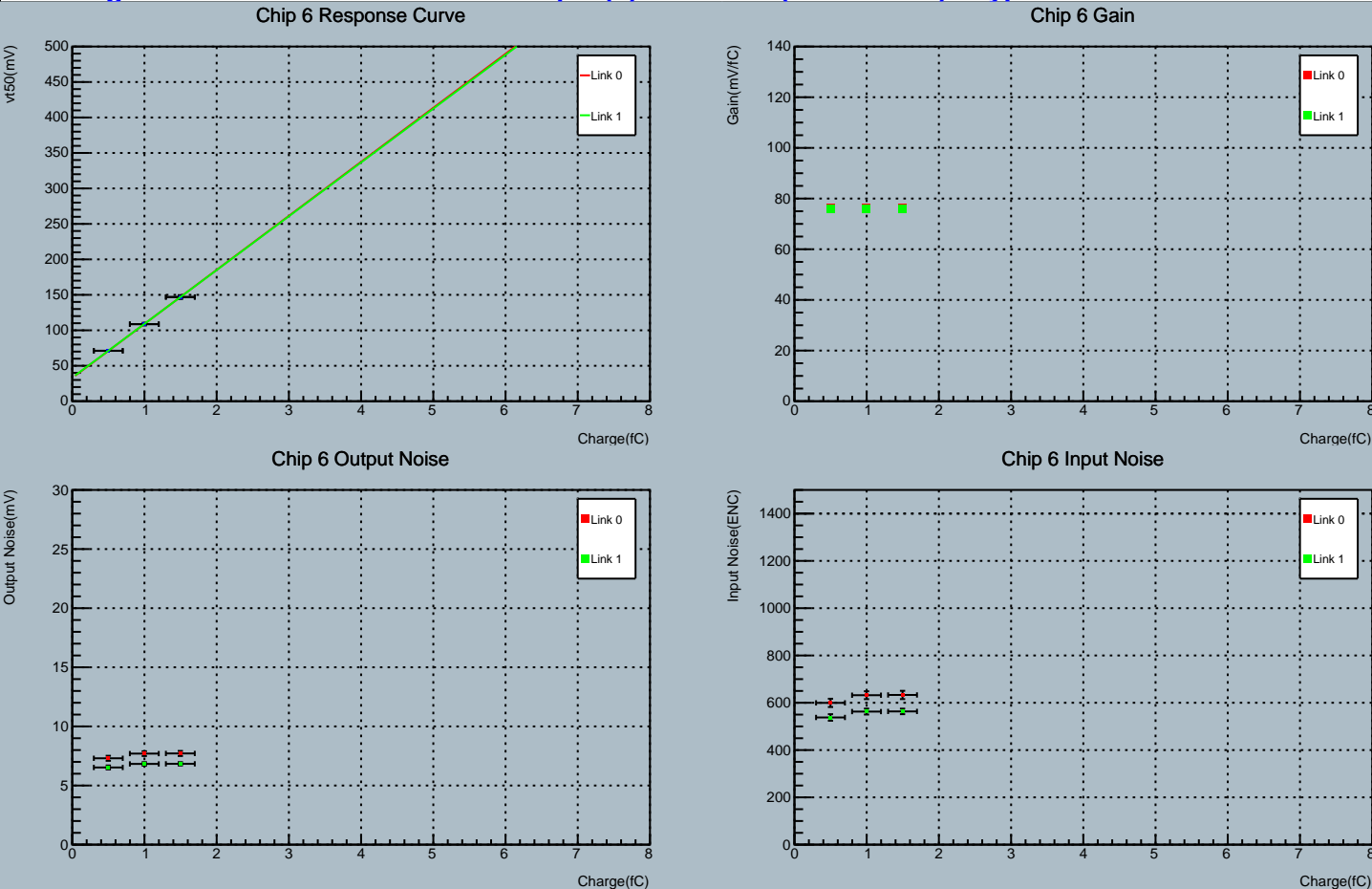


Chip 5 Output Noise

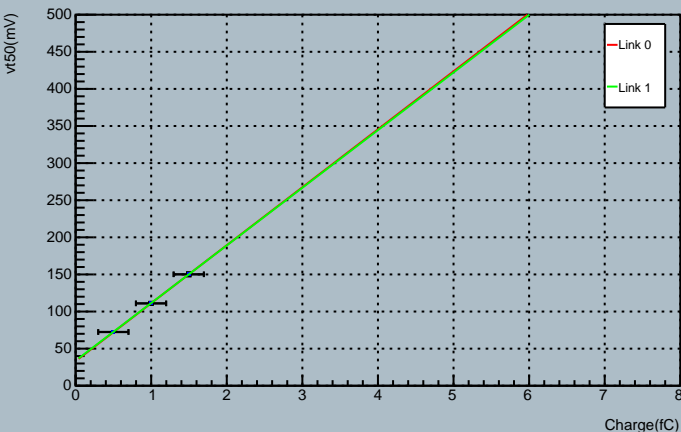


Chip 5 Input Noise

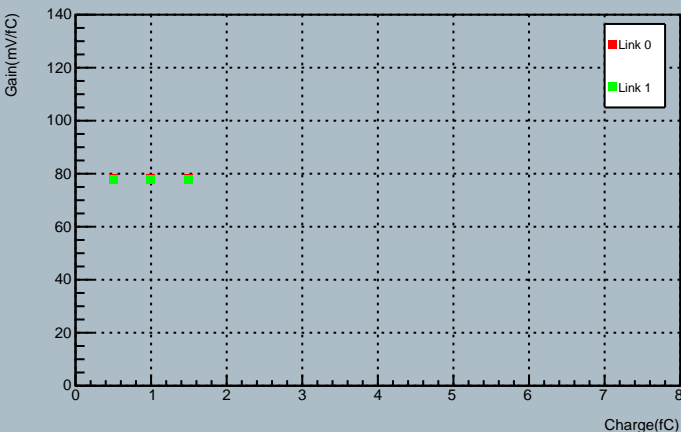




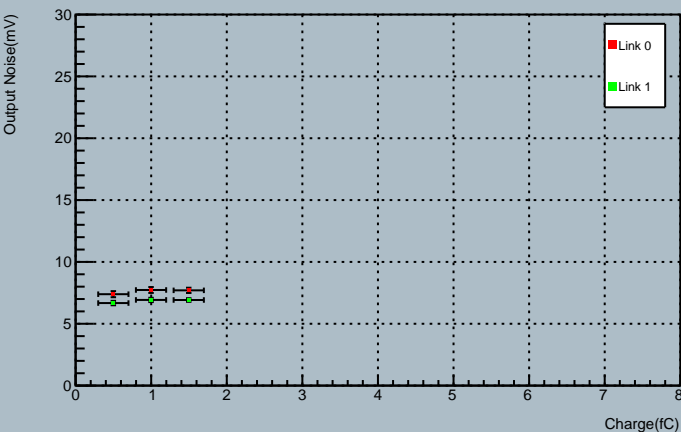
Chip 7 Response Curve



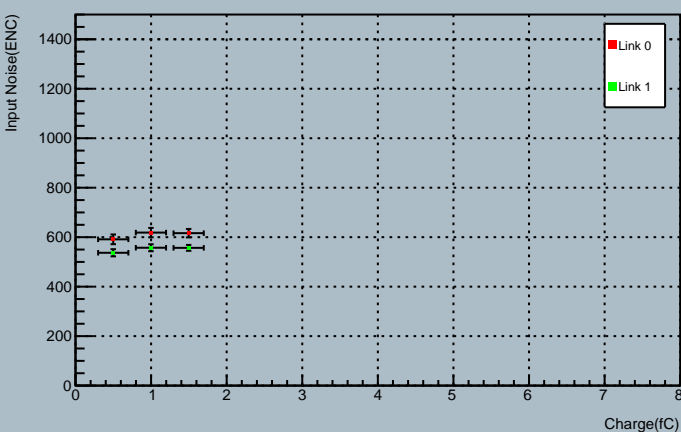
Chip 7 Gain

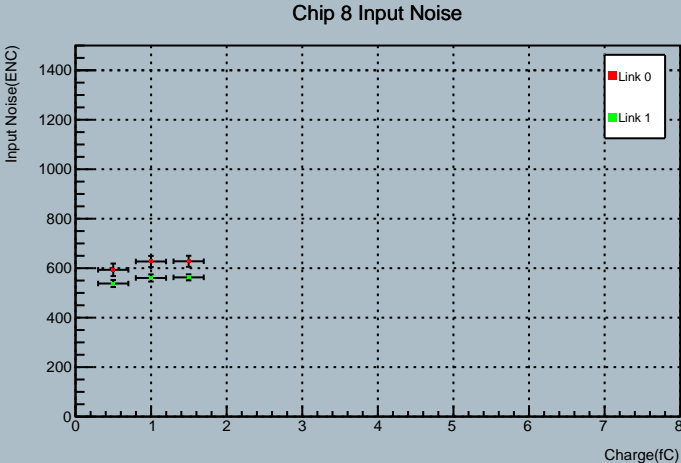
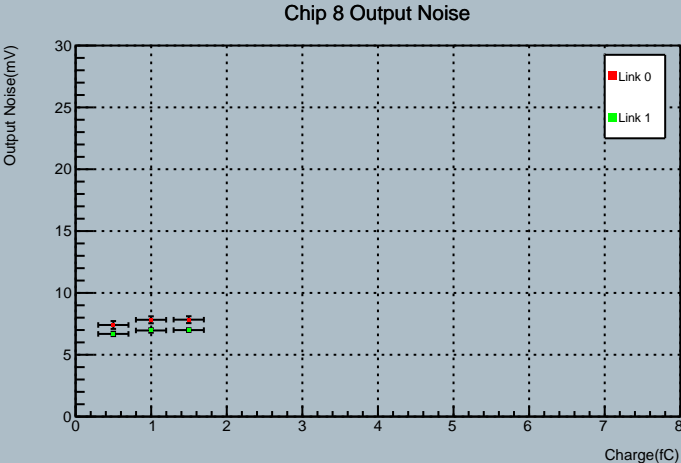
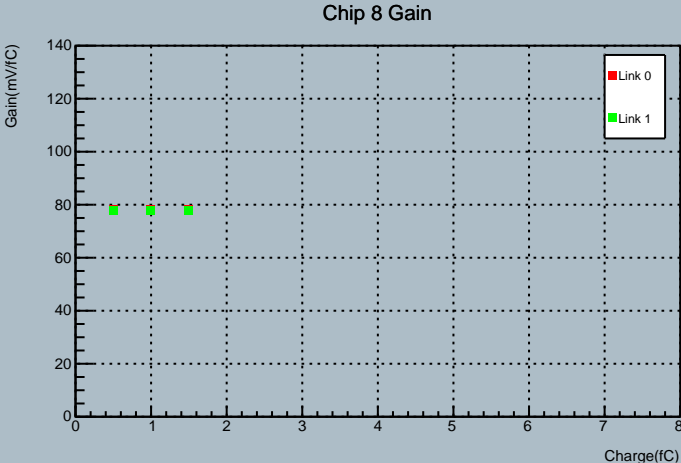
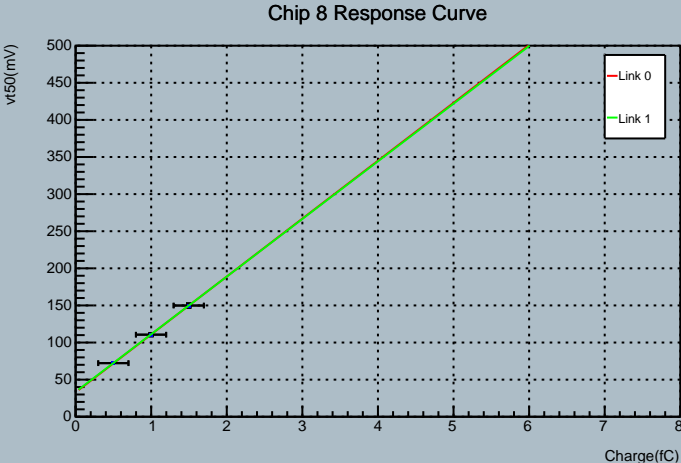


Chip 7 Output Noise

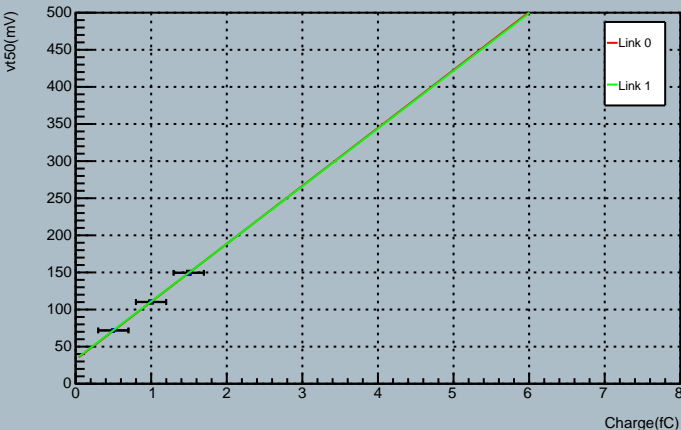


Chip 7 Input Noise

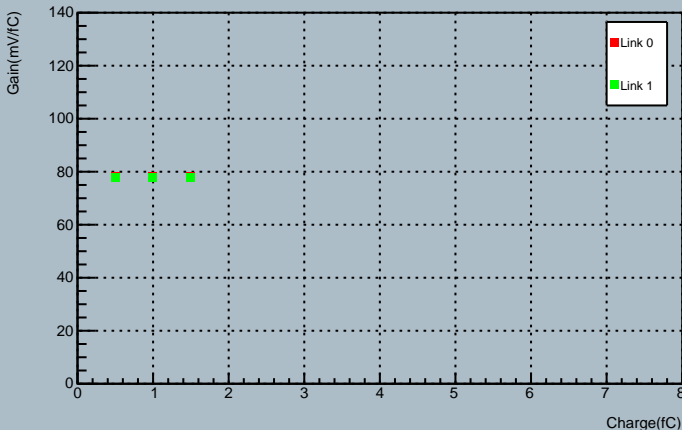




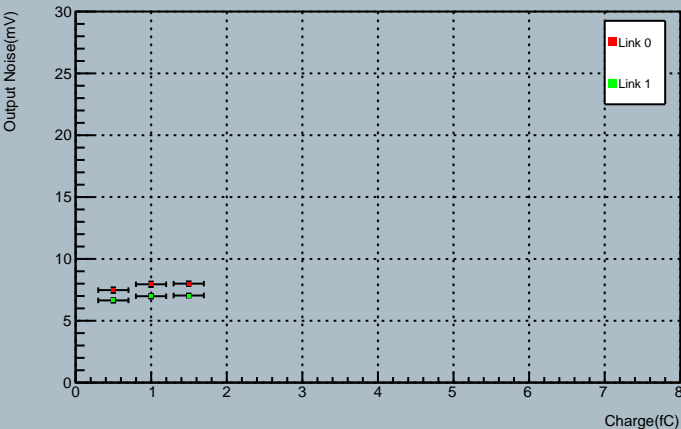
Chip 9 Response Curve



Chip 9 Gain



Chip 9 Output Noise



Chip 9 Input Noise

