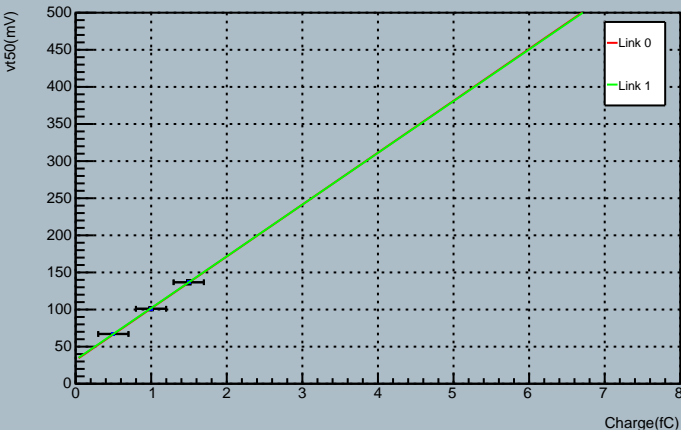
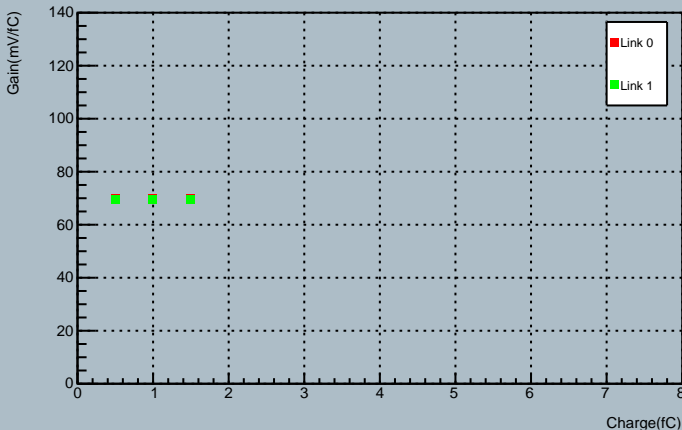


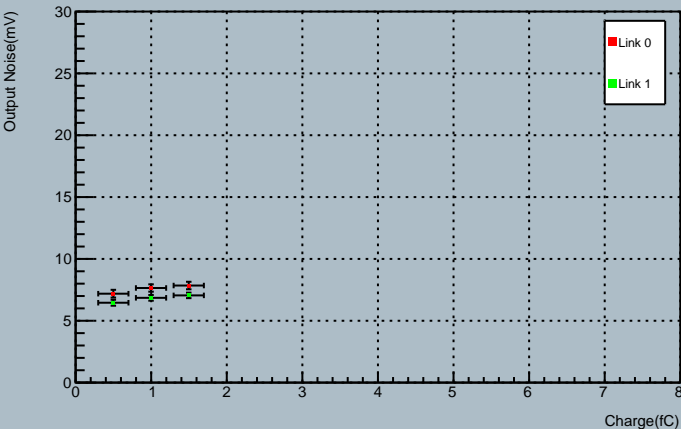
Chip 1 Response Curve



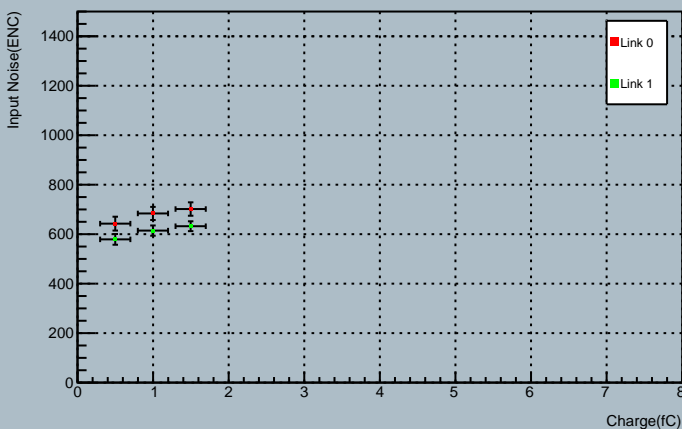
Chip 1 Gain



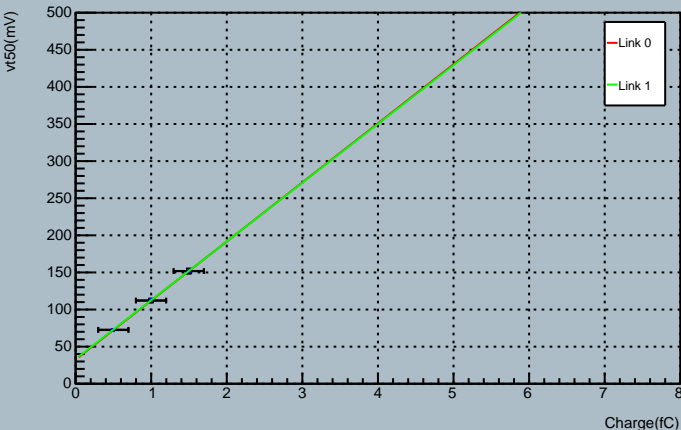
Chip 1 Output Noise



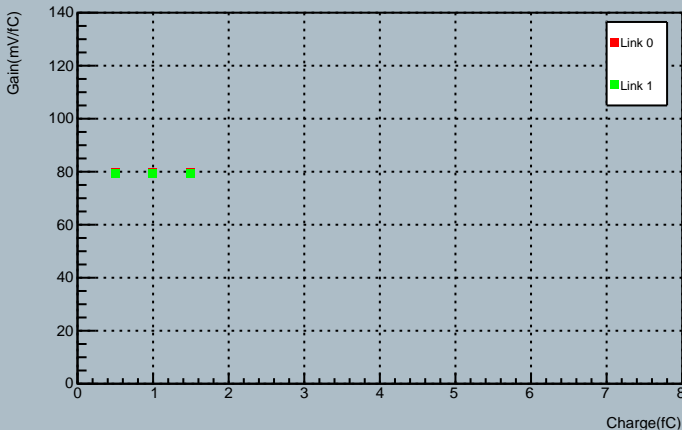
Chip 1 Input Noise



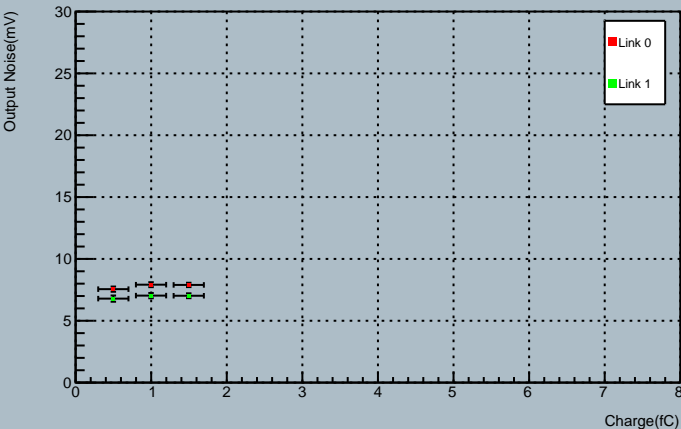
Chip 2 Response Curve



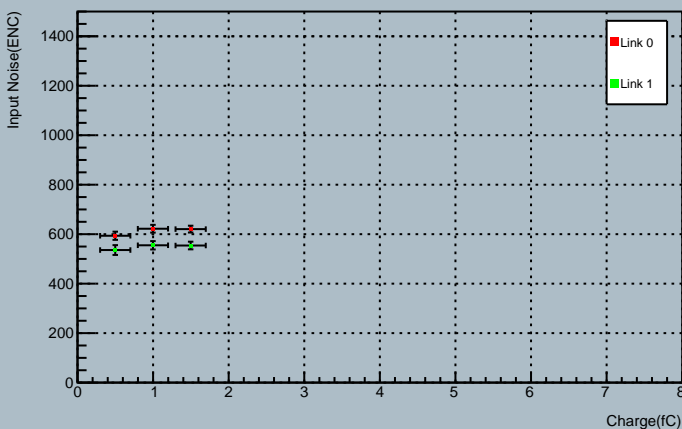
Chip 2 Gain



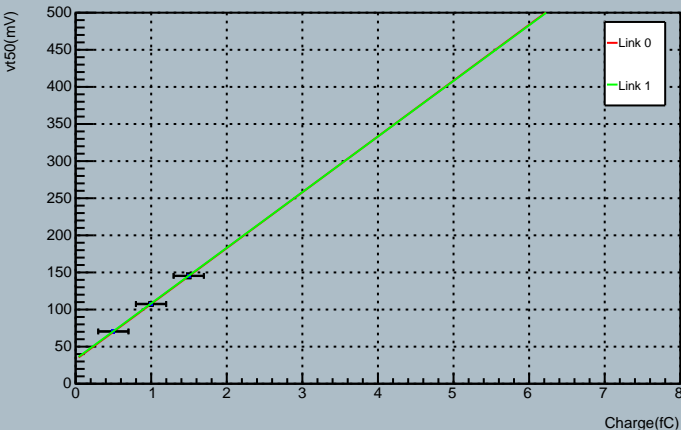
Chip 2 Output Noise



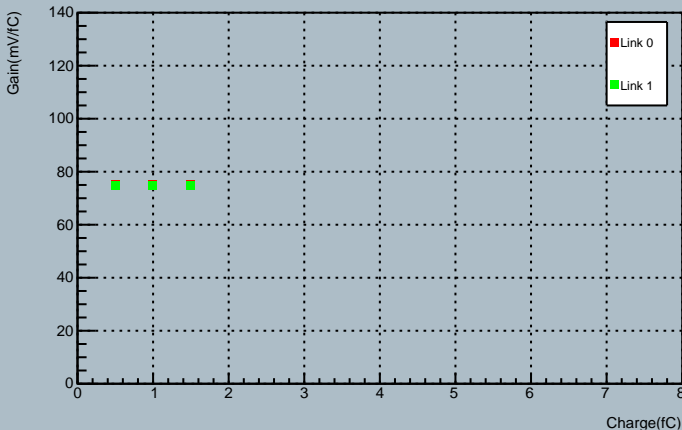
Chip 2 Input Noise



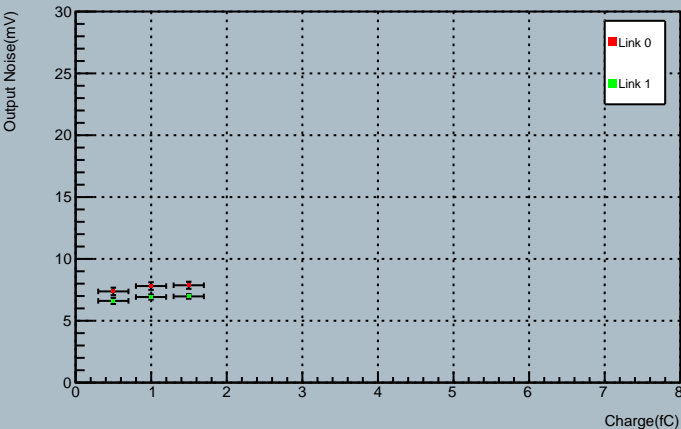
Chip 3 Response Curve



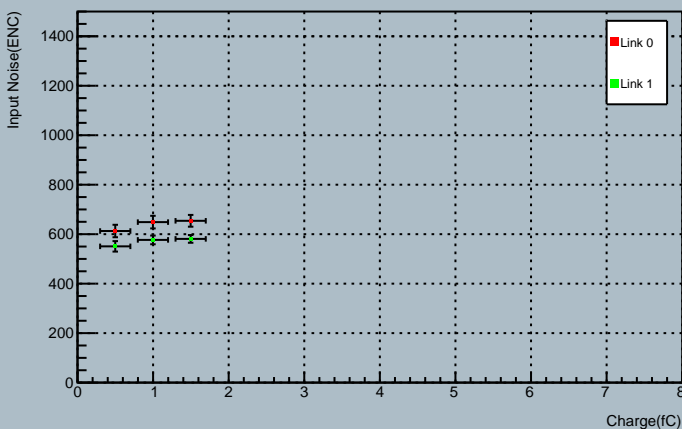
Chip 3 Gain



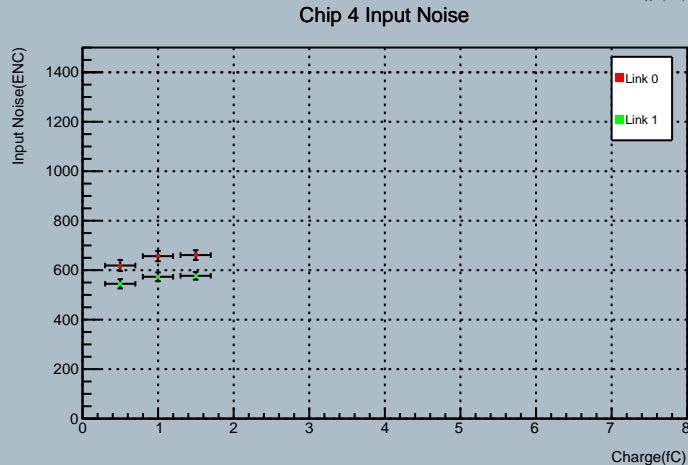
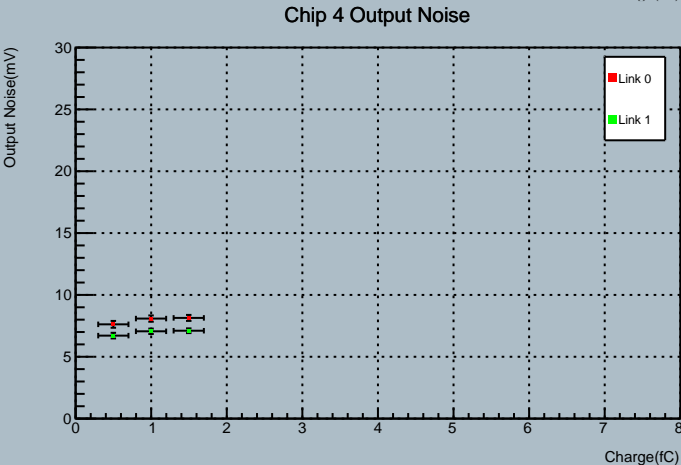
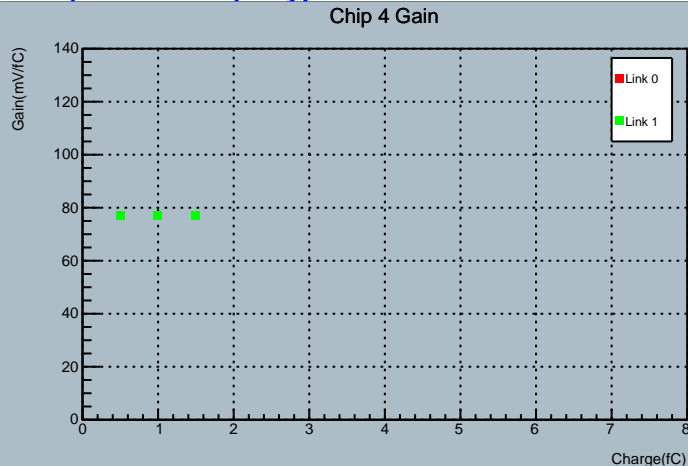
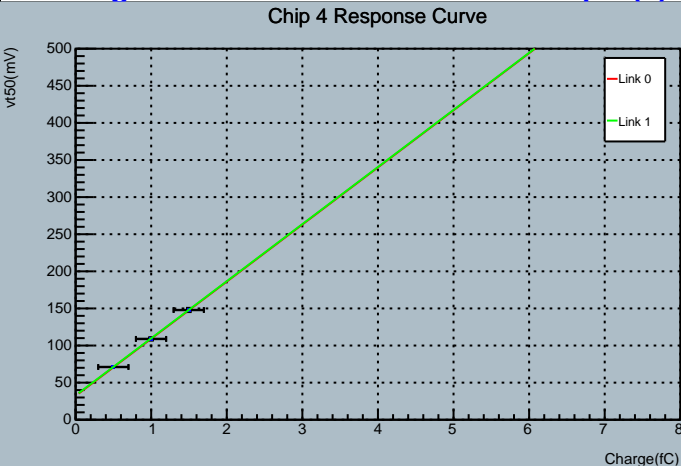
Chip 3 Output Noise



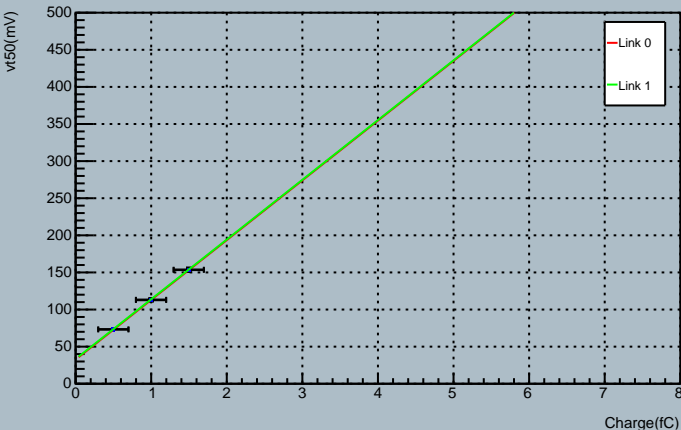
Chip 3 Input Noise



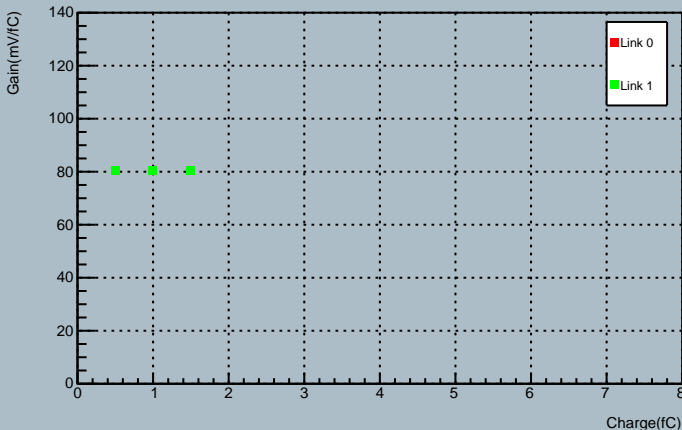




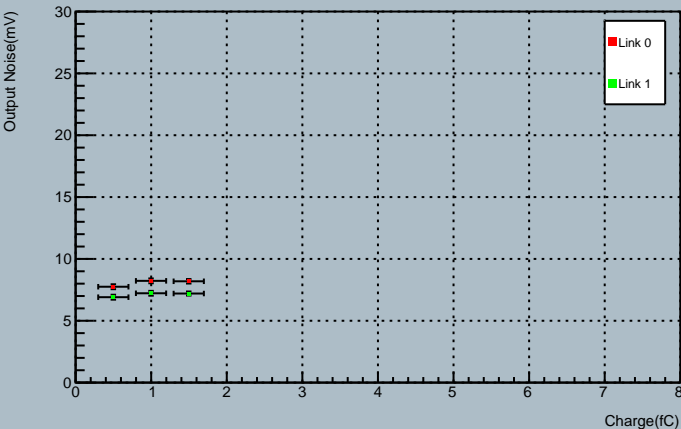
Chip 5 Response Curve



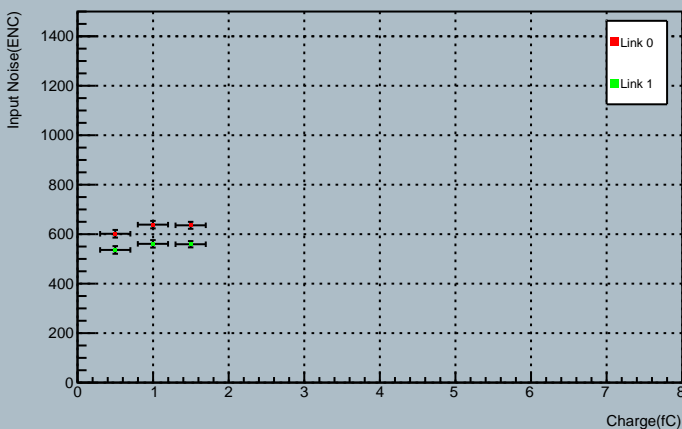
Chip 5 Gain

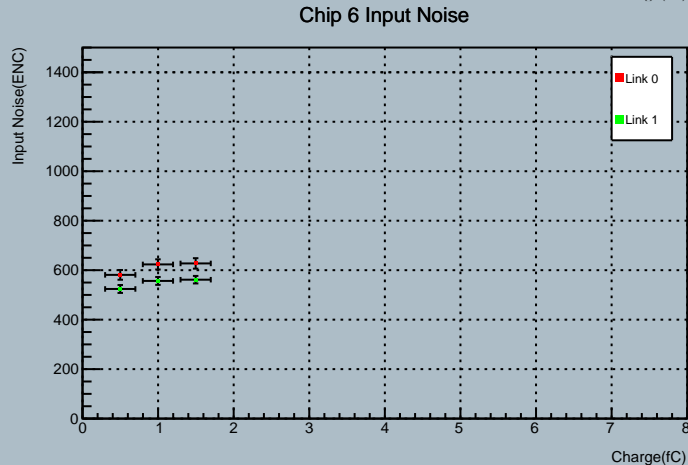
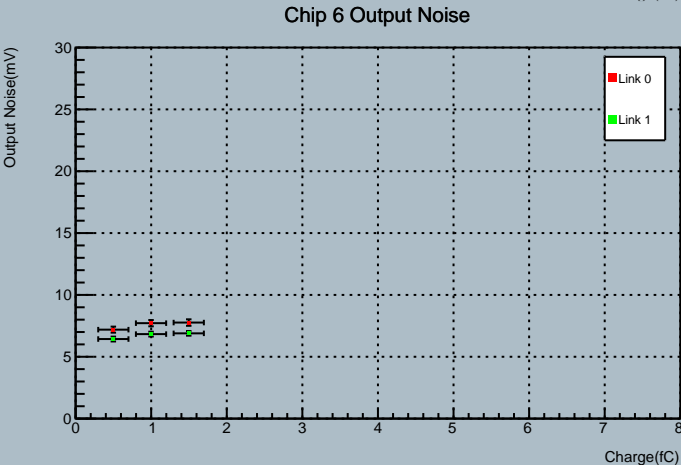
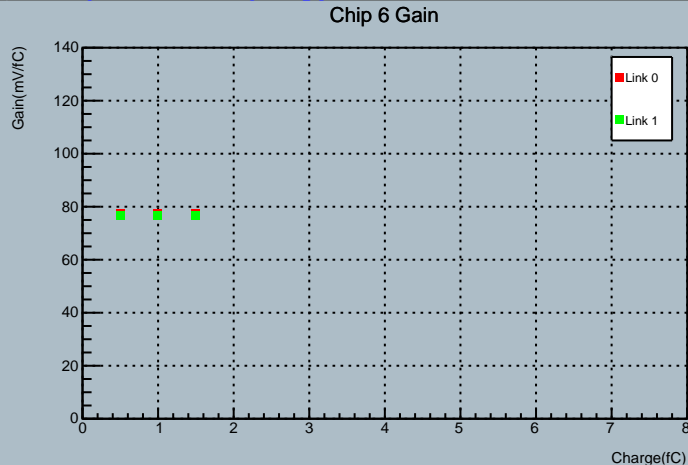
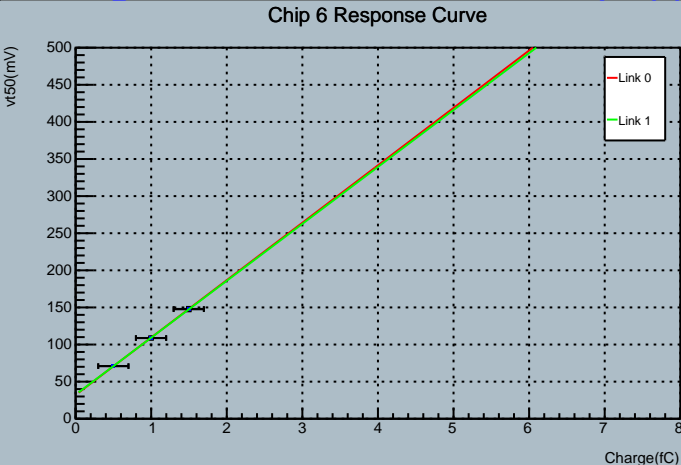


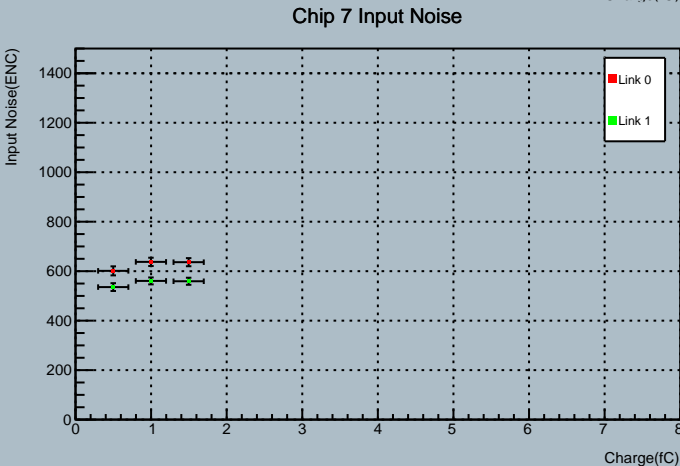
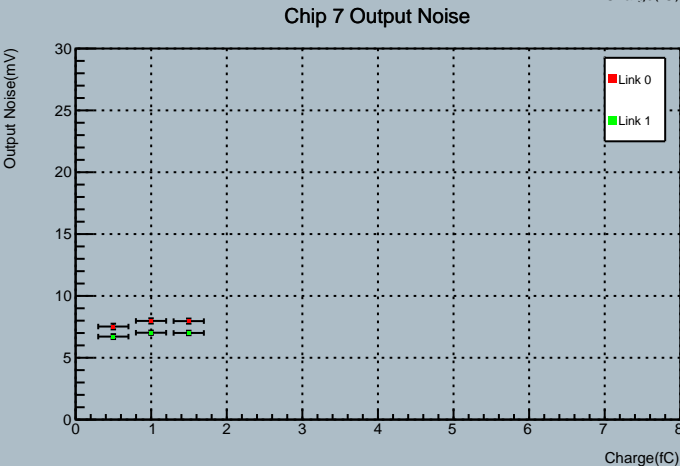
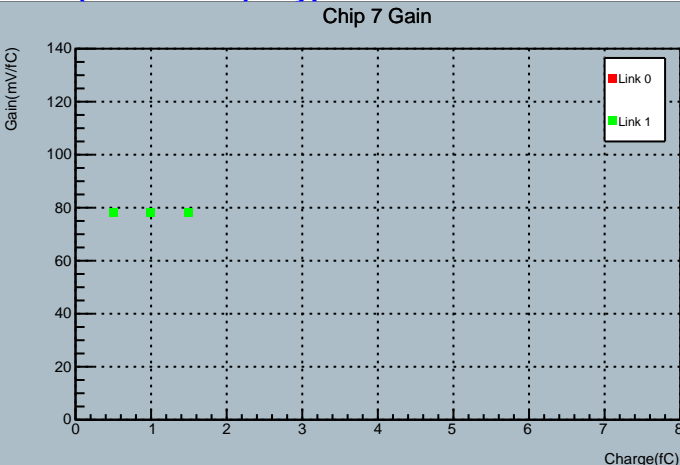
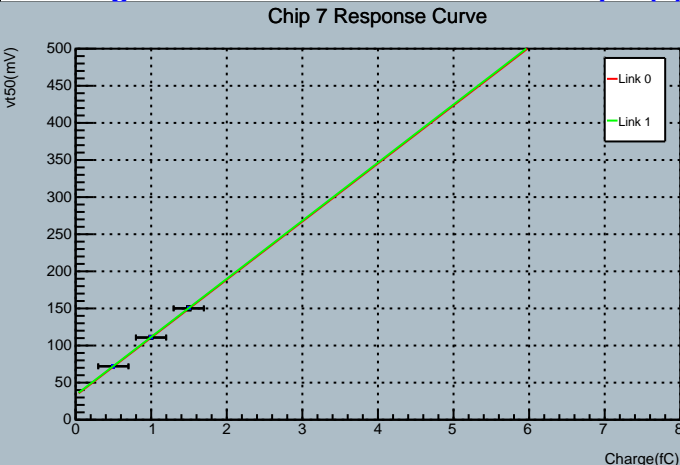
Chip 5 Output Noise

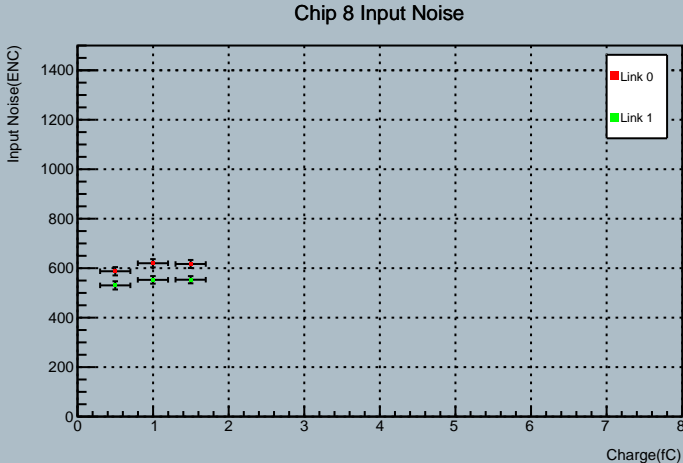
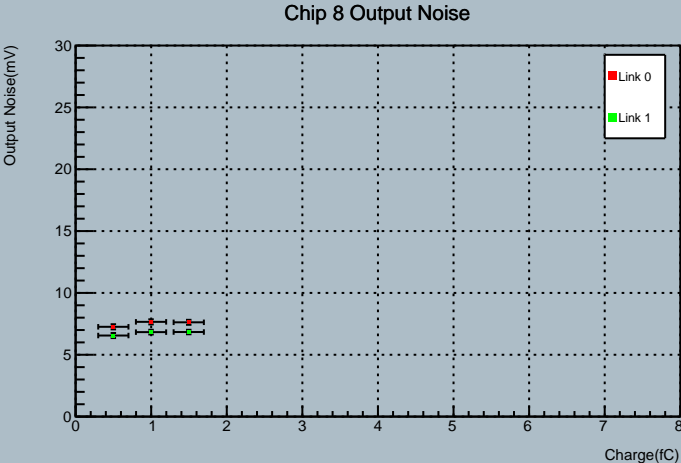
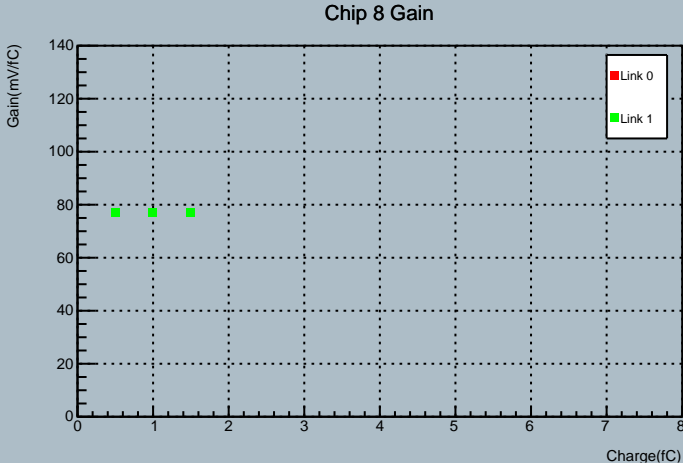
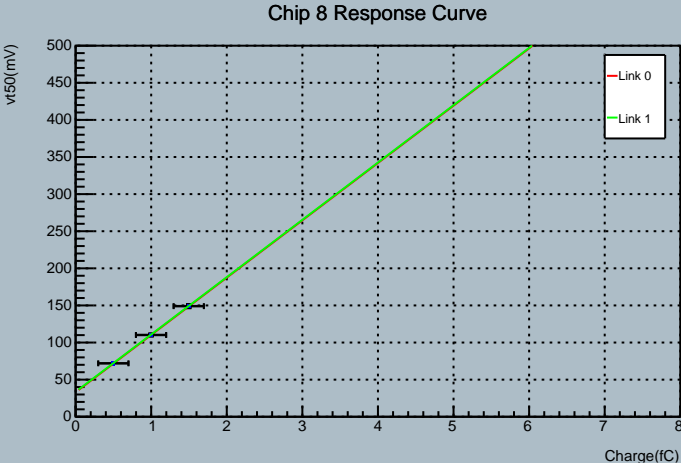


Chip 5 Input Noise

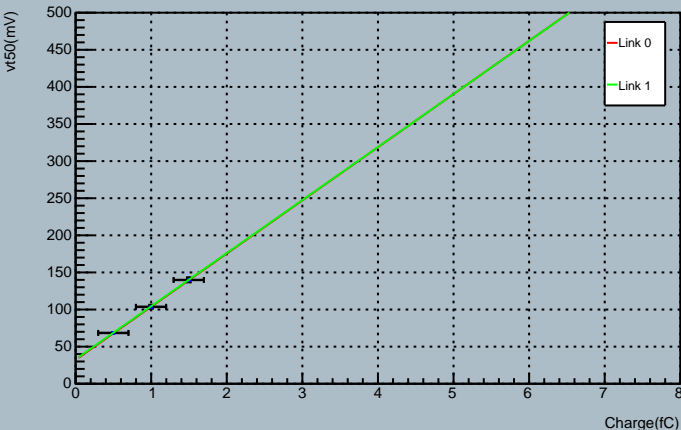




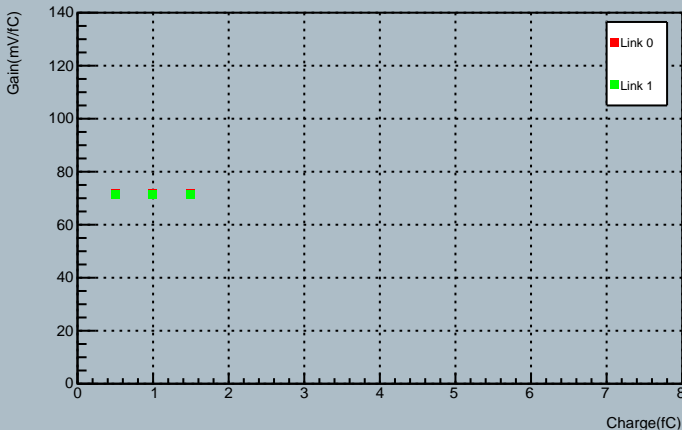




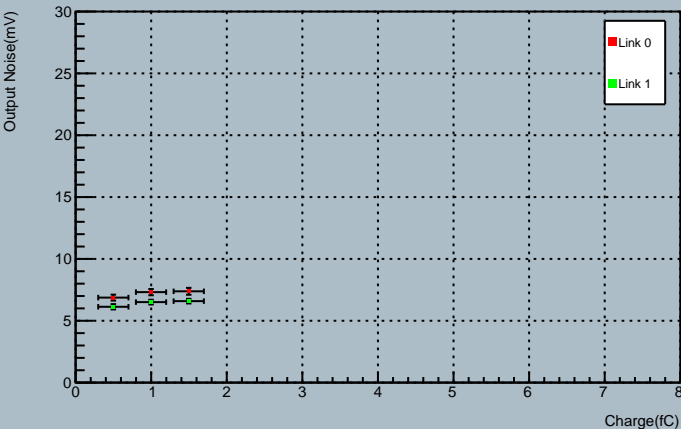
Chip 9 Response Curve



Chip 9 Gain



Chip 9 Output Noise



Chip 9 Input Noise

