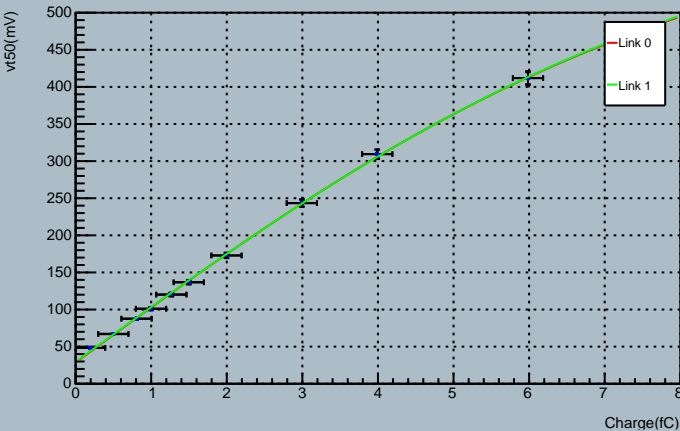
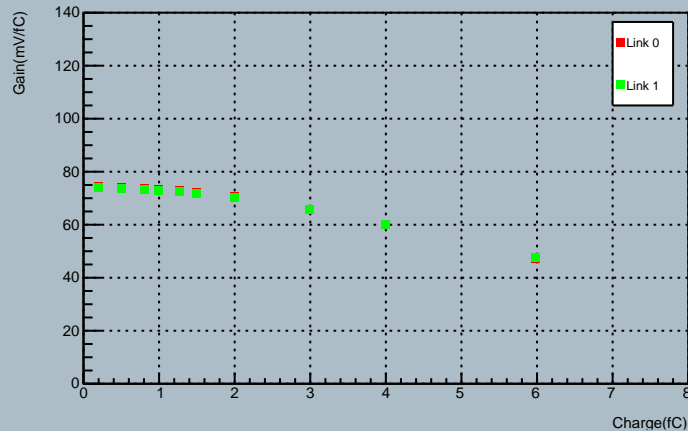


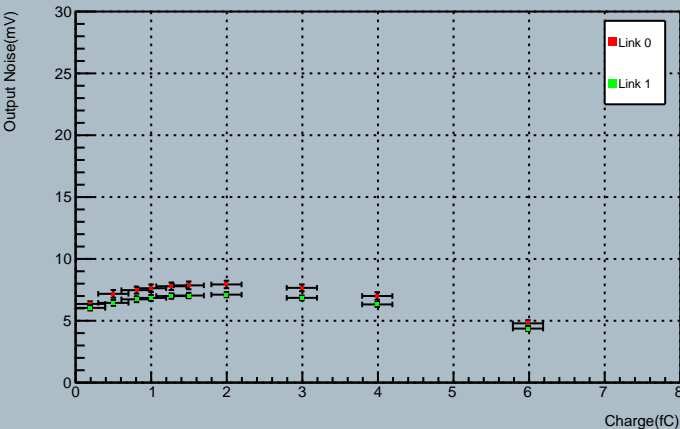
Chip 1 Response Curve



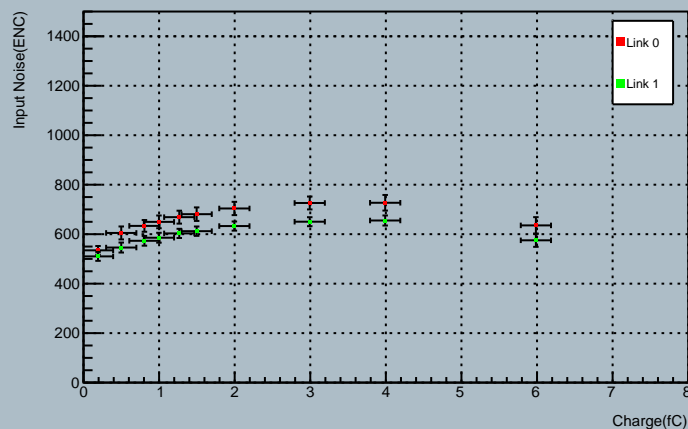
Chip 1 Gain

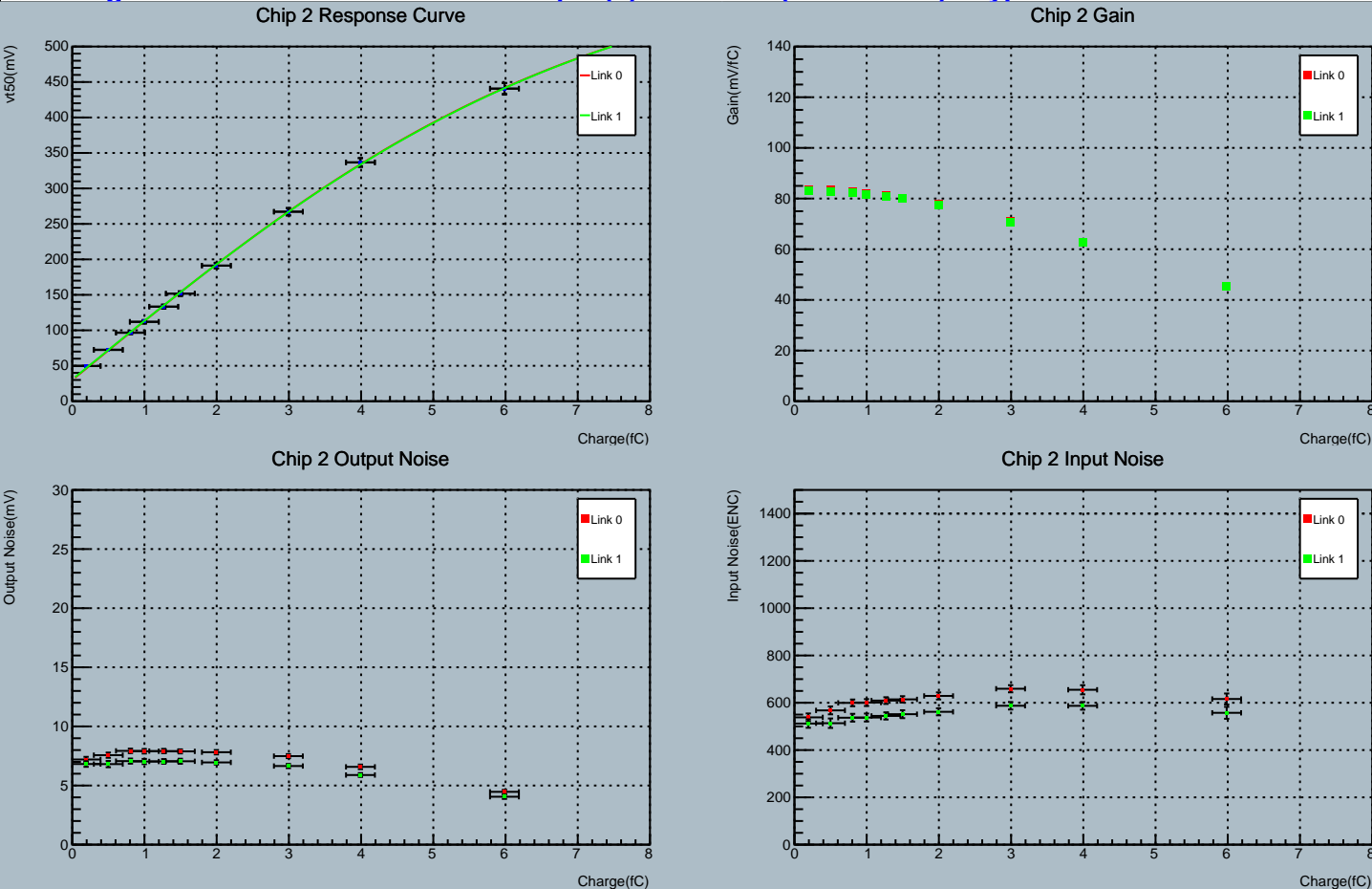


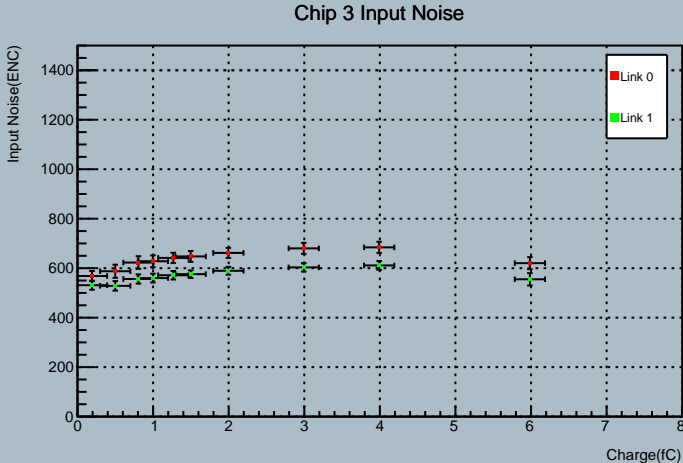
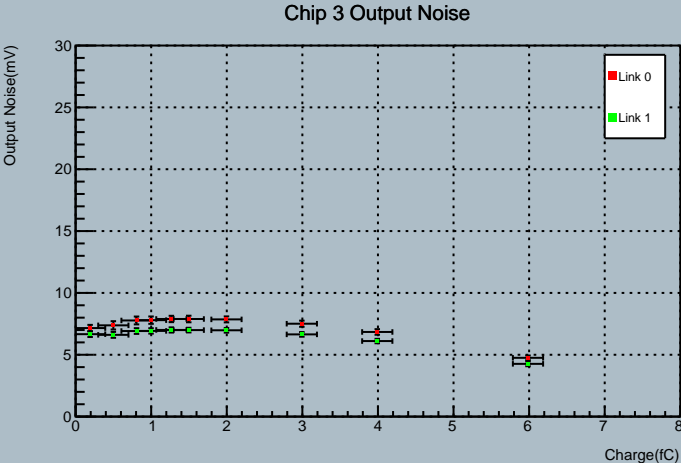
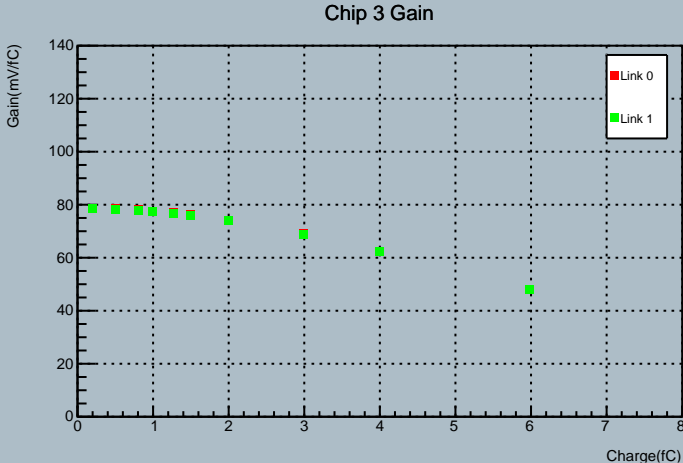
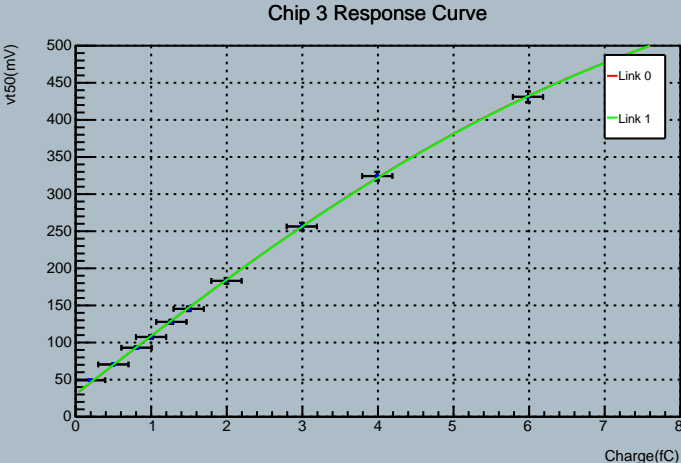
Chip 1 Output Noise



Chip 1 Input Noise

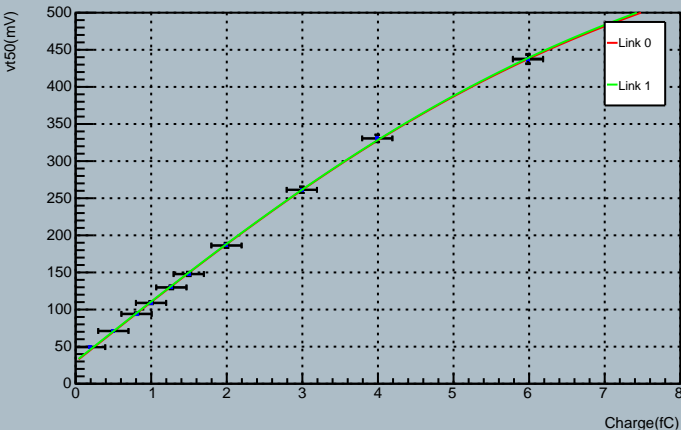




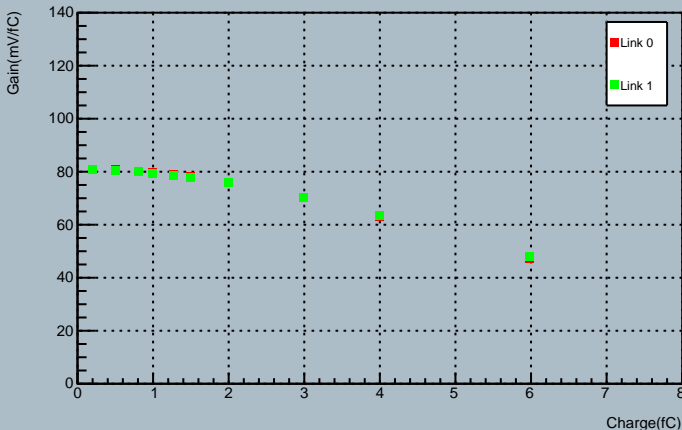




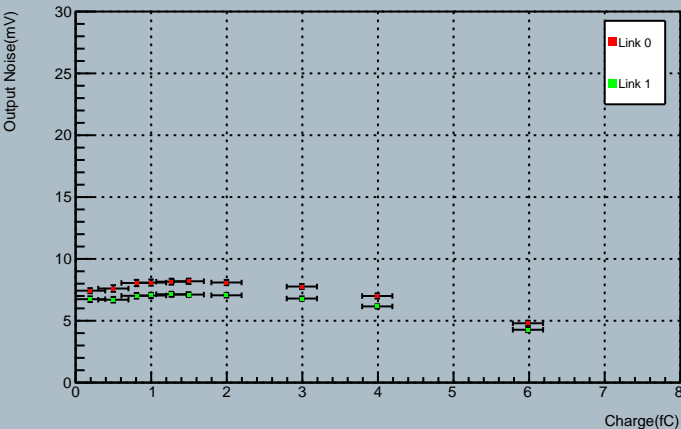
Chip 4 Response Curve



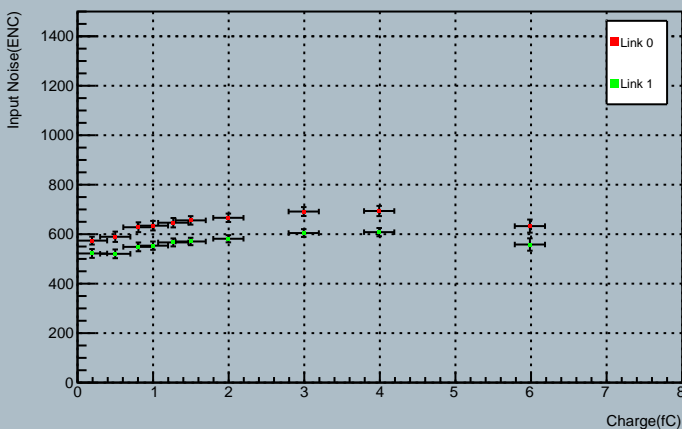
Chip 4 Gain



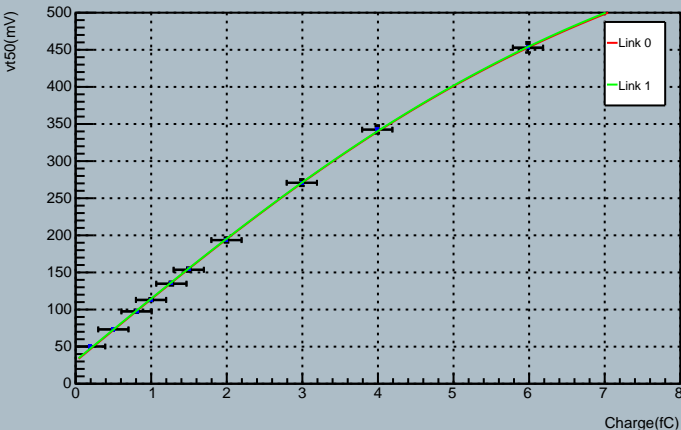
Chip 4 Output Noise



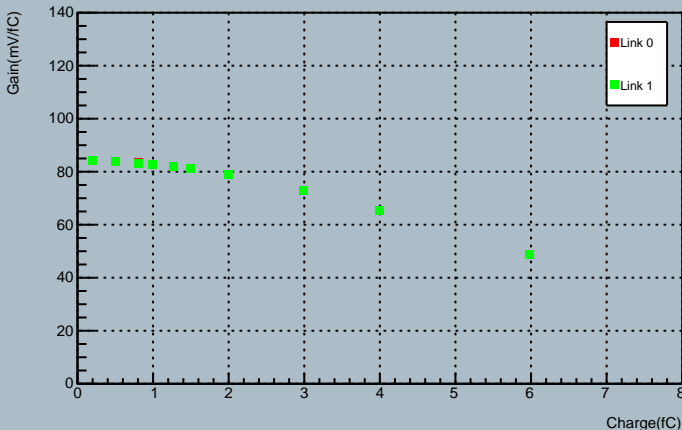
Chip 4 Input Noise



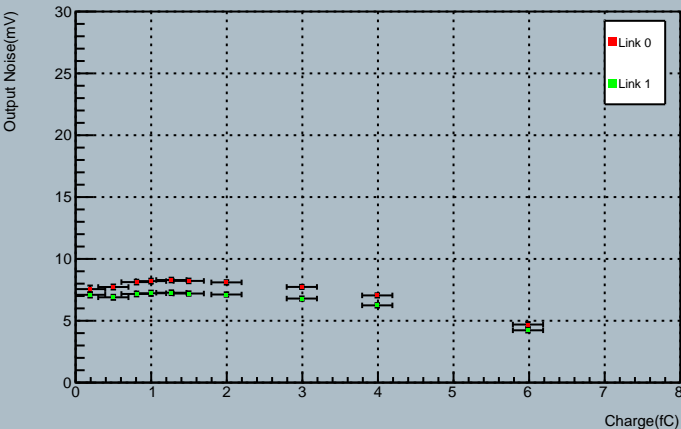
Chip 5 Response Curve



Chip 5 Gain



Chip 5 Output Noise



Chip 5 Input Noise

