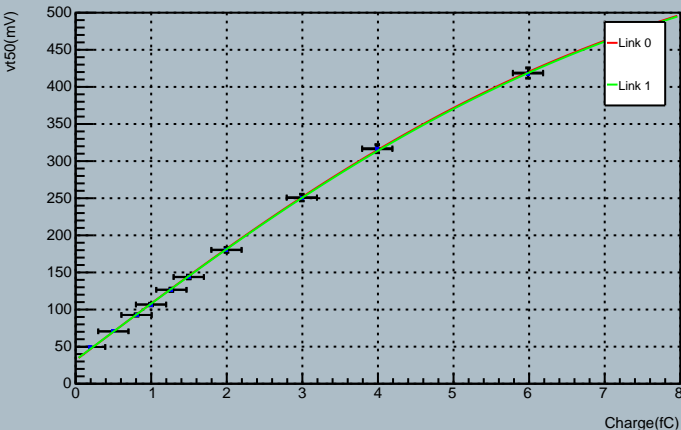
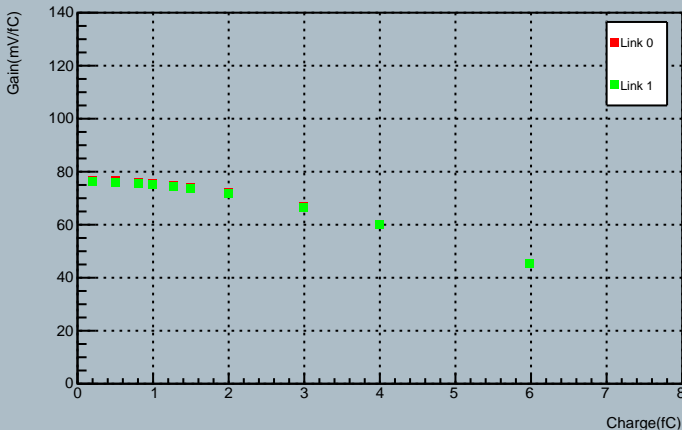


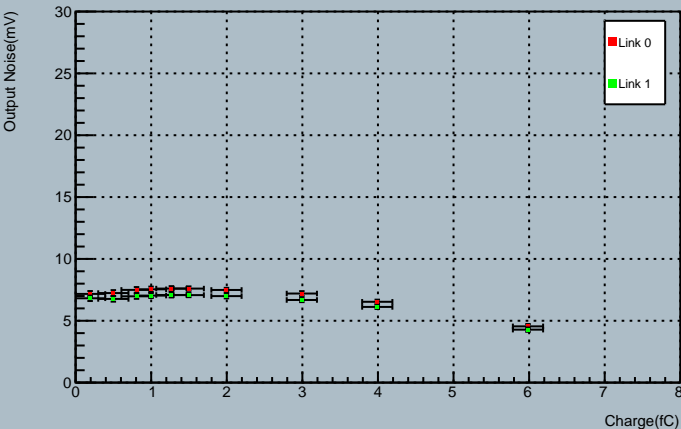
Chip 0 Response Curve



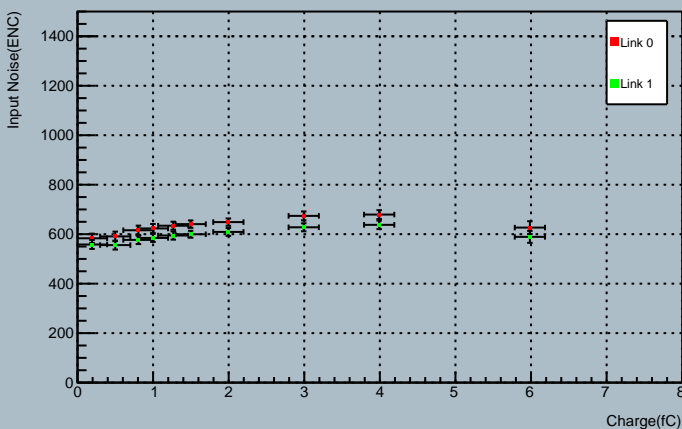
Chip 0 Gain

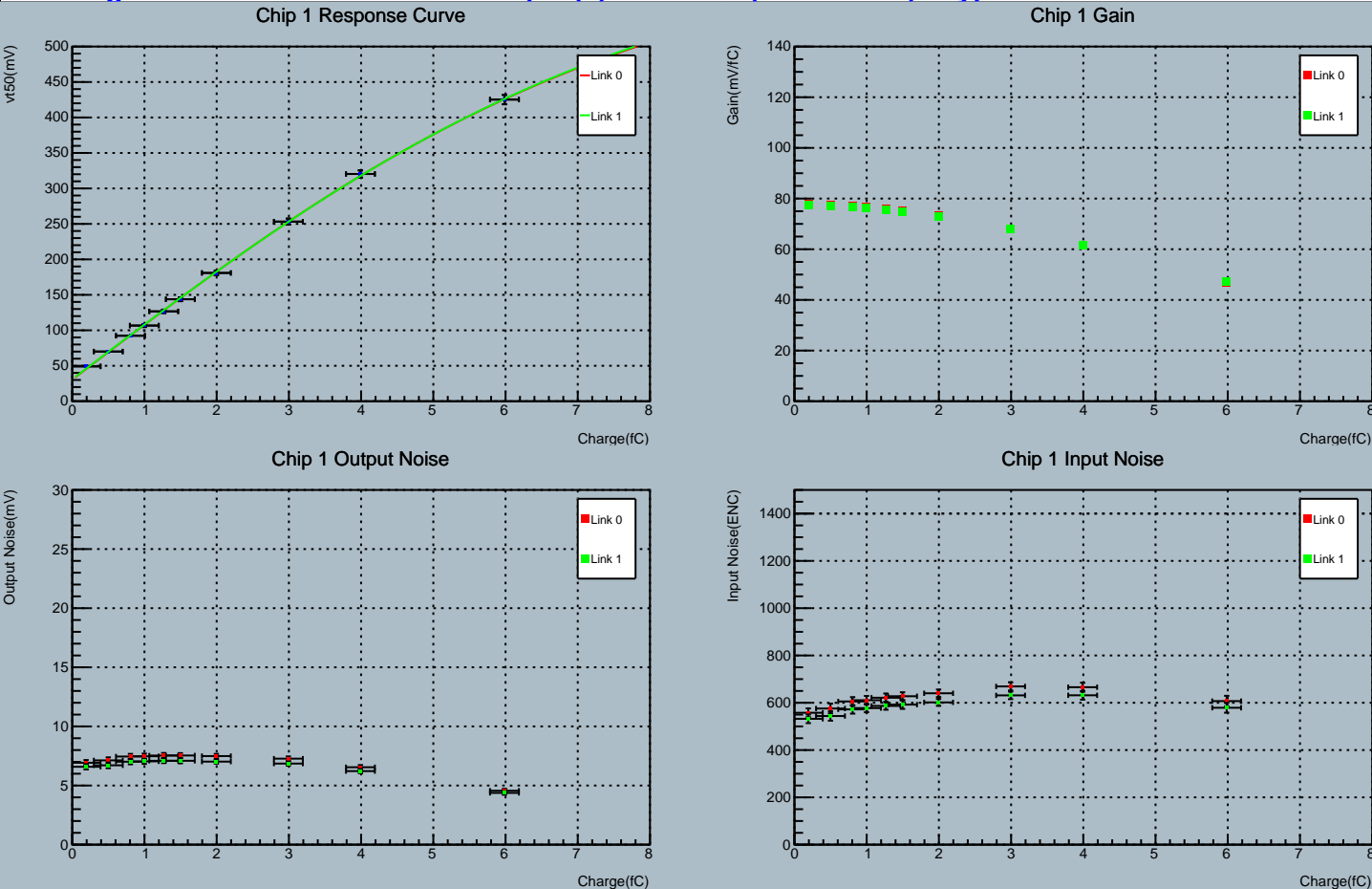


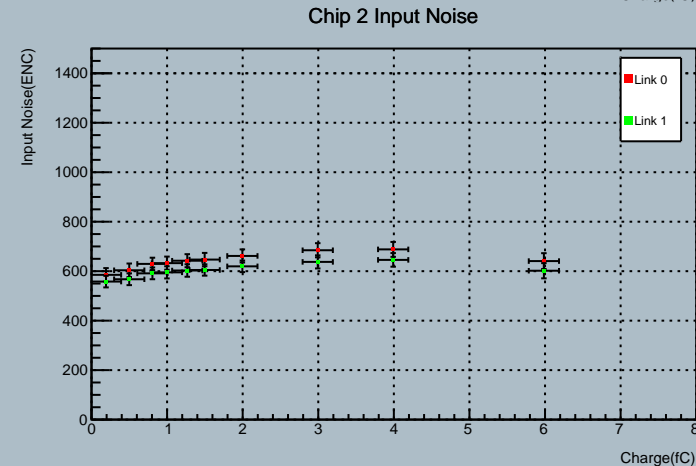
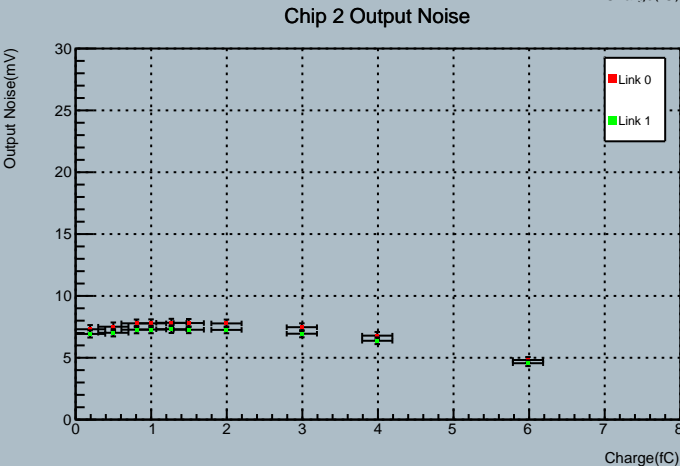
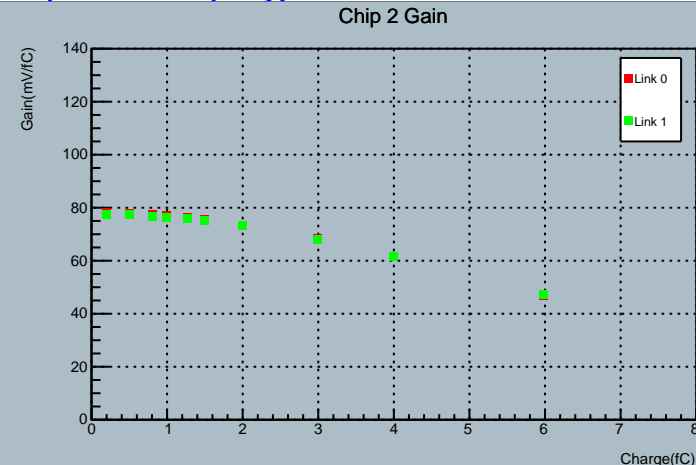
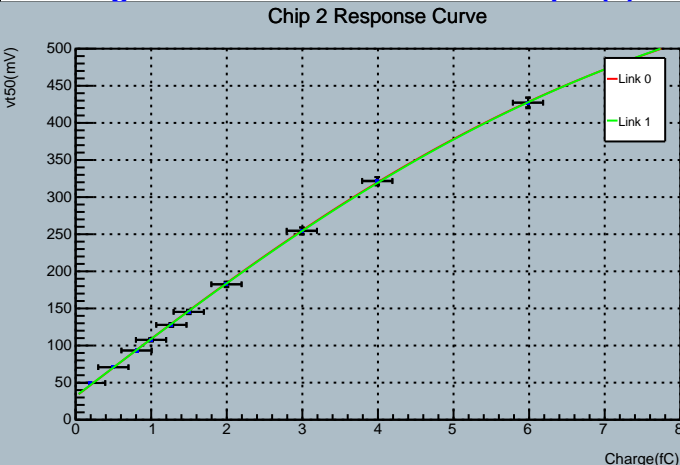
Chip 0 Output Noise

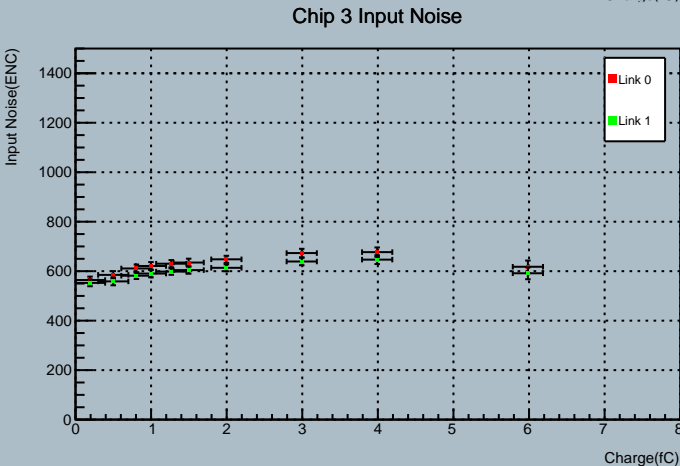
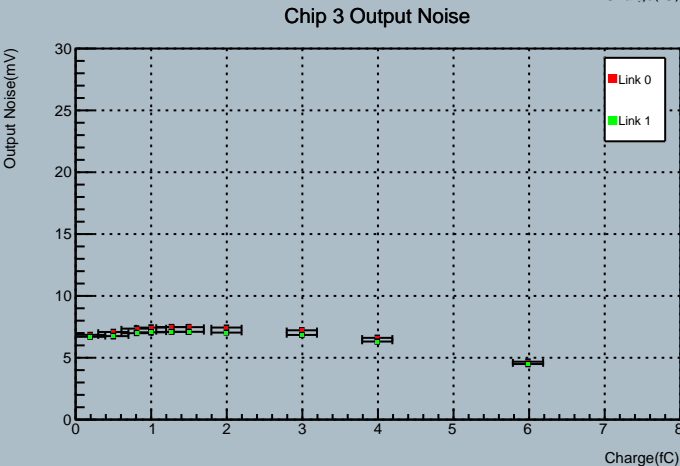
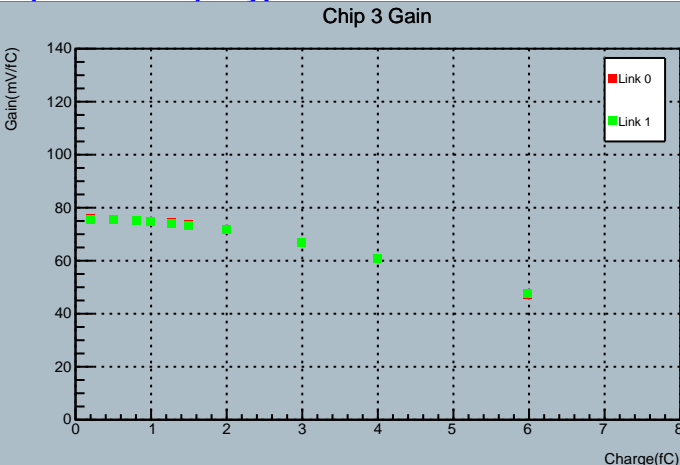
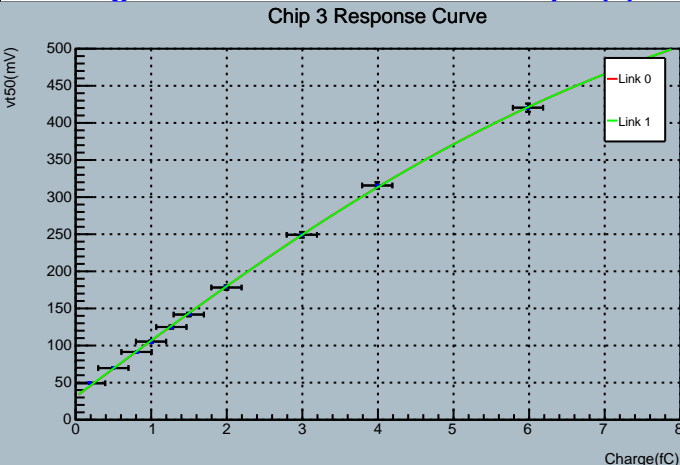


Chip 0 Input Noise



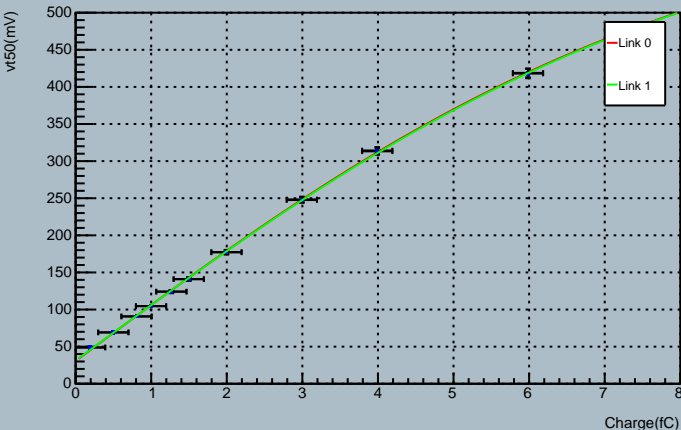




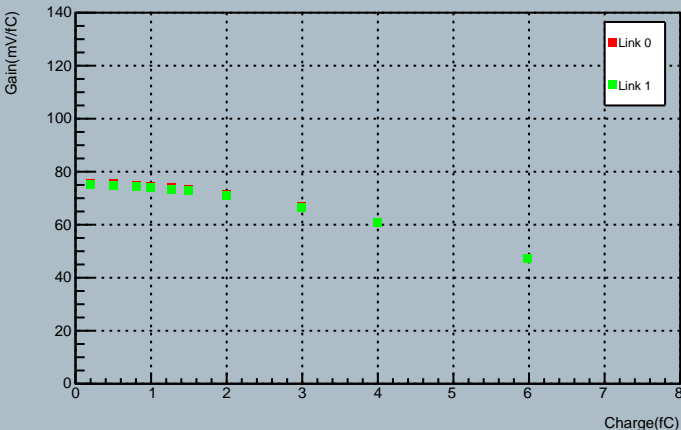




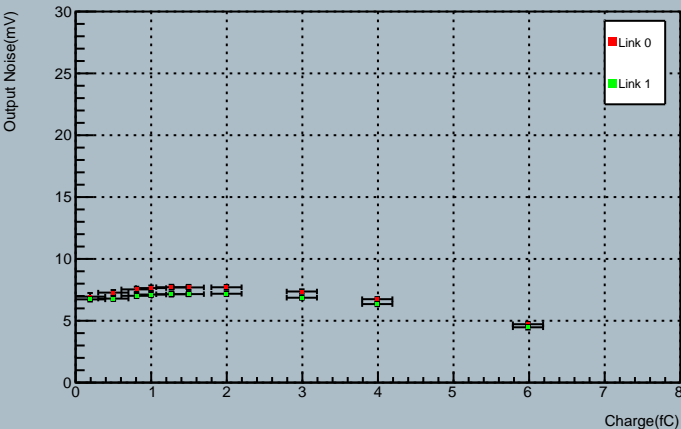
Chip 4 Response Curve



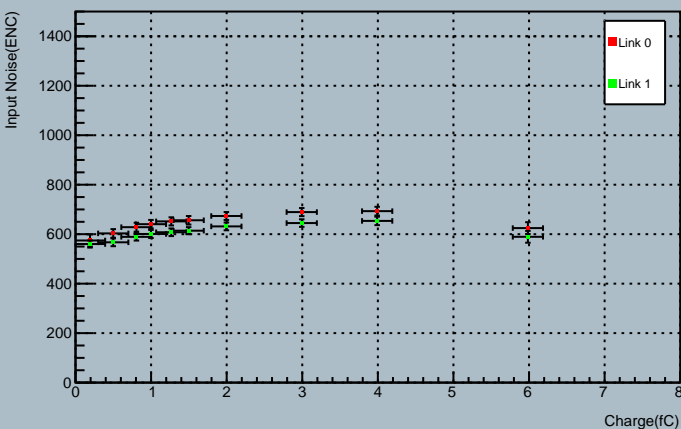
Chip 4 Gain

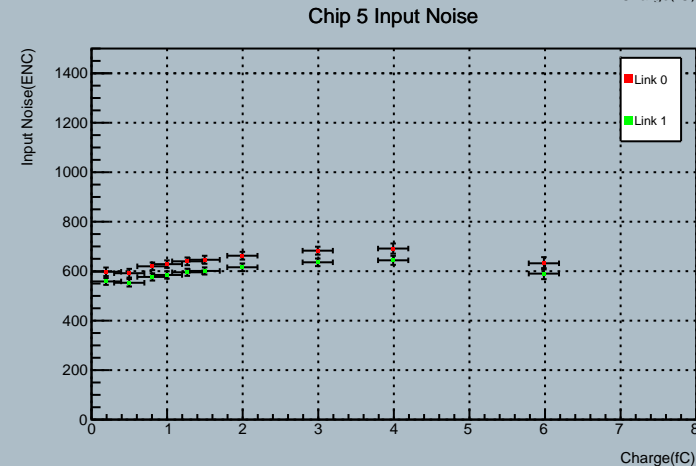
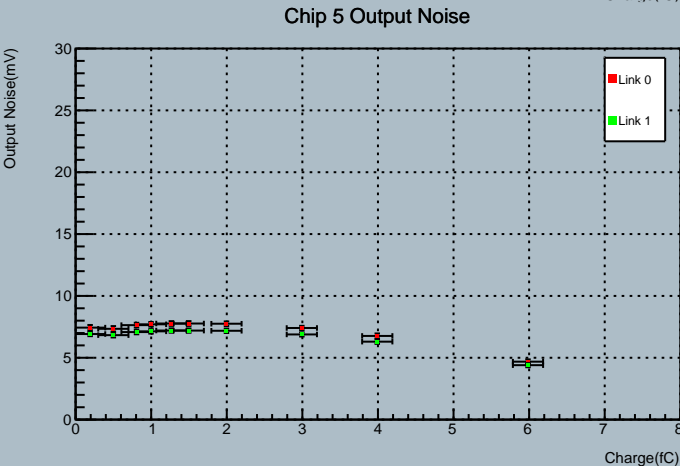
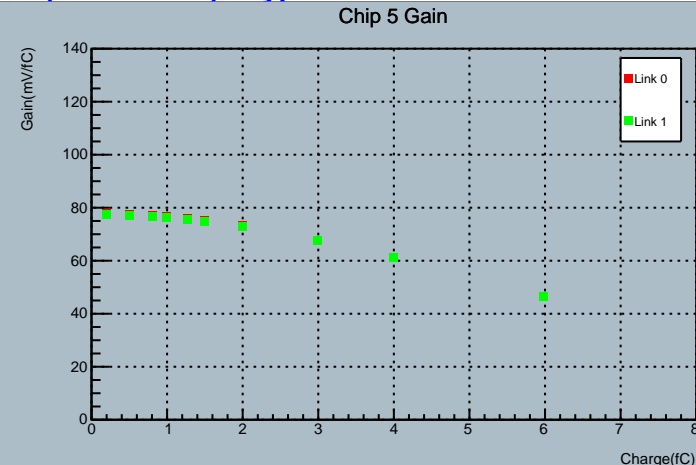
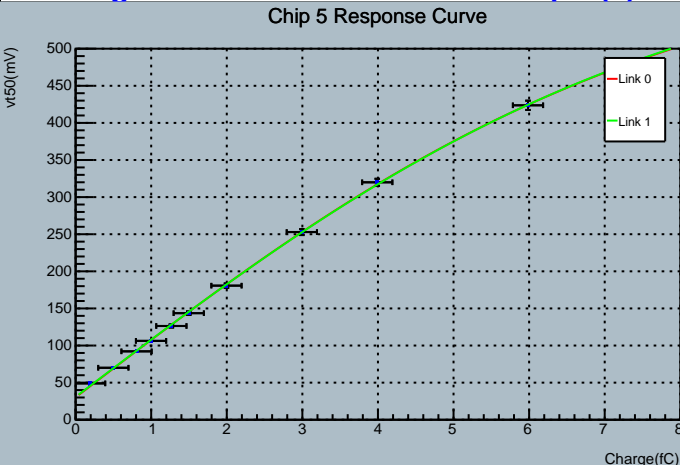


Chip 4 Output Noise

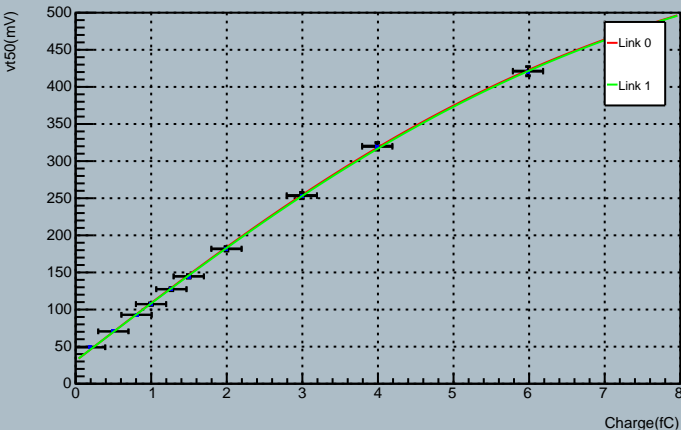


Chip 4 Input Noise

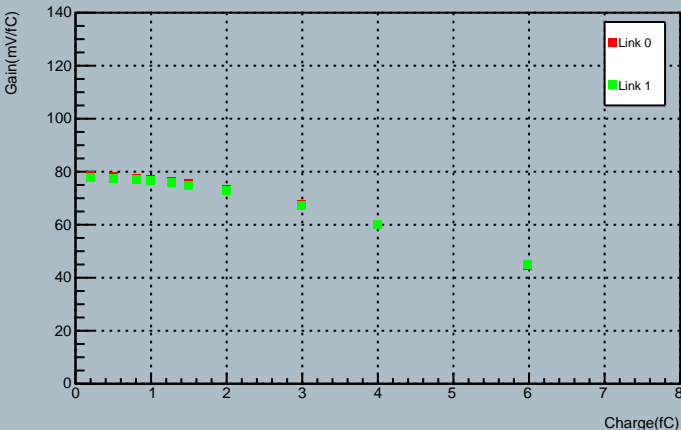




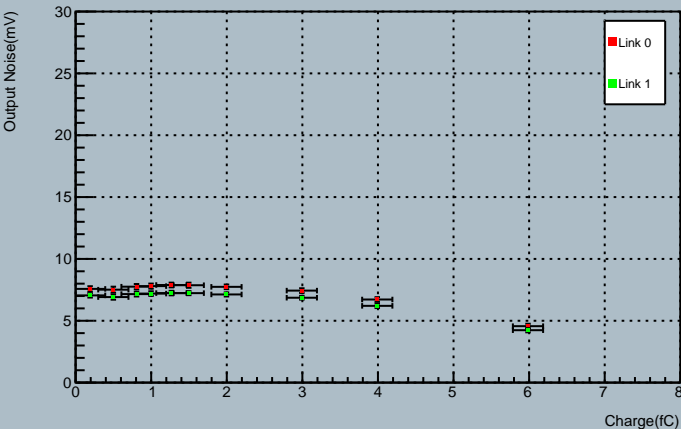
Chip 6 Response Curve



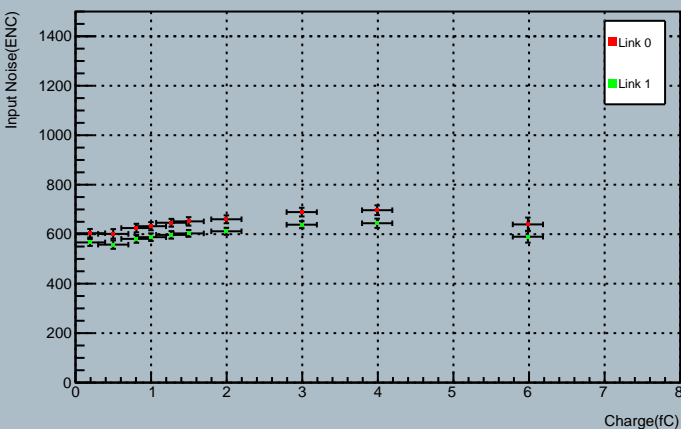
Chip 6 Gain



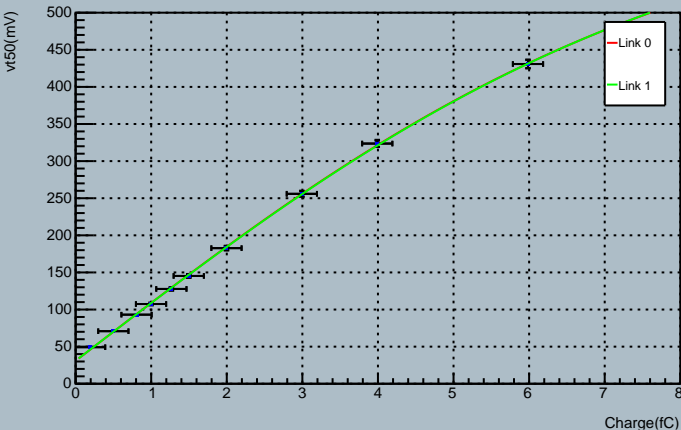
Chip 6 Output Noise



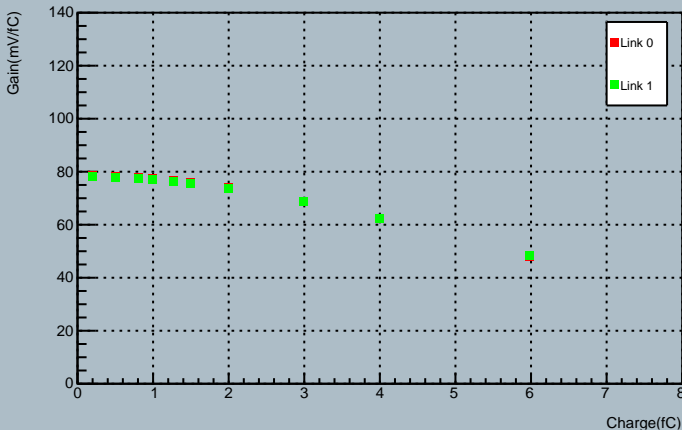
Chip 6 Input Noise



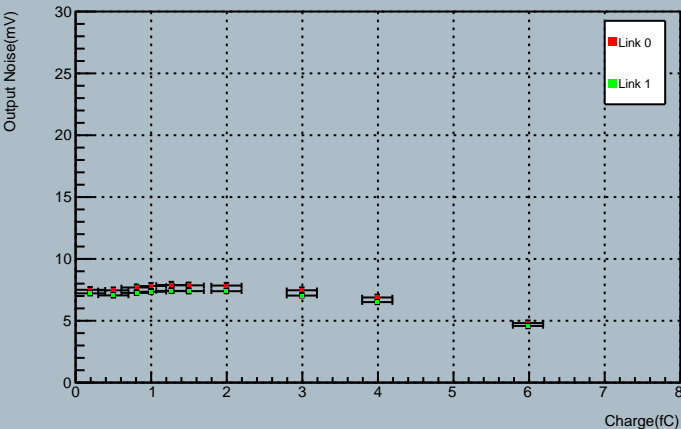
Chip 7 Response Curve



Chip 7 Gain



Chip 7 Output Noise



Chip 7 Input Noise

