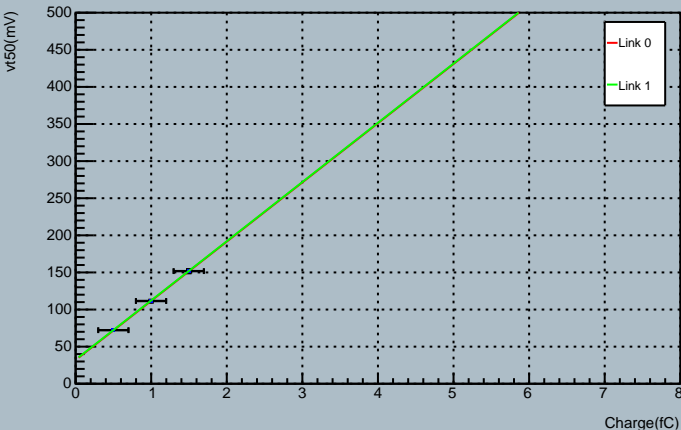
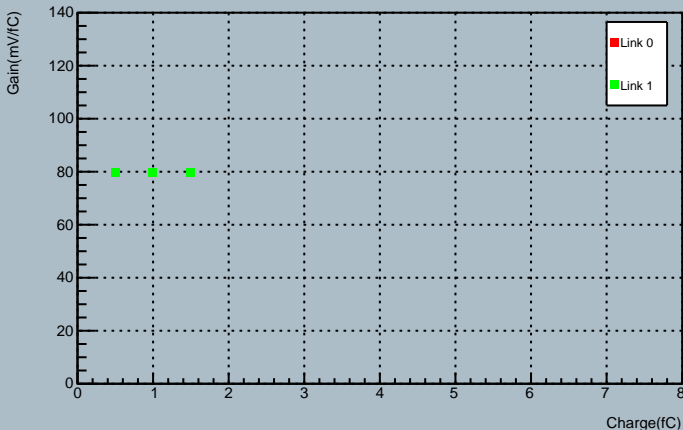


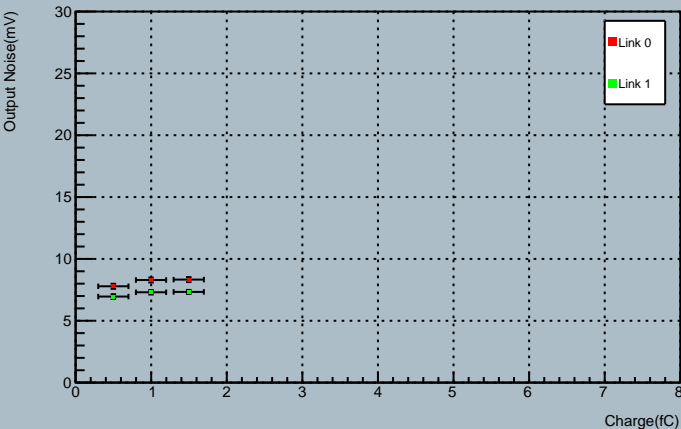
Chip 2 Response Curve



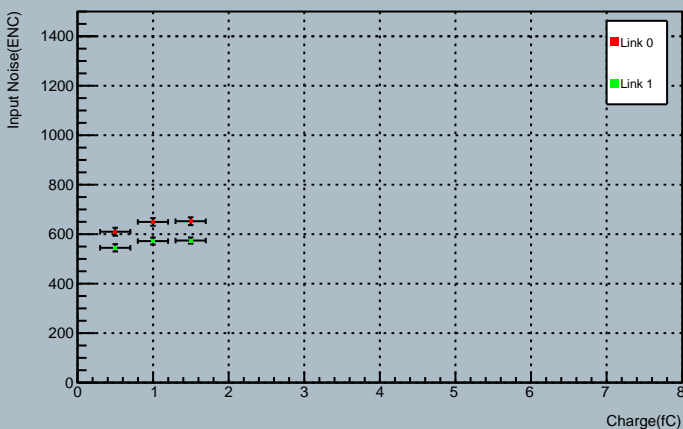
Chip 2 Gain

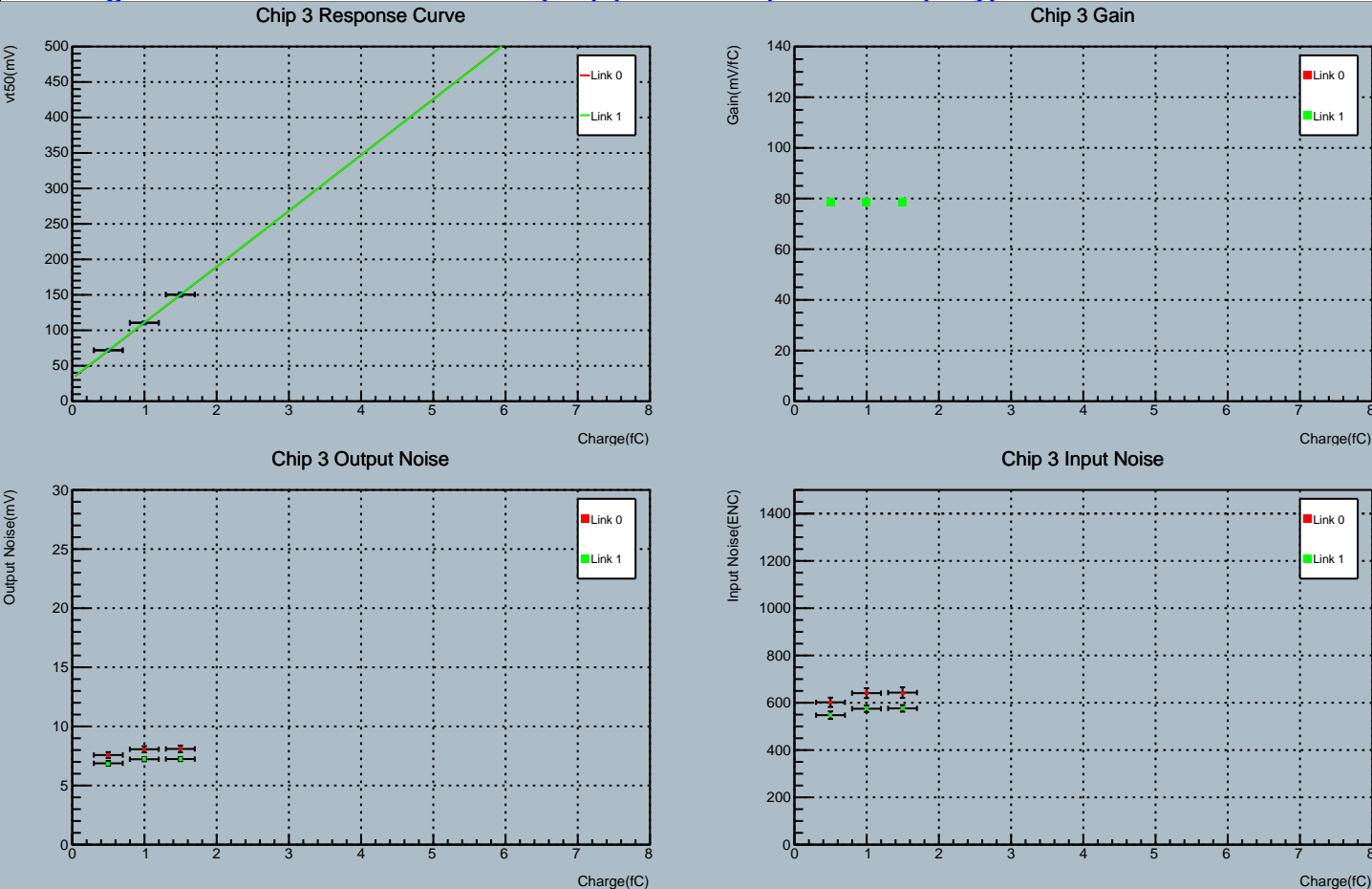


Chip 2 Output Noise



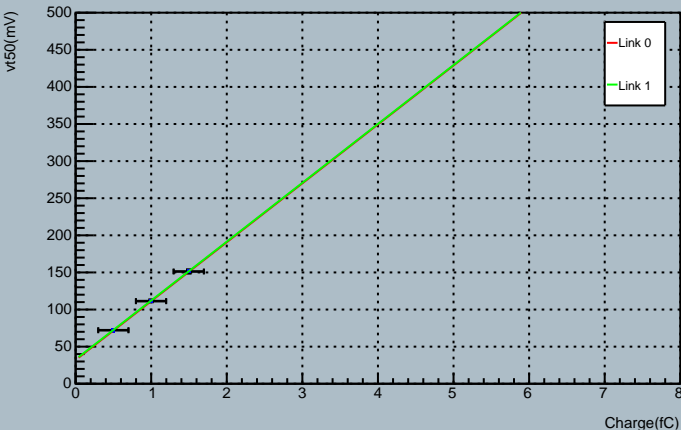
Chip 2 Input Noise



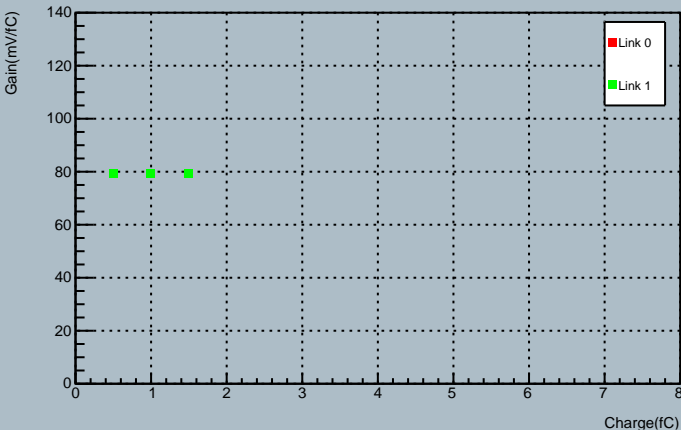




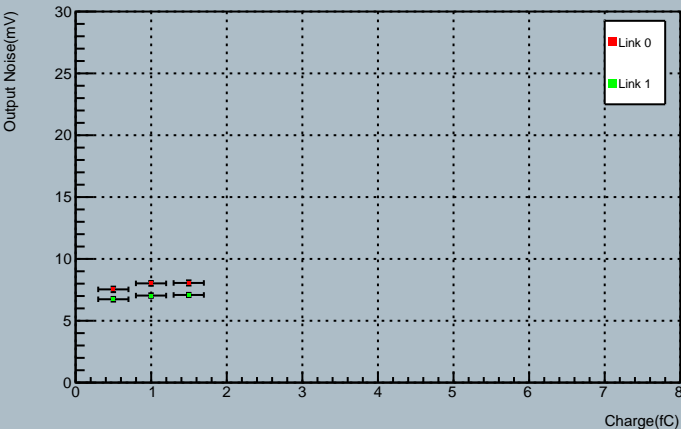
Chip 4 Response Curve



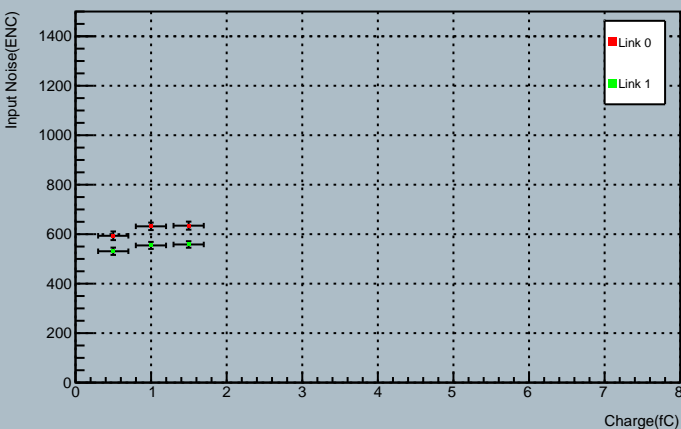
Chip 4 Gain



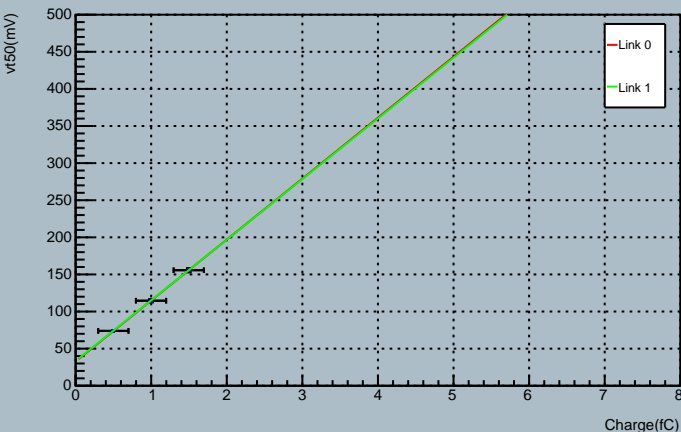
Chip 4 Output Noise



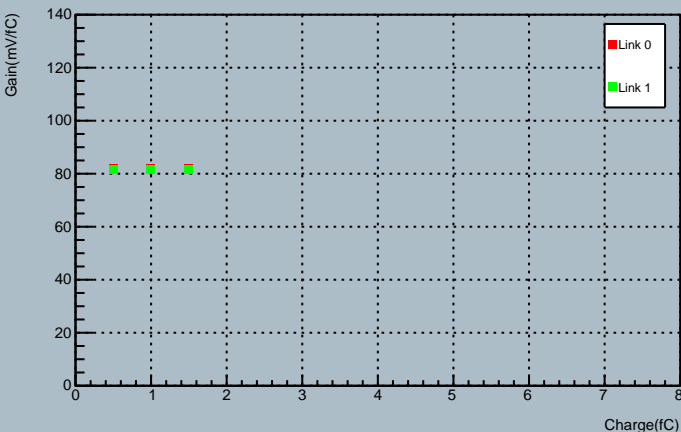
Chip 4 Input Noise



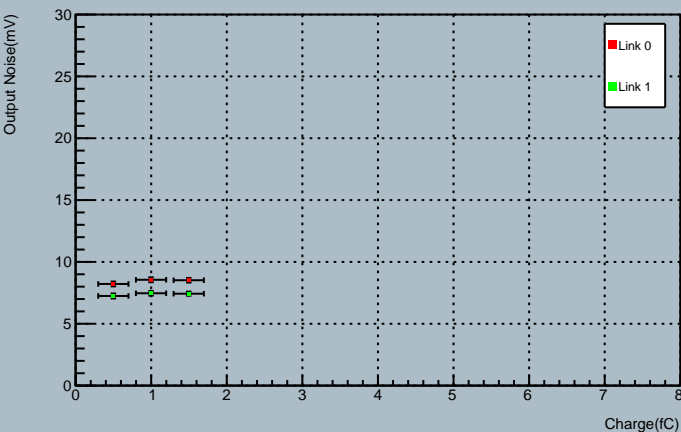
Chip 5 Response Curve



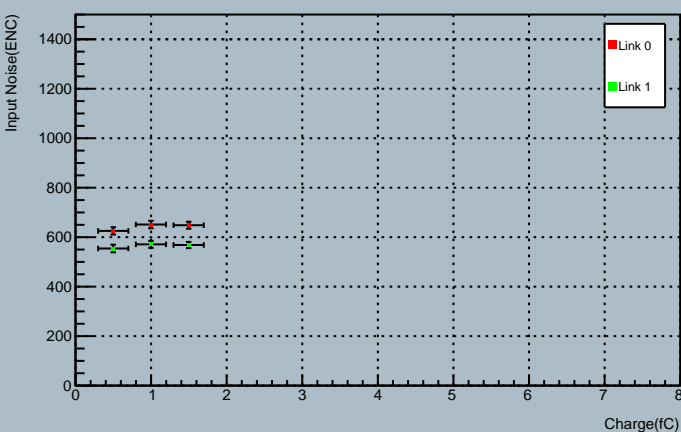
Chip 5 Gain



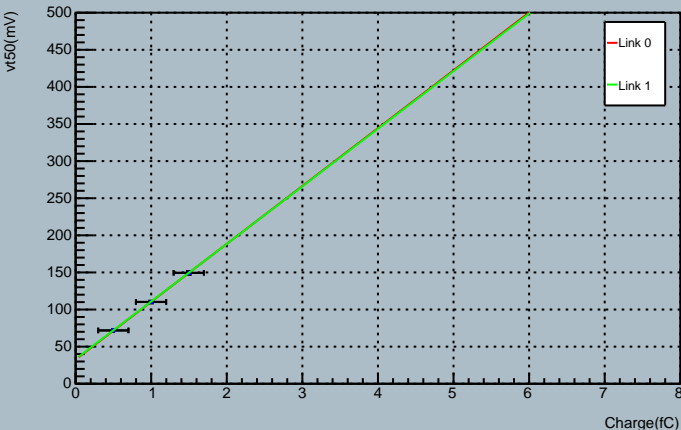
Chip 5 Output Noise



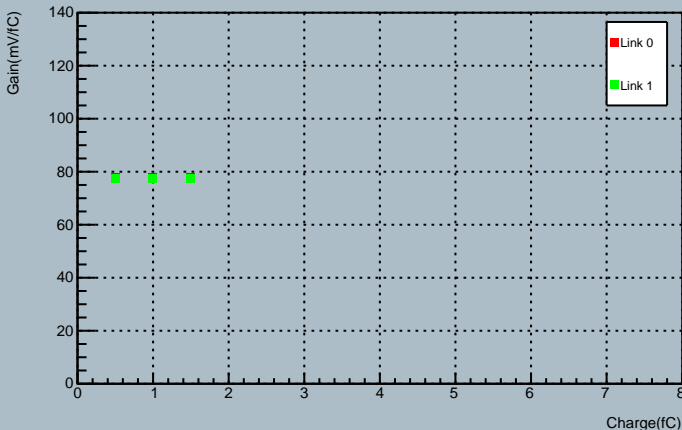
Chip 5 Input Noise



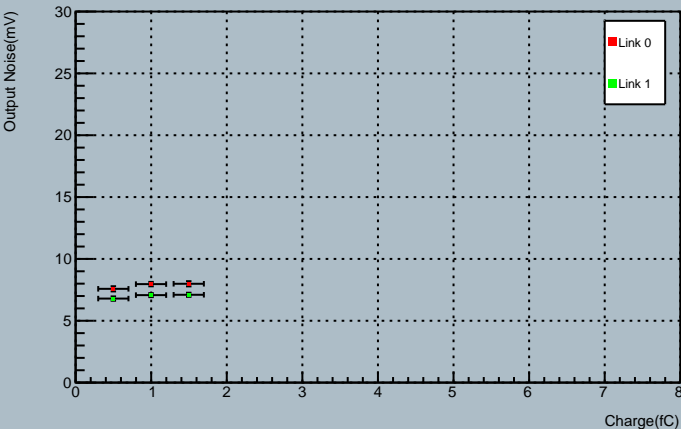
Chip 6 Response Curve



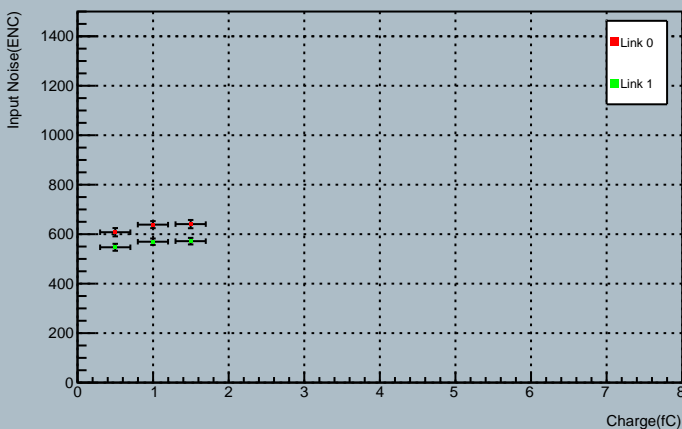
Chip 6 Gain



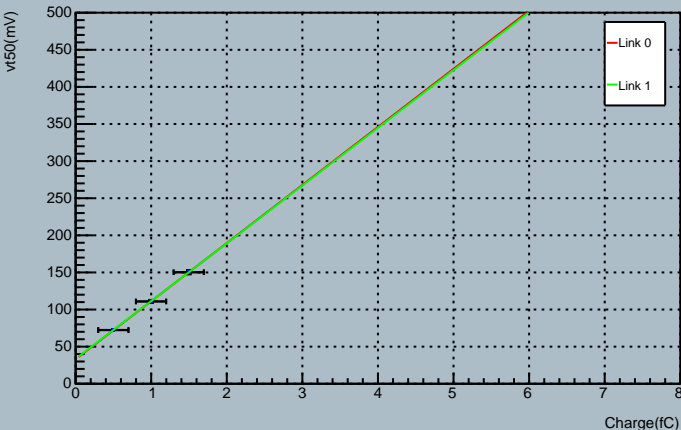
Chip 6 Output Noise



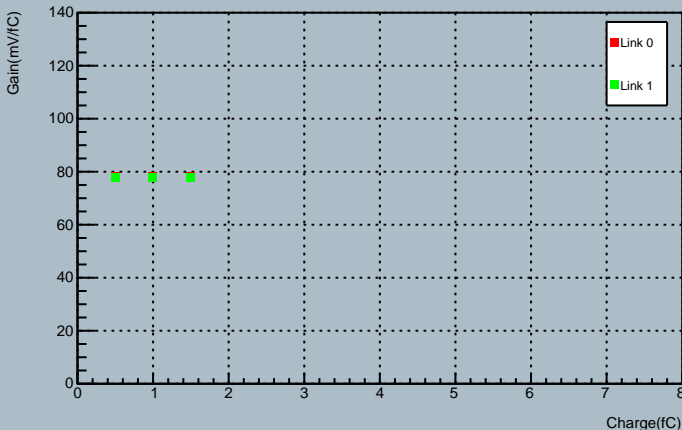
Chip 6 Input Noise



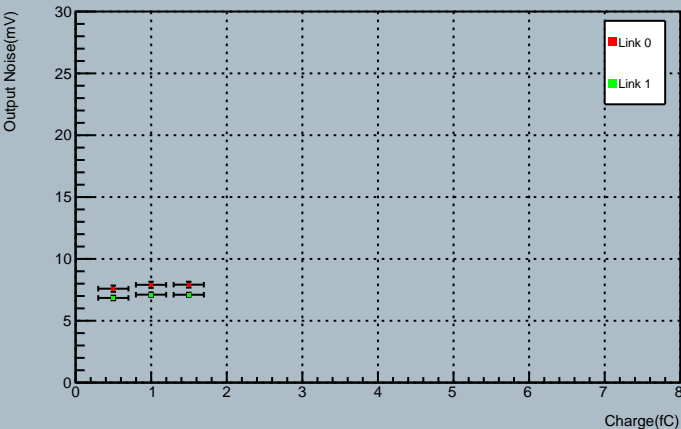
Chip 7 Response Curve



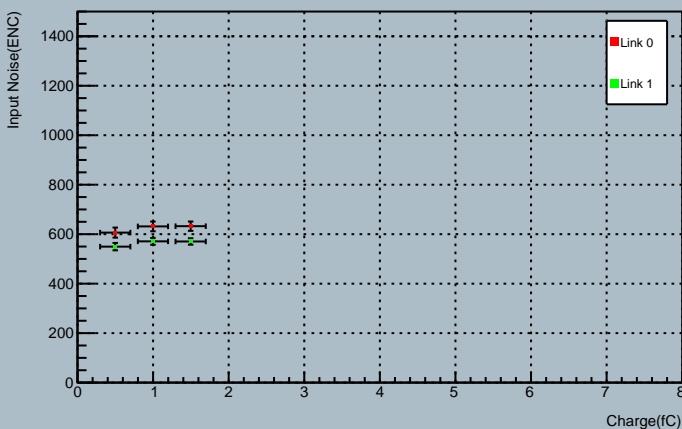
Chip 7 Gain



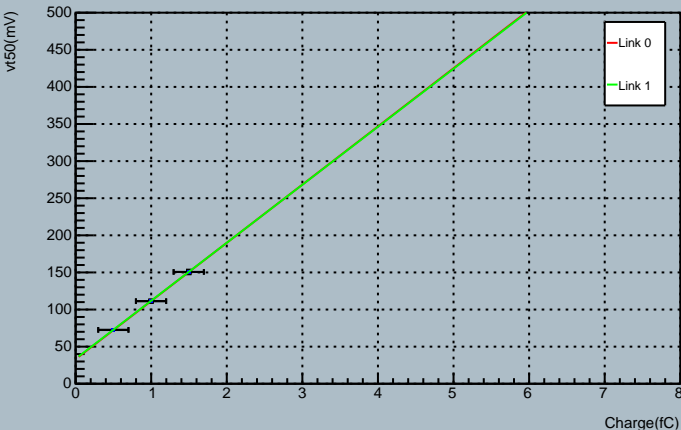
Chip 7 Output Noise



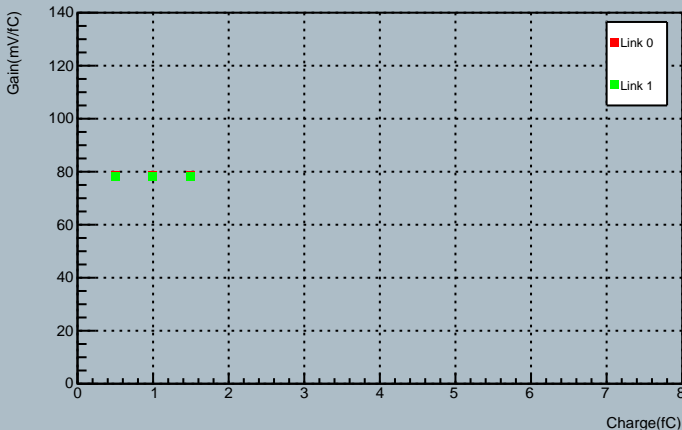
Chip 7 Input Noise



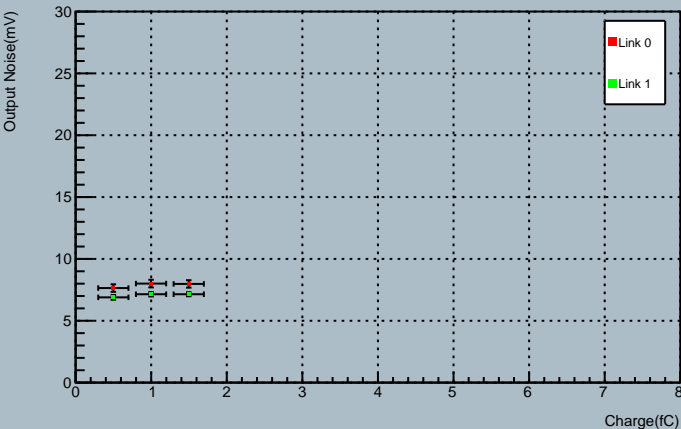
Chip 8 Response Curve



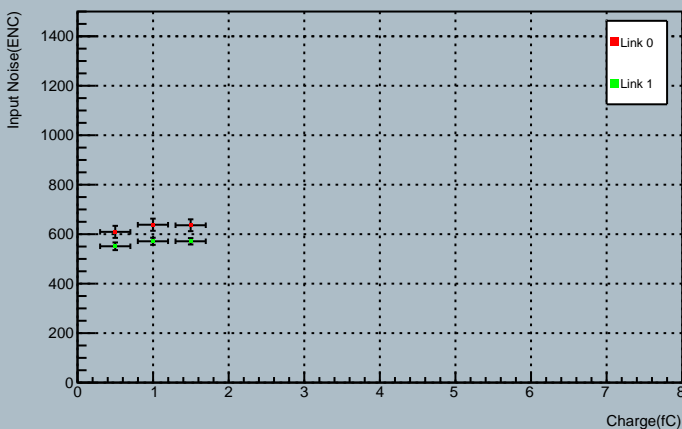
Chip 8 Gain



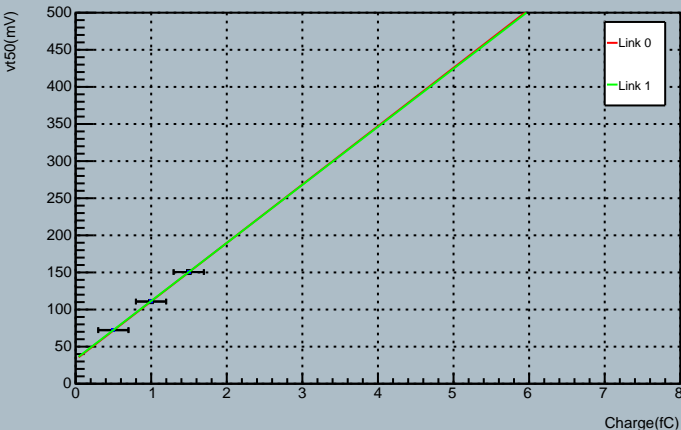
Chip 8 Output Noise



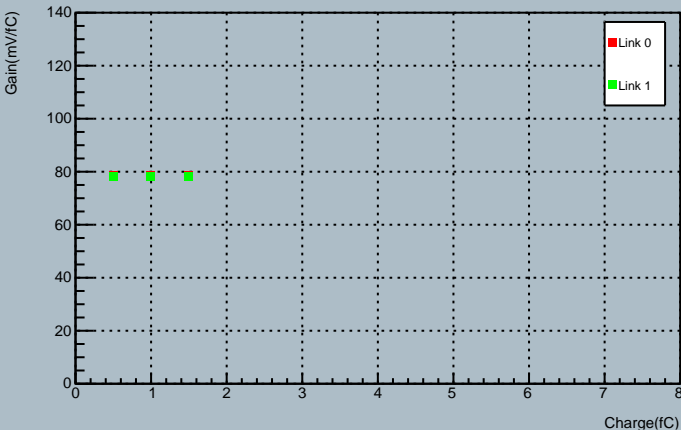
Chip 8 Input Noise



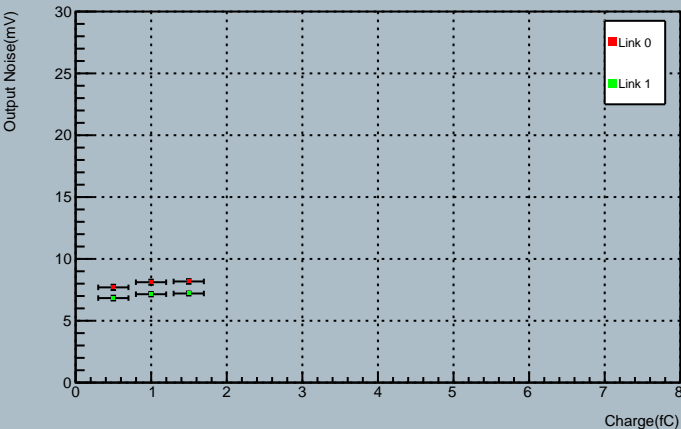
Chip 9 Response Curve



Chip 9 Gain



Chip 9 Output Noise



Chip 9 Input Noise

