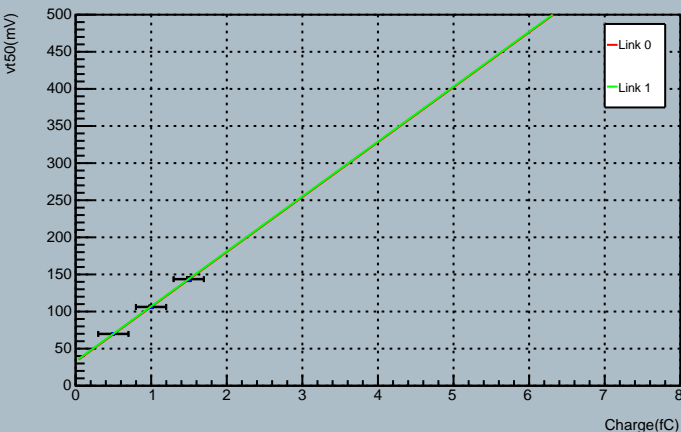
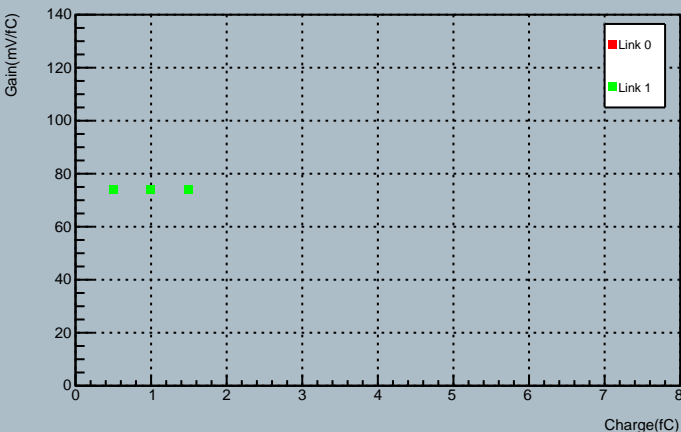


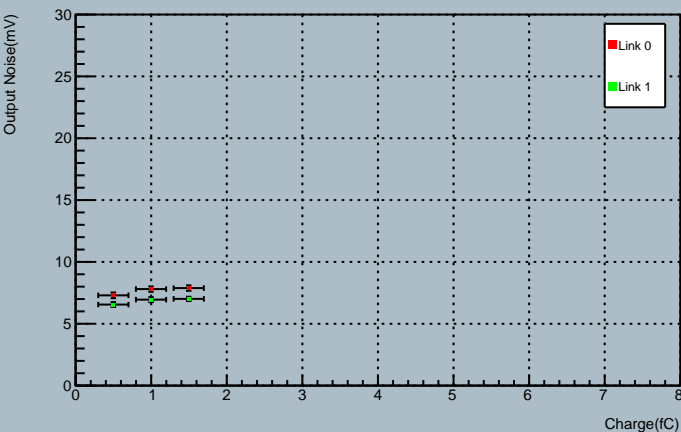
Chip 1 Response Curve



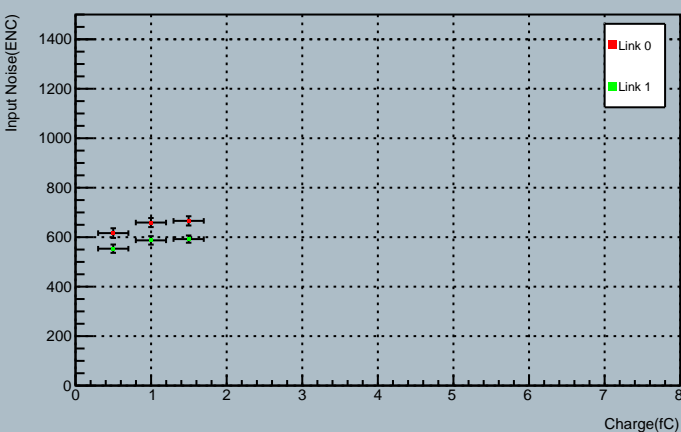
Chip 1 Gain

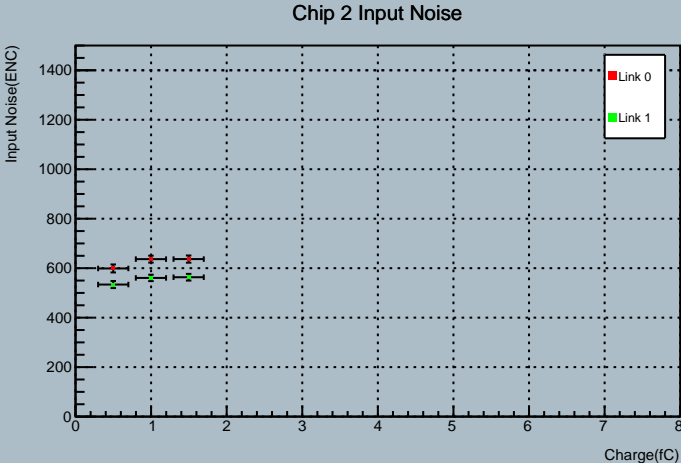
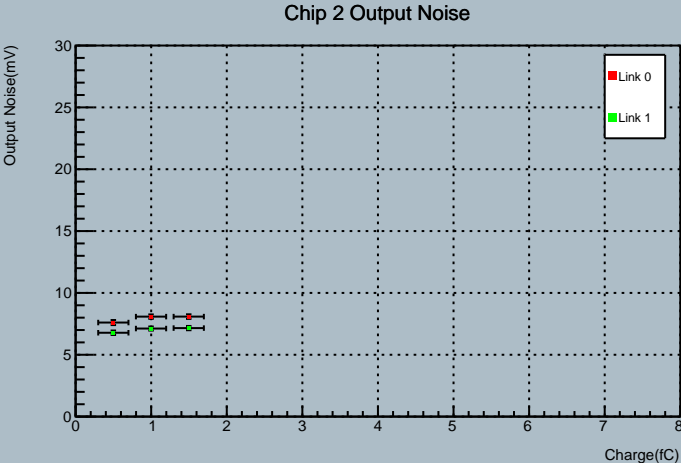
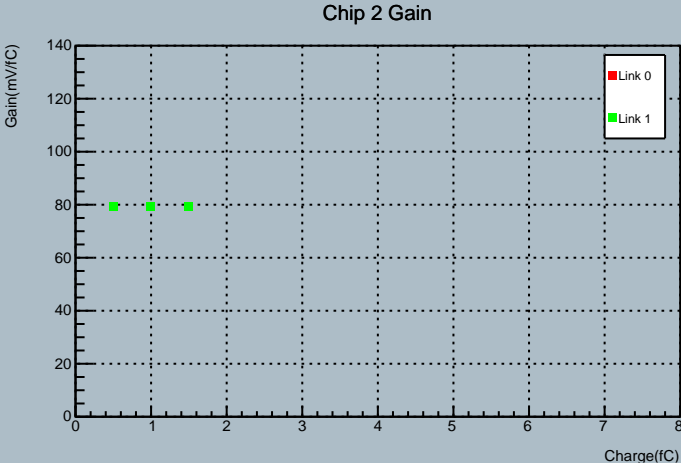
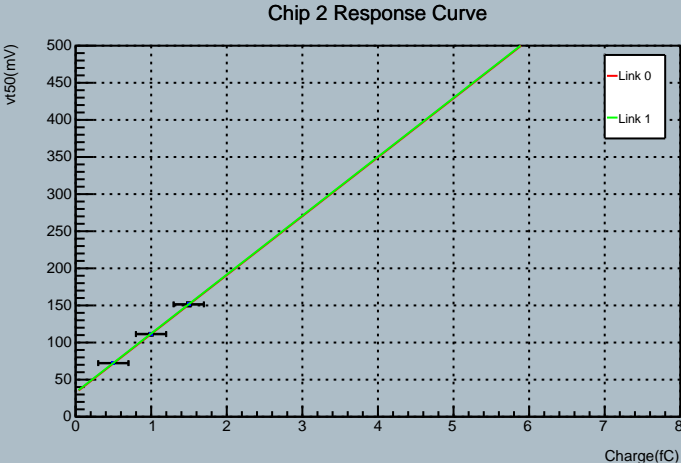


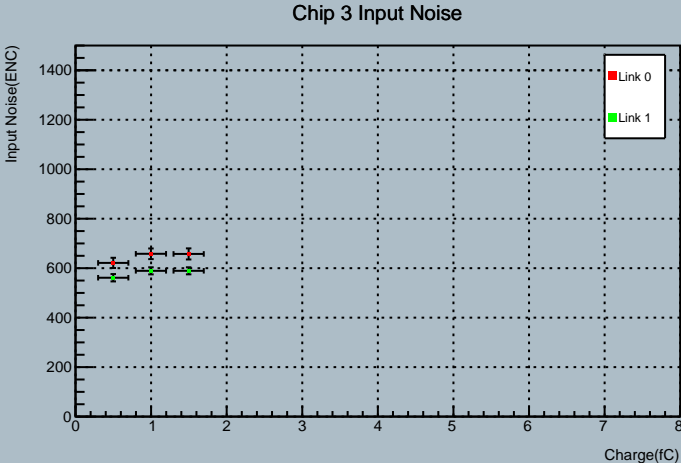
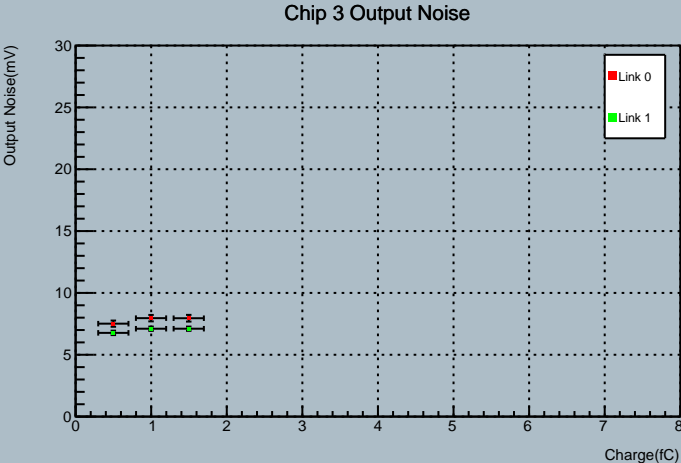
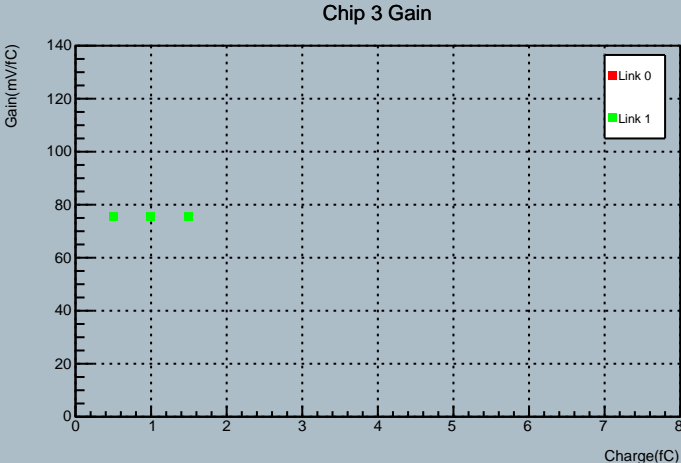
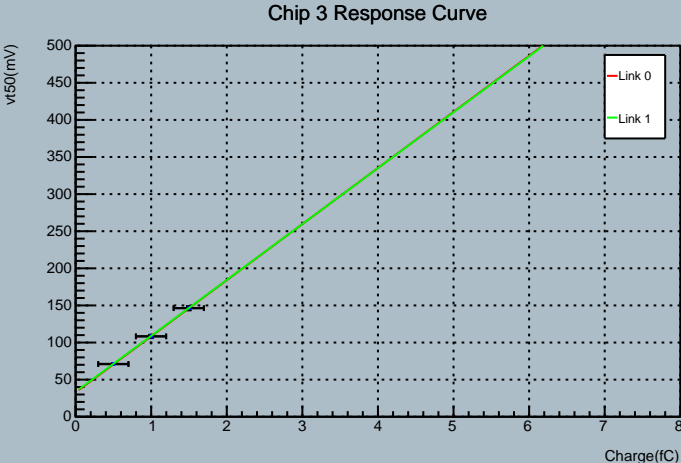
Chip 1 Output Noise



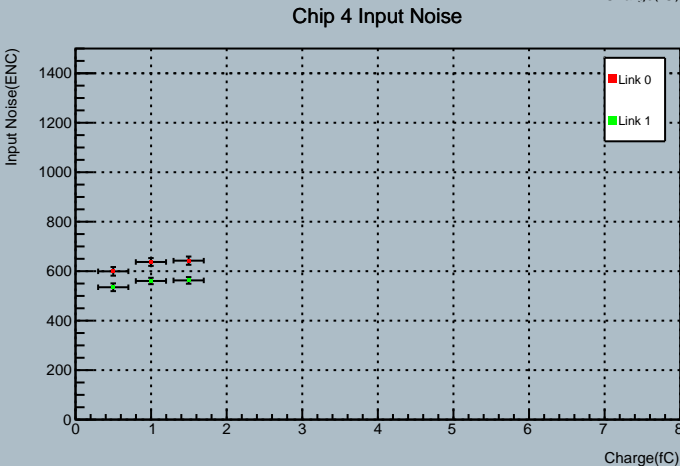
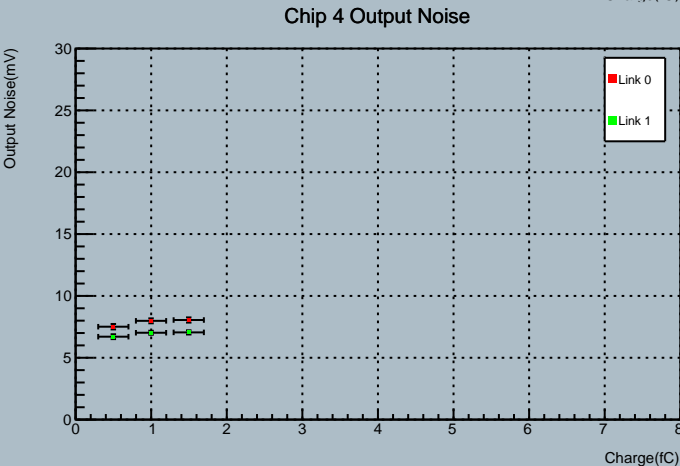
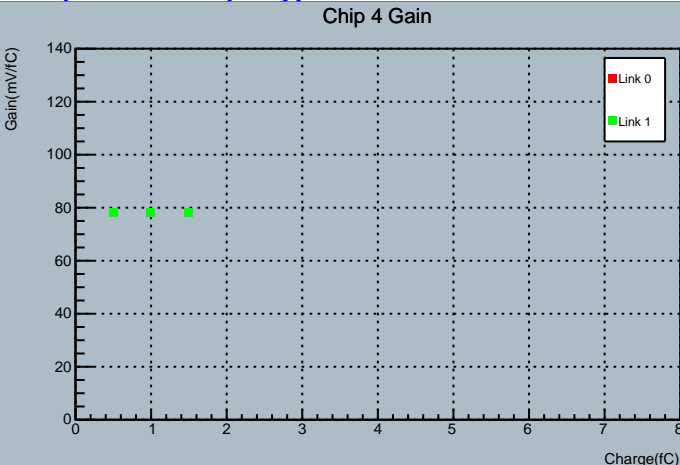
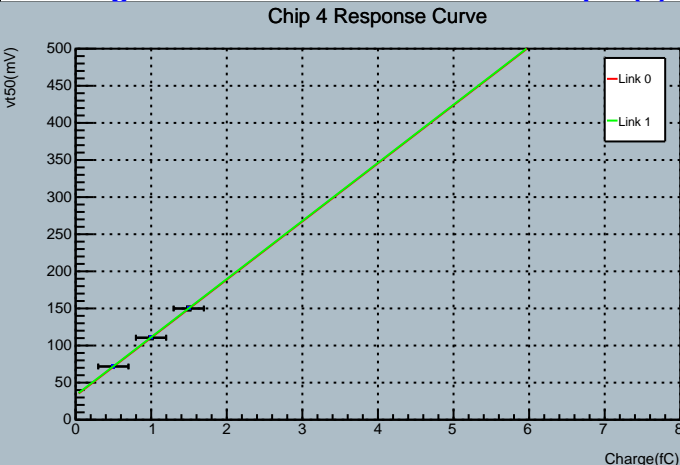
Chip 1 Input Noise

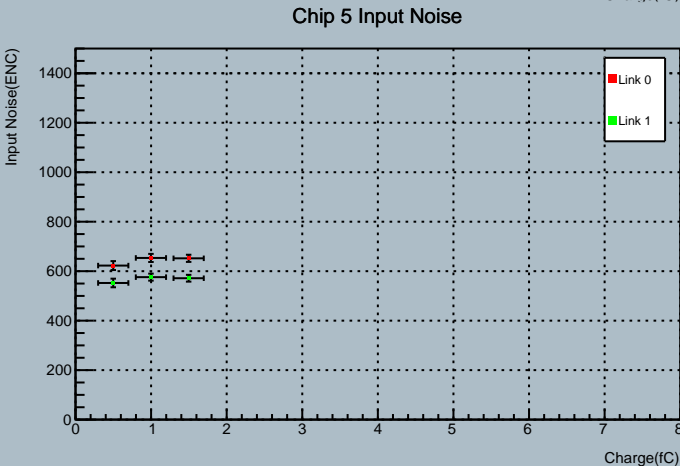
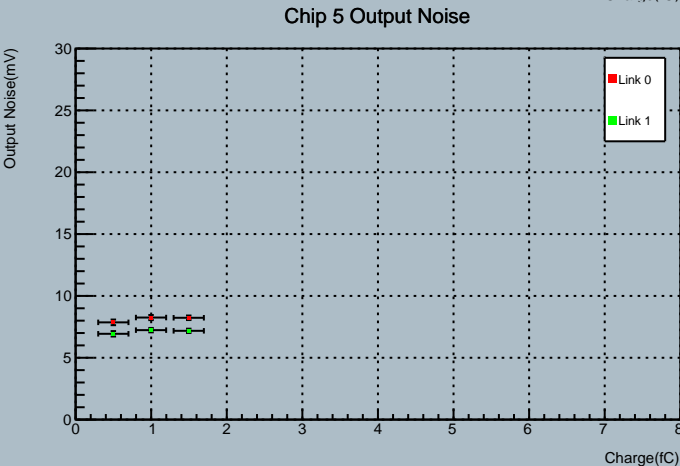
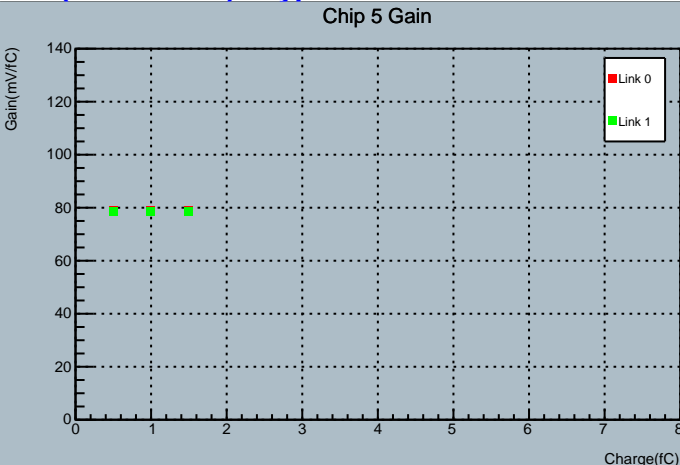
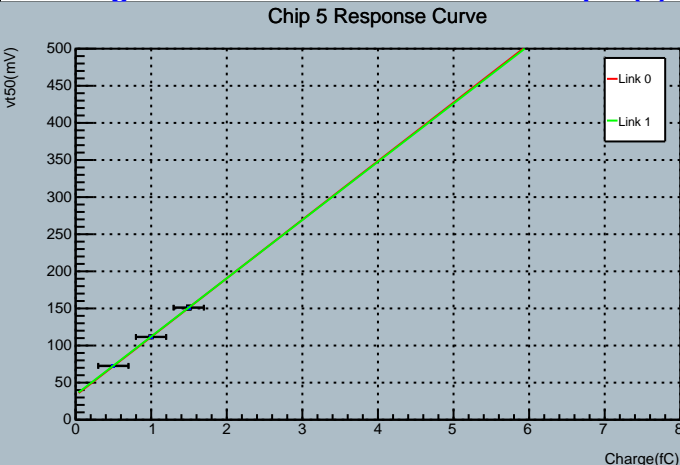


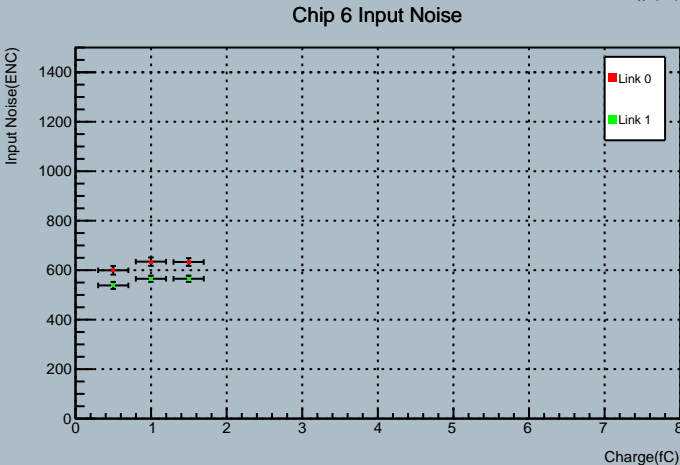
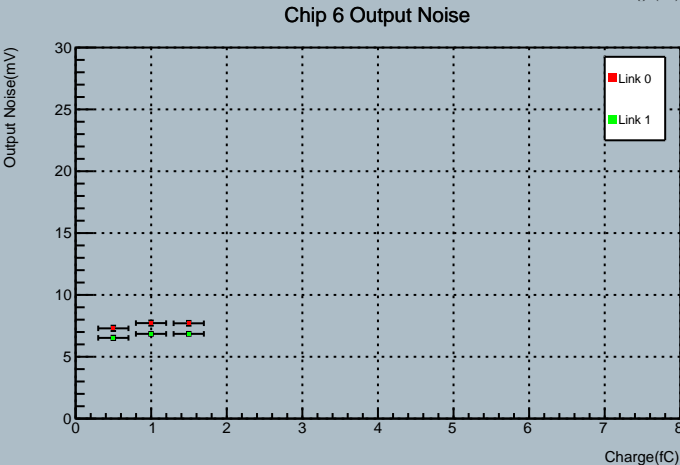
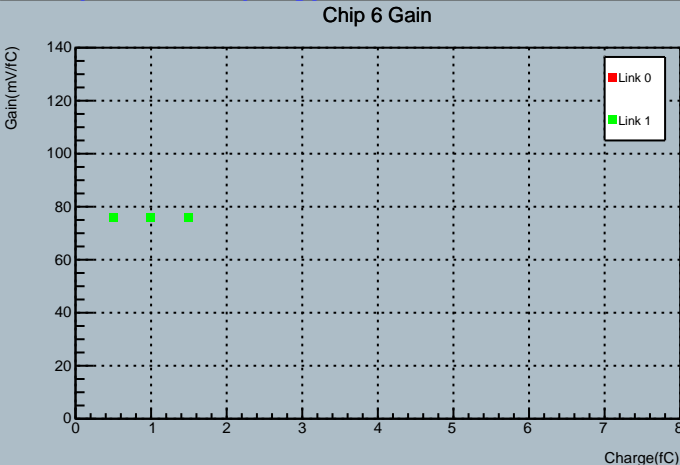
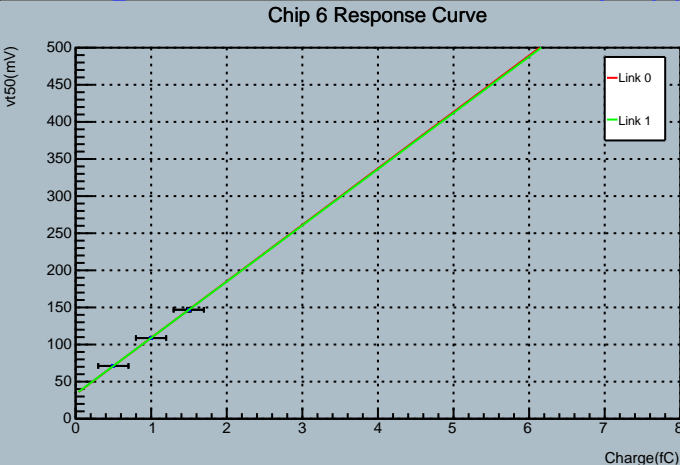




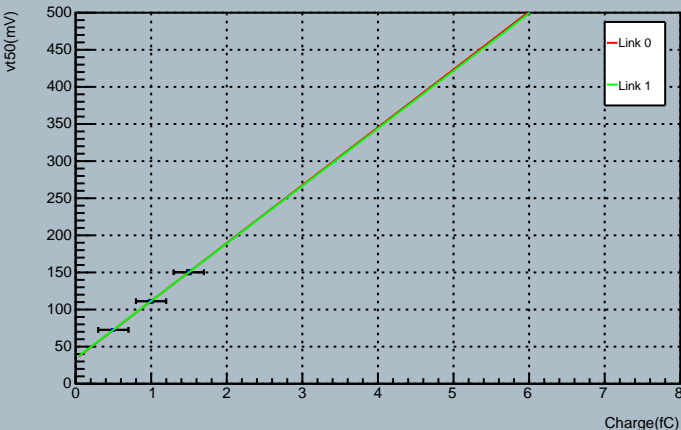




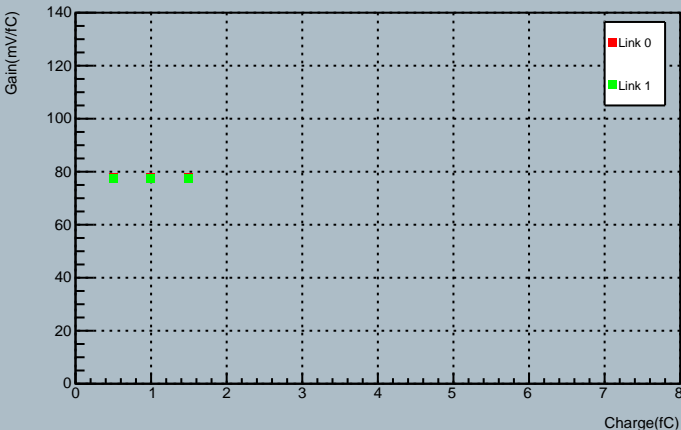




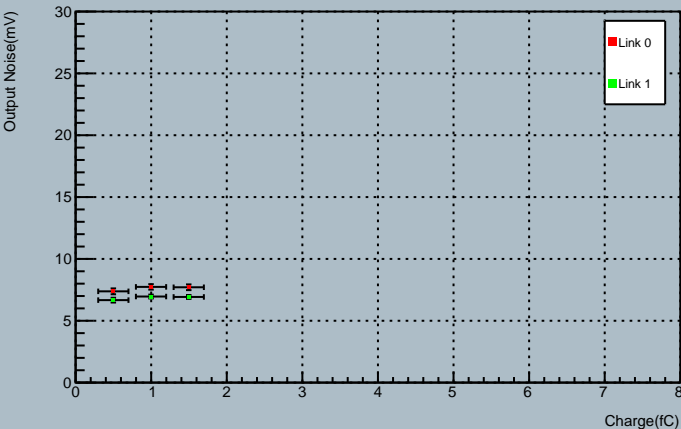
Chip 7 Response Curve



Chip 7 Gain



Chip 7 Output Noise



Chip 7 Input Noise

