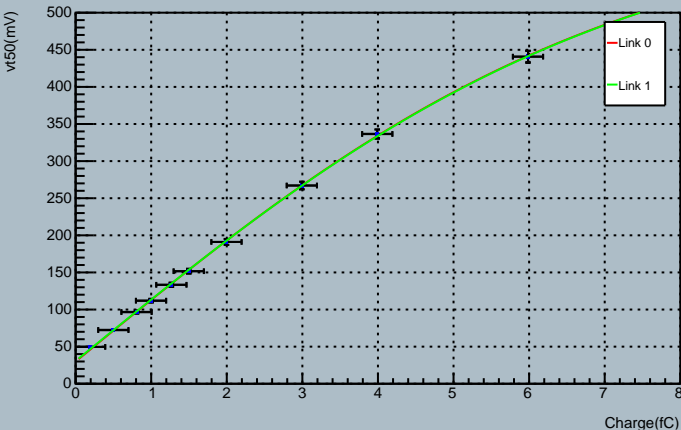
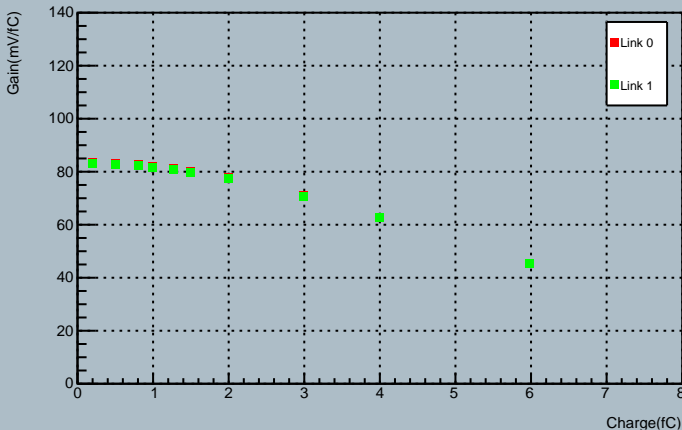


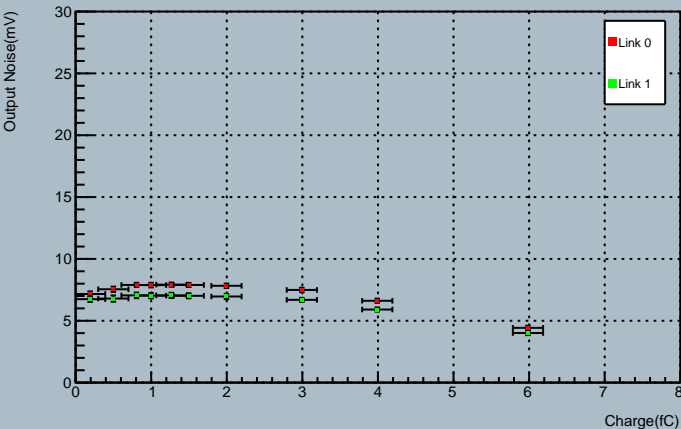
Chip 2 Response Curve



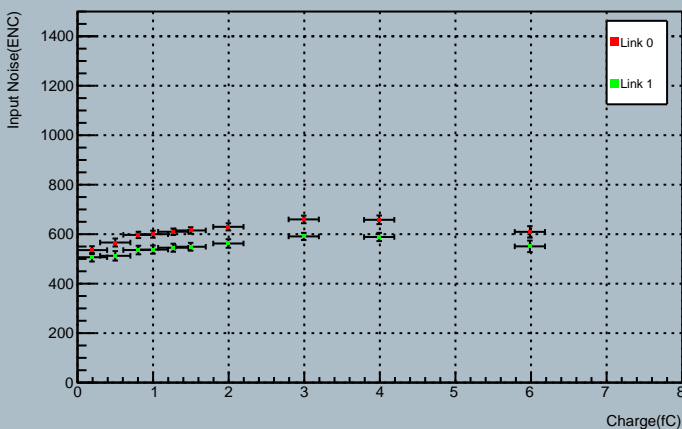
Chip 2 Gain

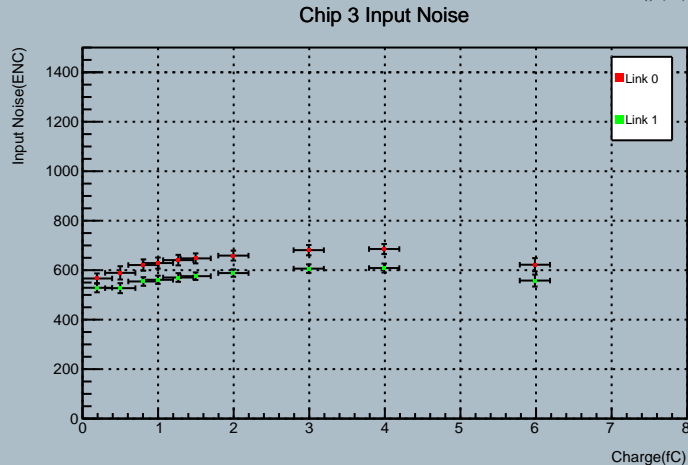
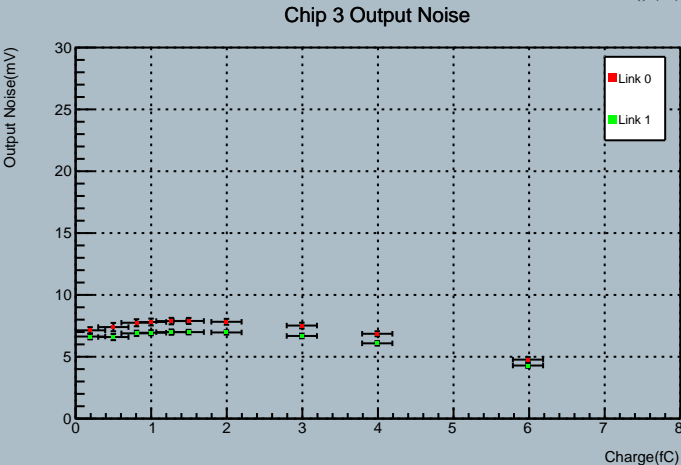
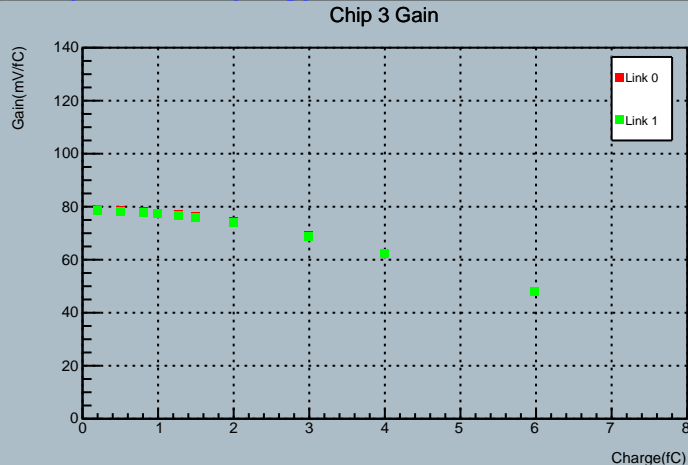
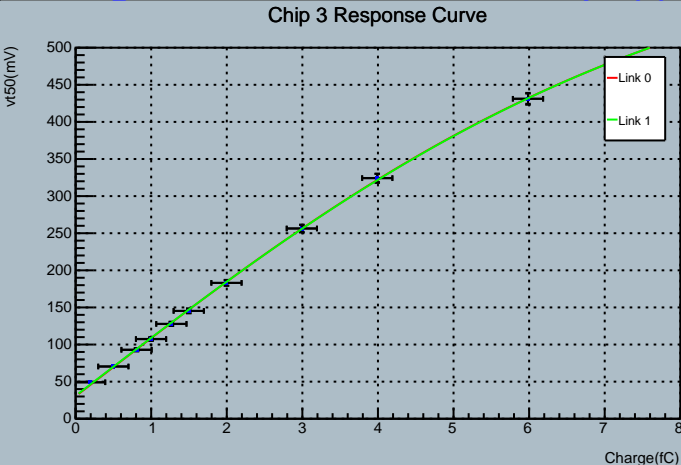


Chip 2 Output Noise



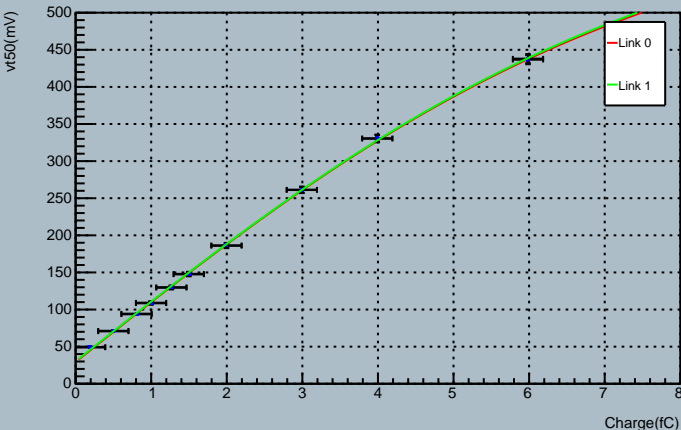
Chip 2 Input Noise



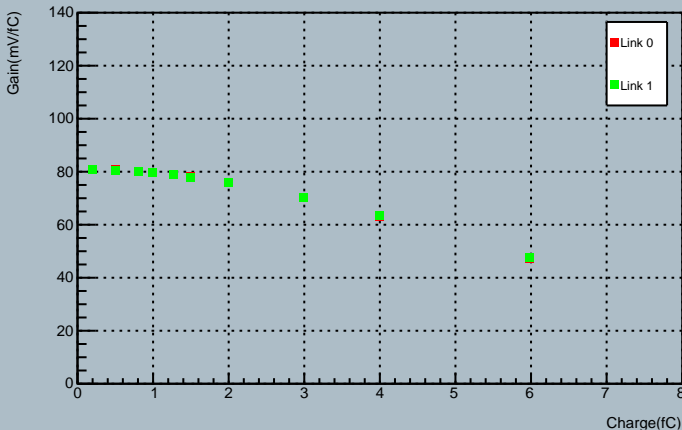




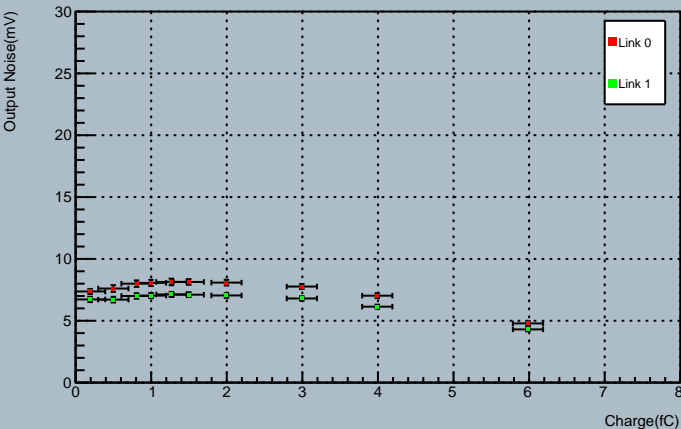
Chip 4 Response Curve



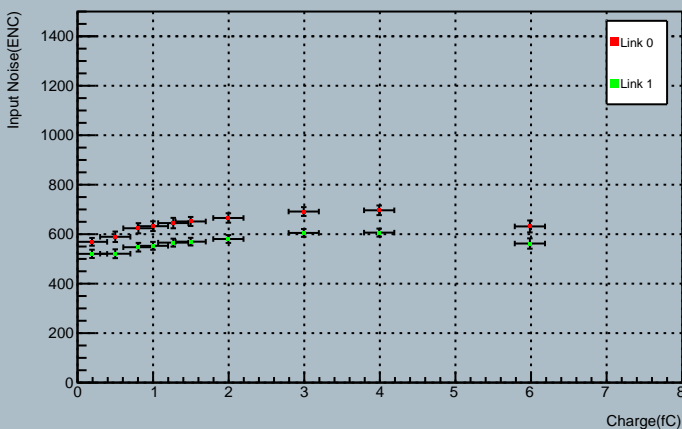
Chip 4 Gain



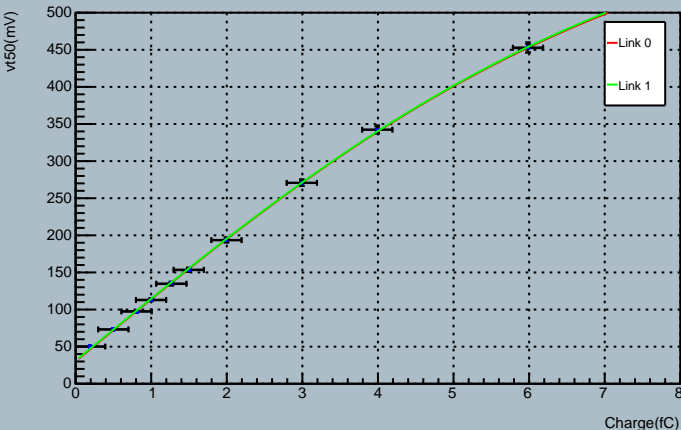
Chip 4 Output Noise



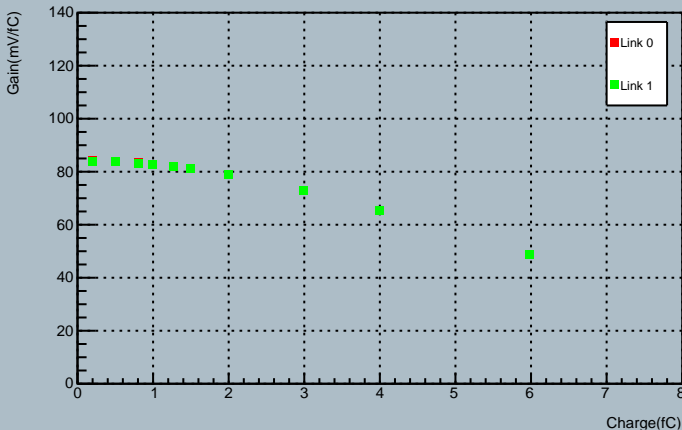
Chip 4 Input Noise



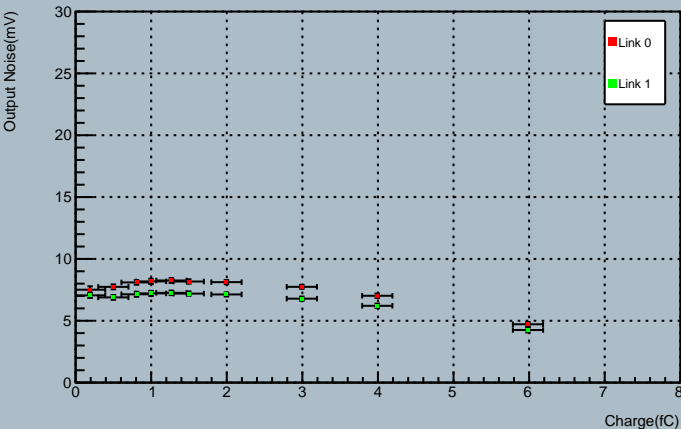
Chip 5 Response Curve



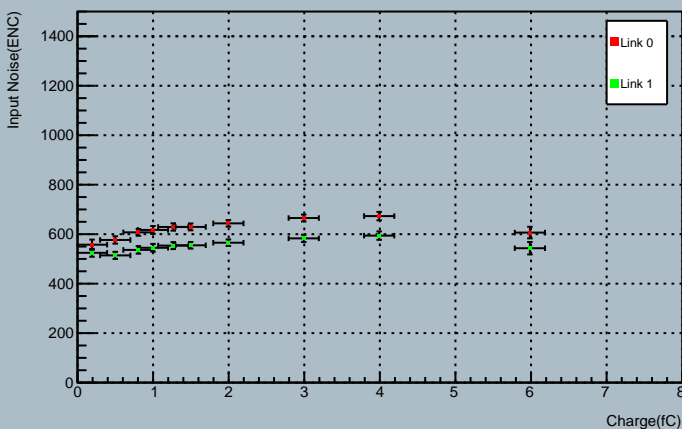
Chip 5 Gain

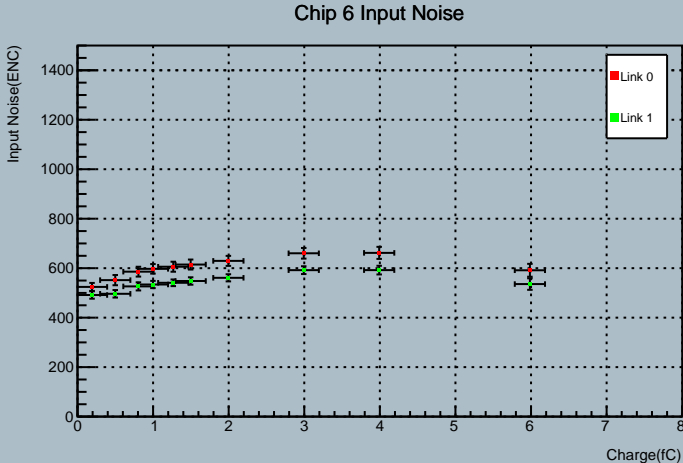
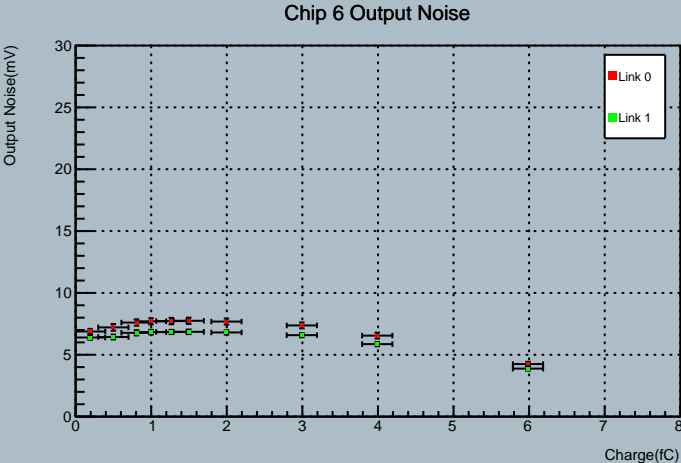
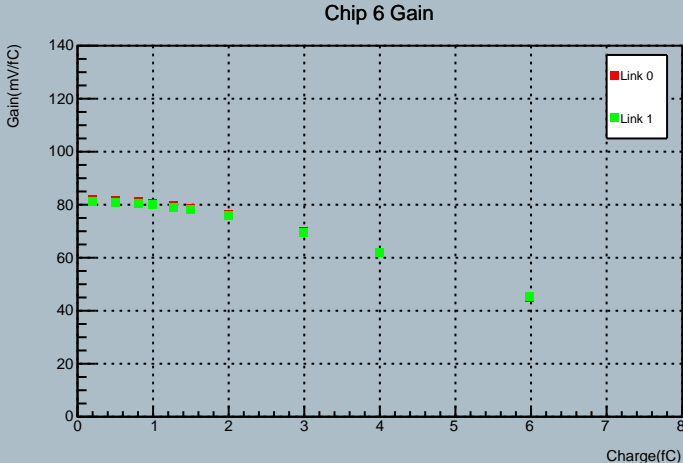
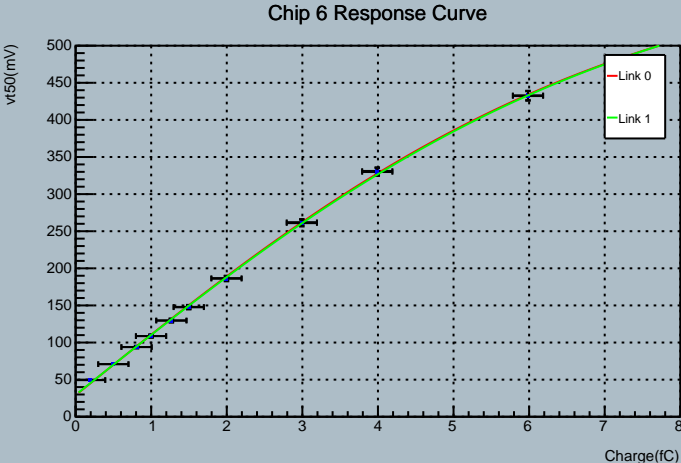


Chip 5 Output Noise

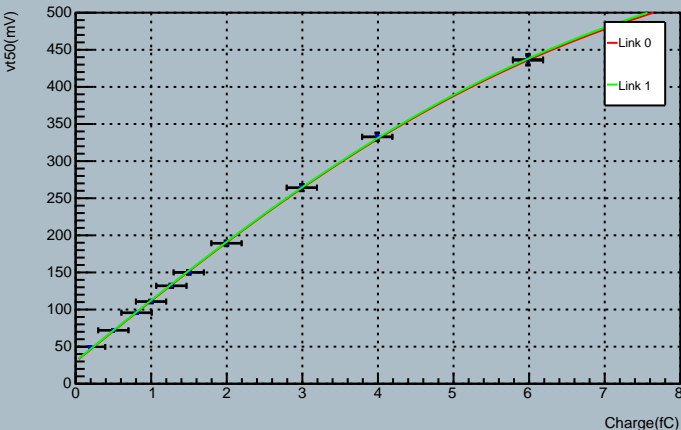


Chip 5 Input Noise

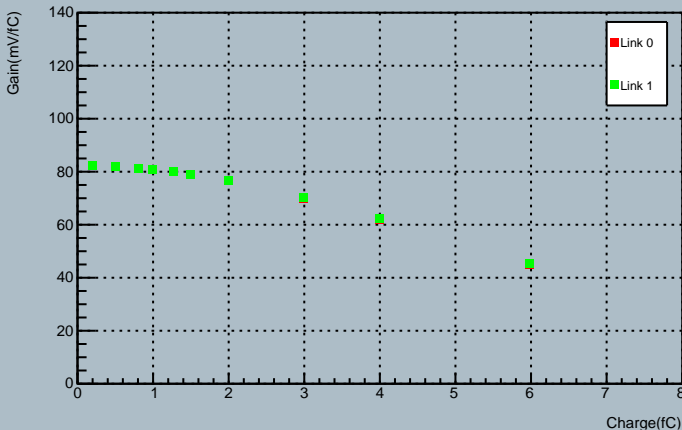




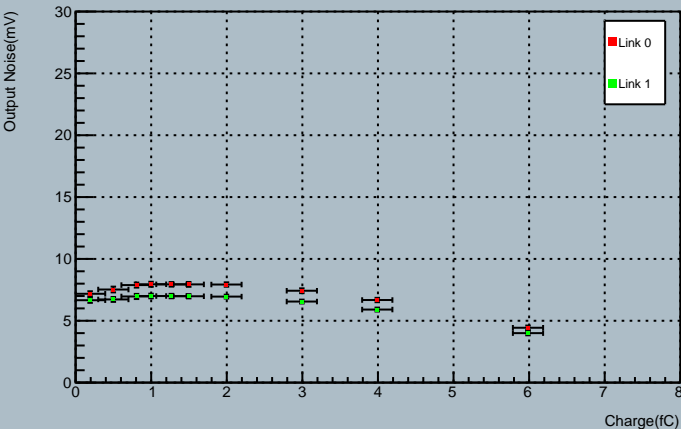
Chip 7 Response Curve



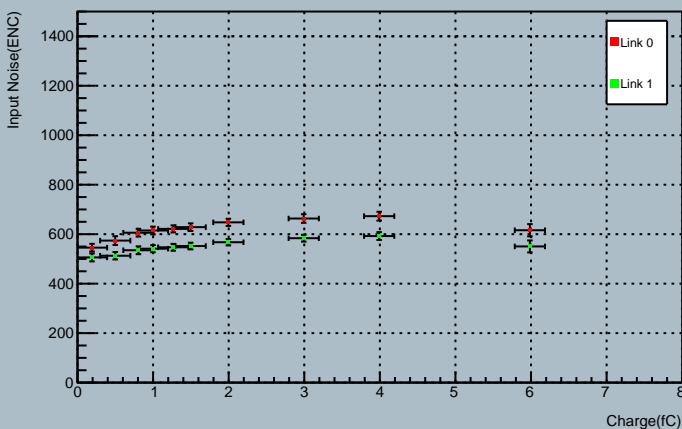
Chip 7 Gain



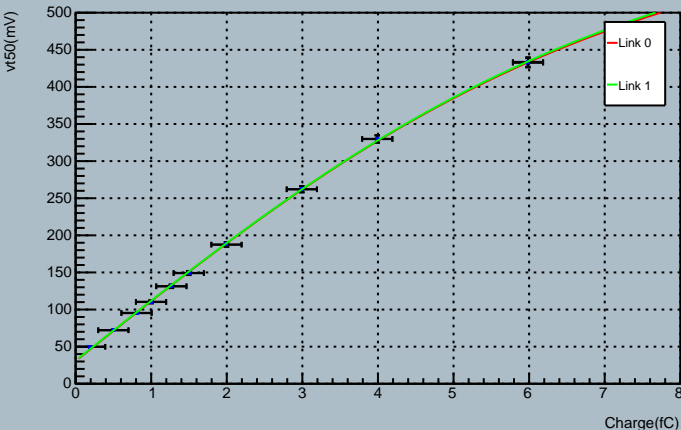
Chip 7 Output Noise



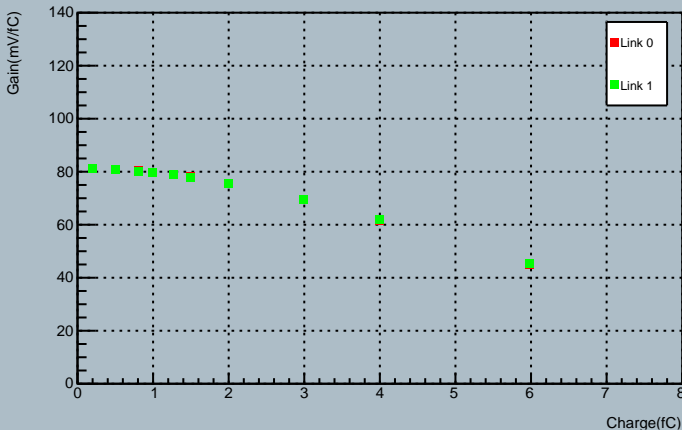
Chip 7 Input Noise



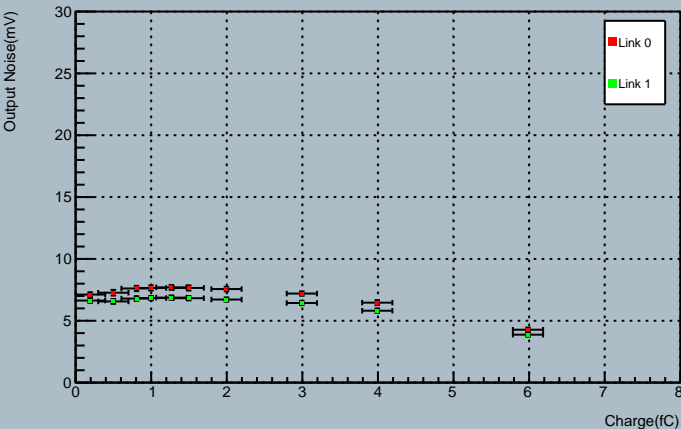
Chip 8 Response Curve



Chip 8 Gain



Chip 8 Output Noise



Chip 8 Input Noise

