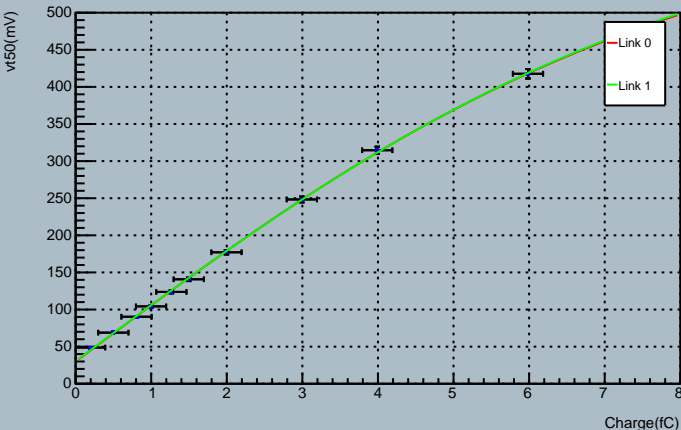
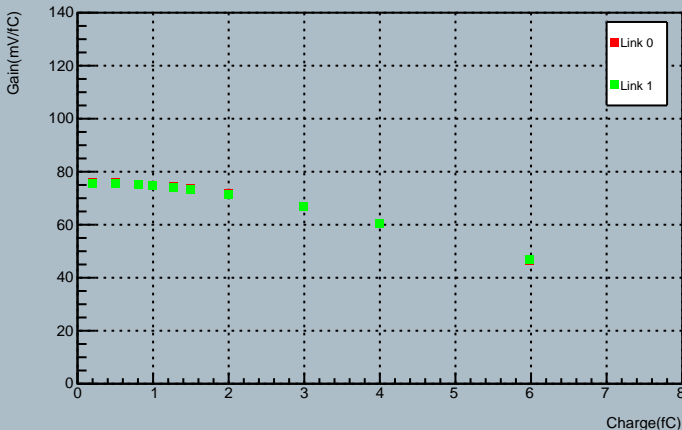


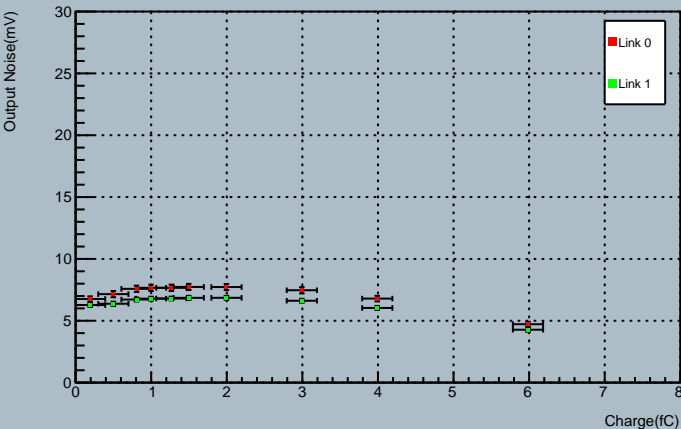
Chip 0 Response Curve



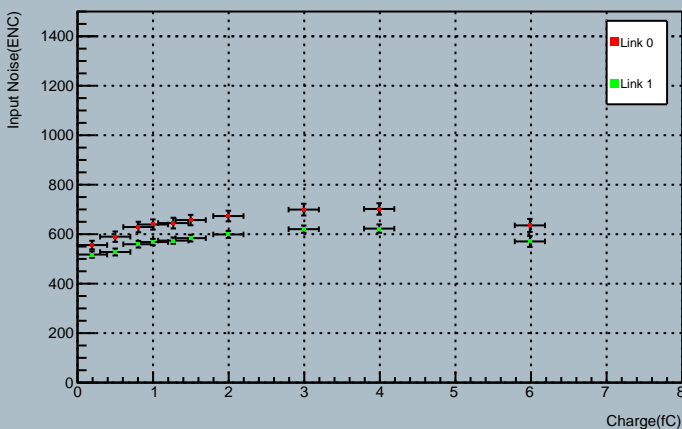
Chip 0 Gain

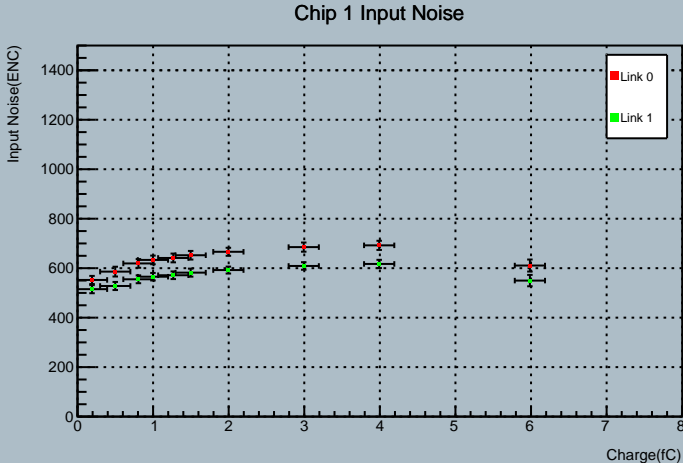
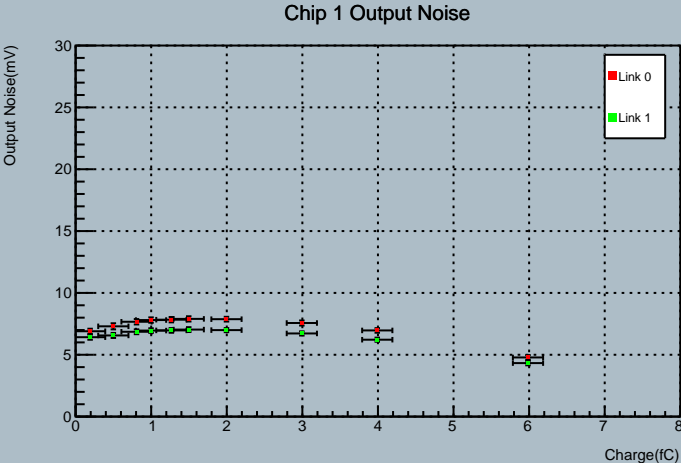
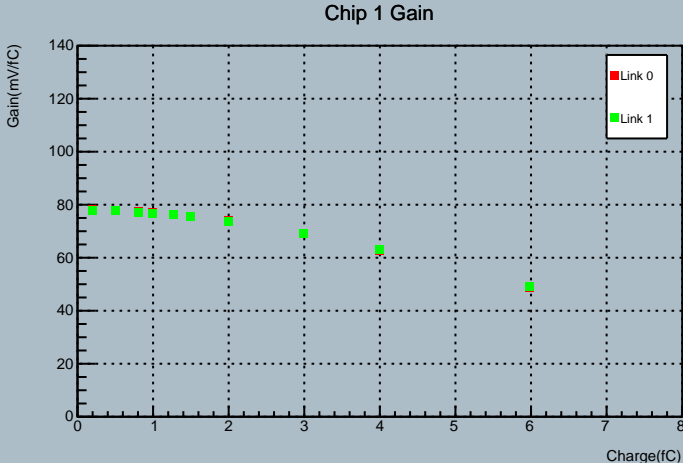
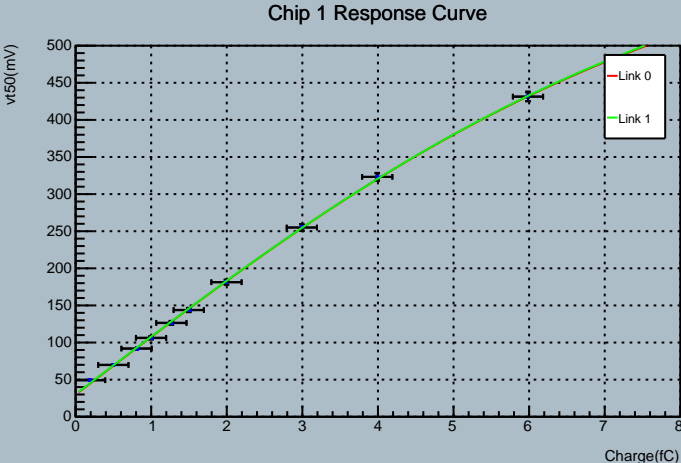


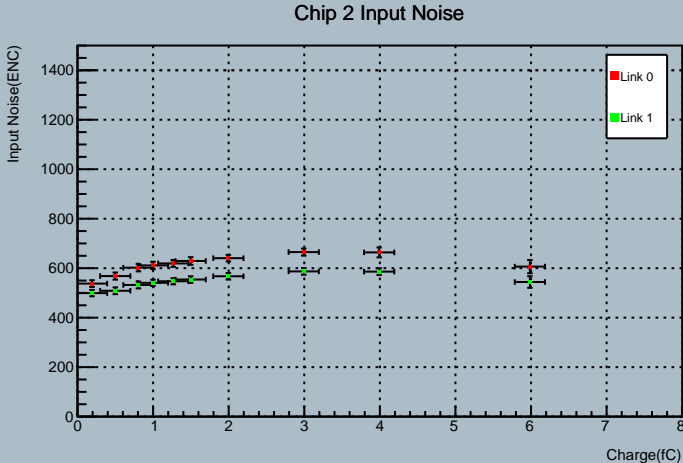
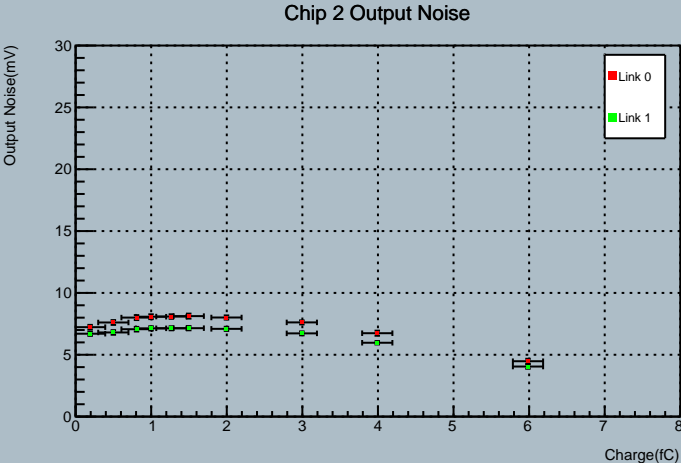
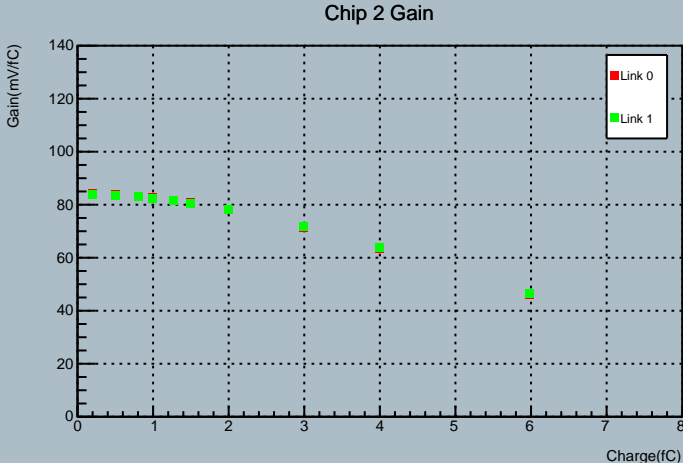
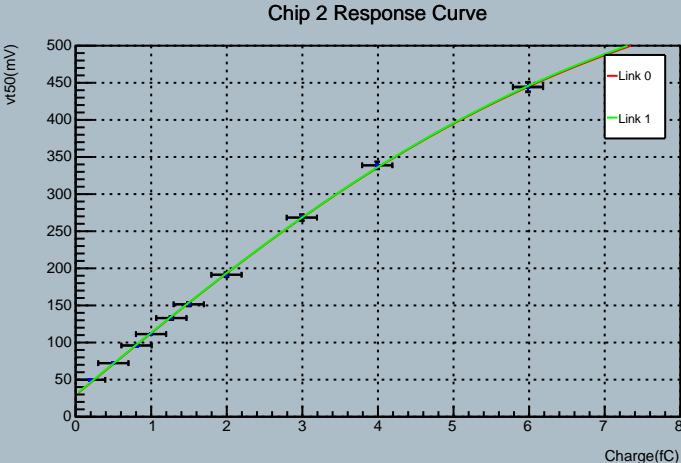
Chip 0 Output Noise



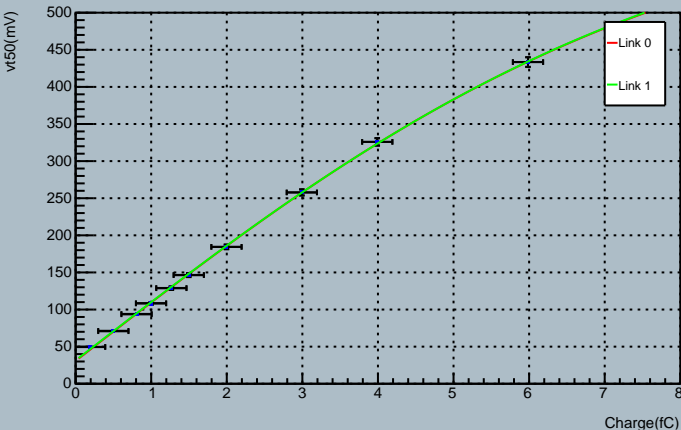
Chip 0 Input Noise



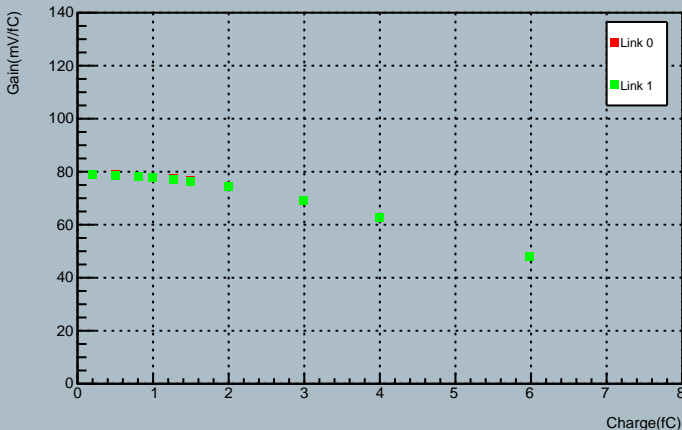




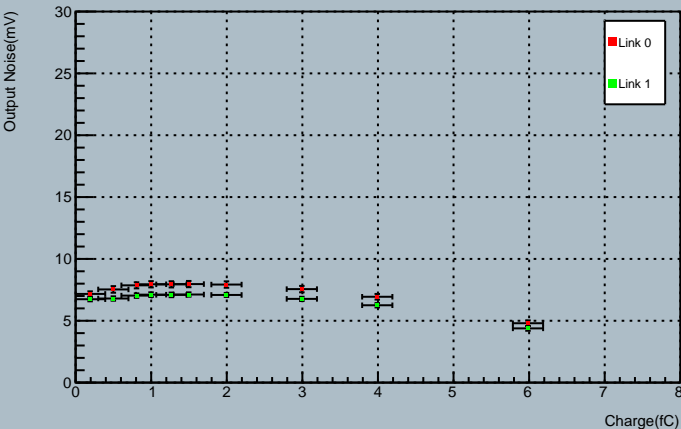
Chip 3 Response Curve



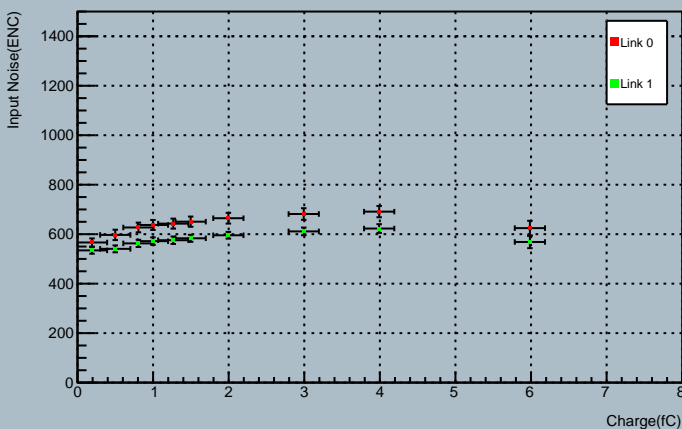
Chip 3 Gain



Chip 3 Output Noise

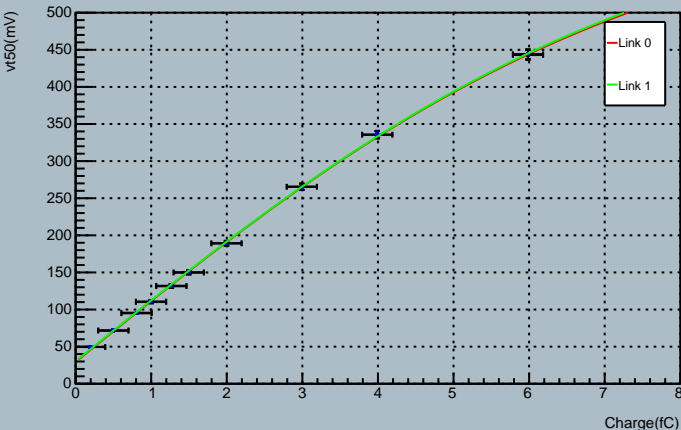


Chip 3 Input Noise

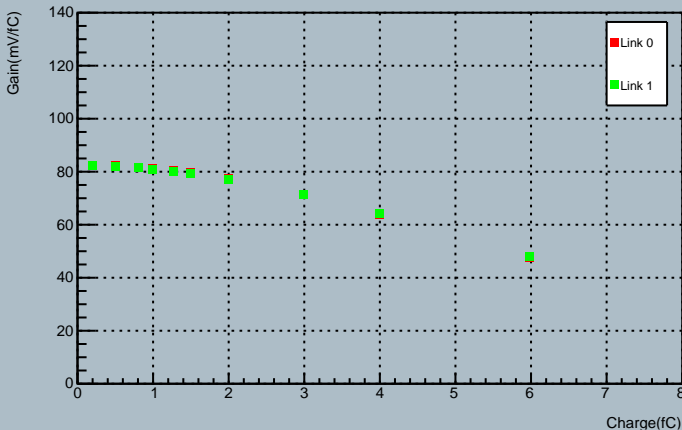




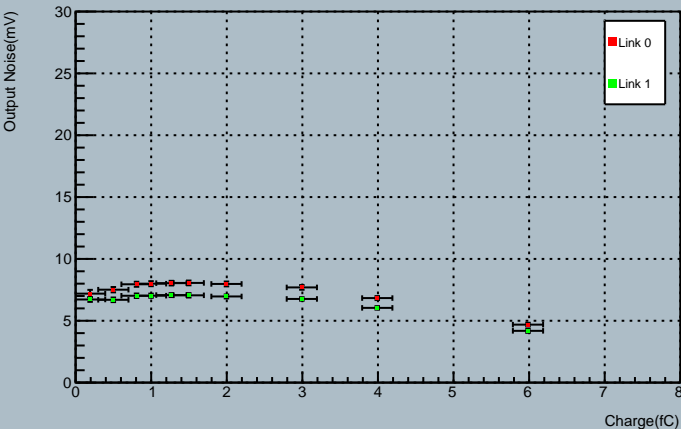
Chip 4 Response Curve



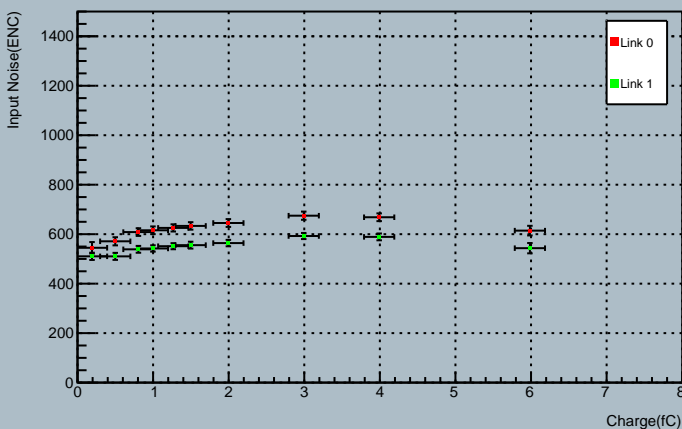
Chip 4 Gain

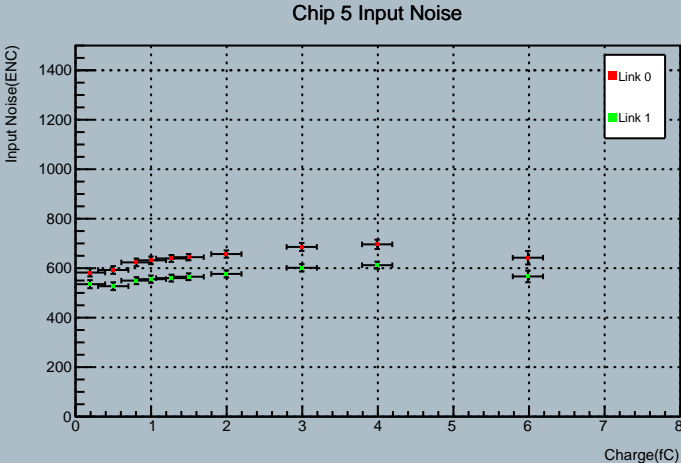
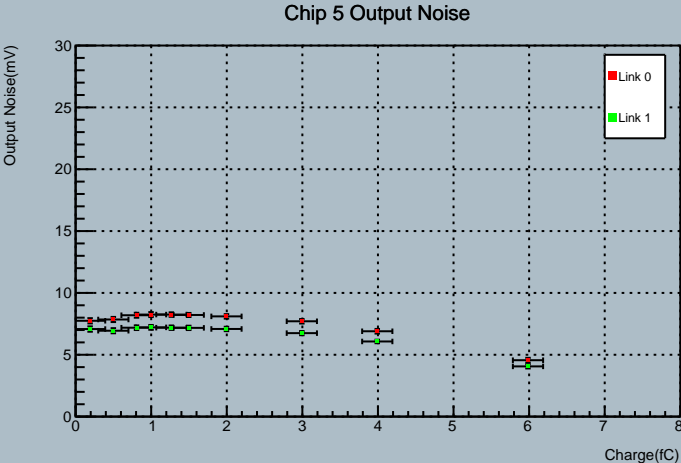
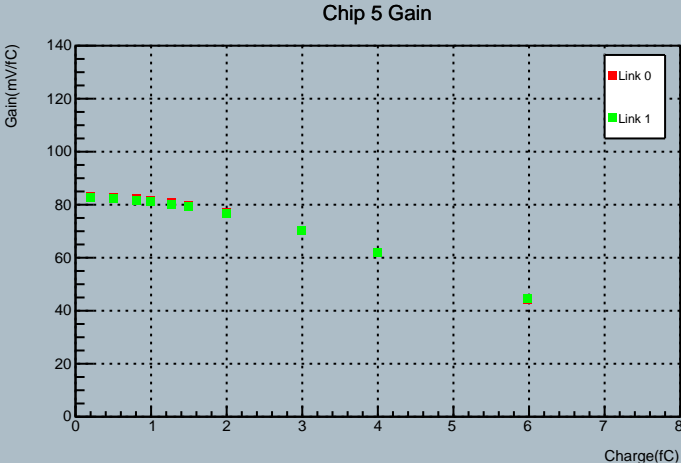
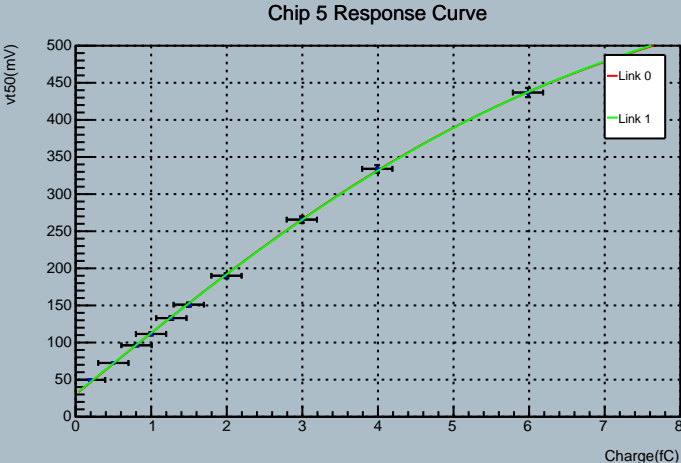


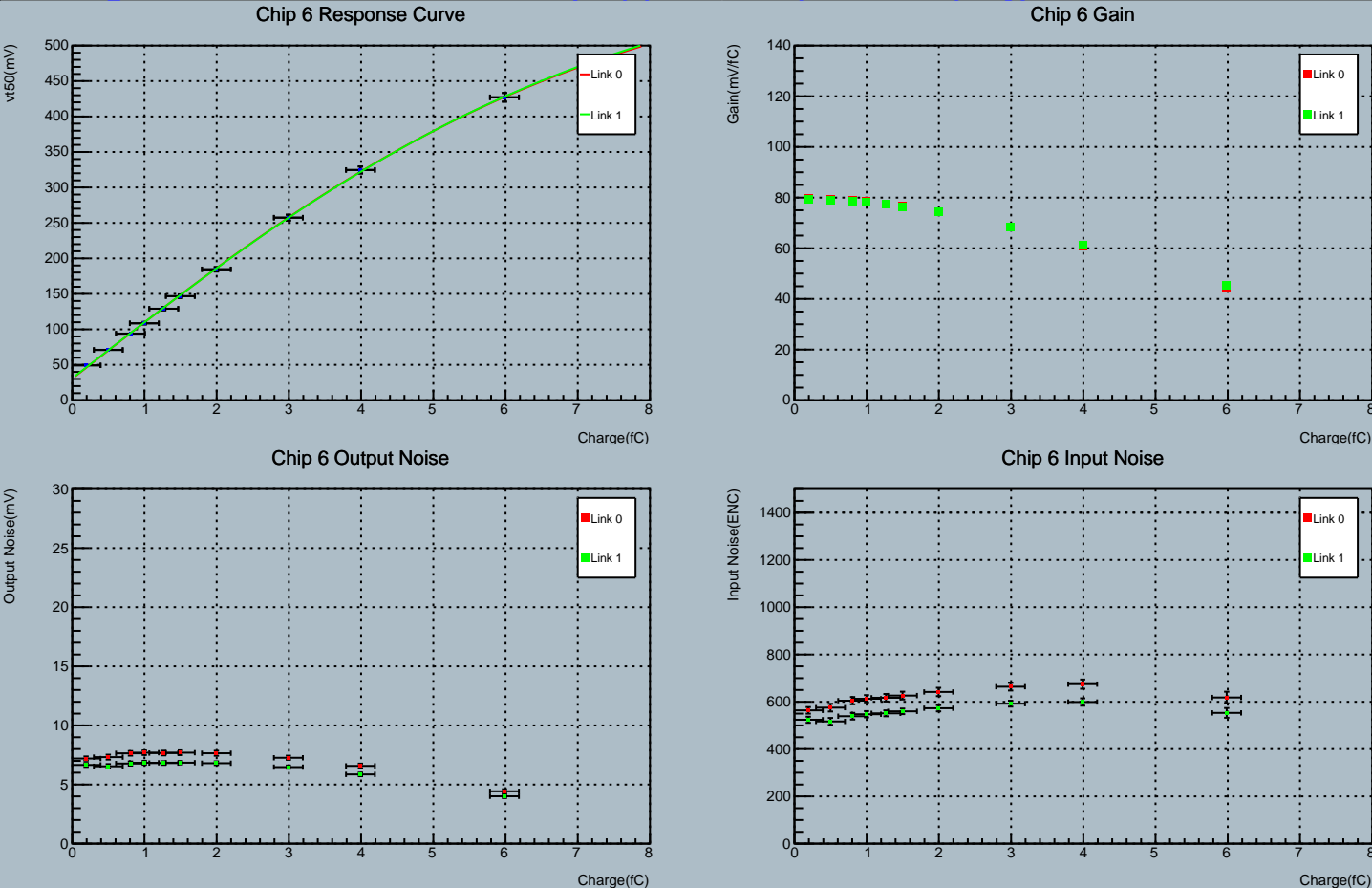
Chip 4 Output Noise



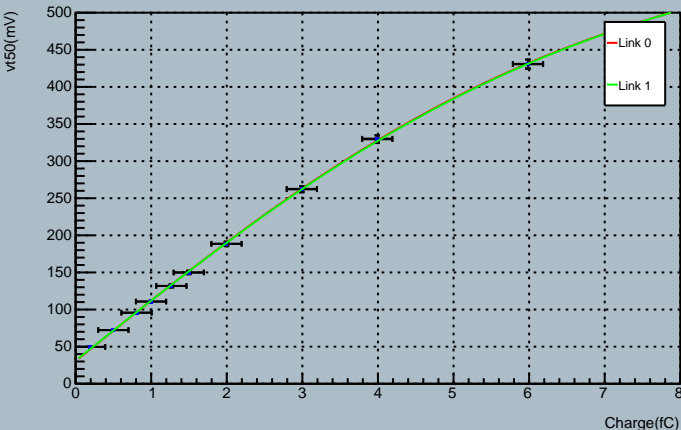
Chip 4 Input Noise



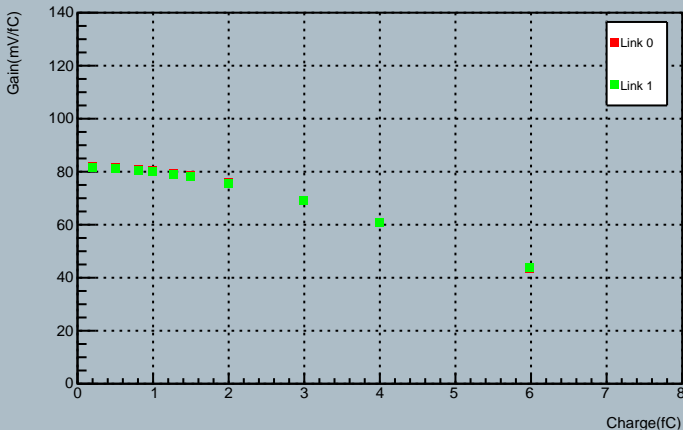




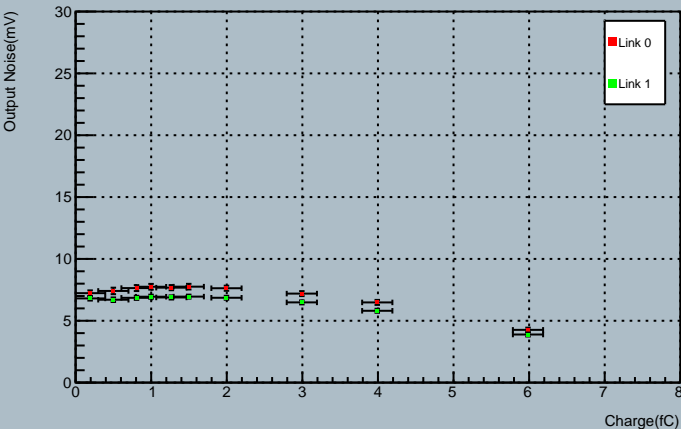
Chip 7 Response Curve



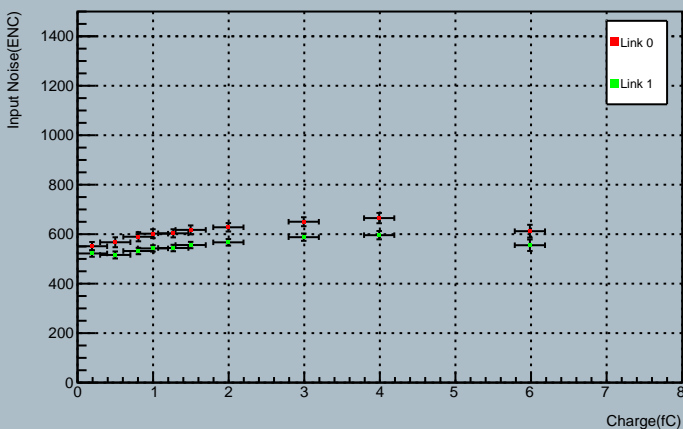
Chip 7 Gain

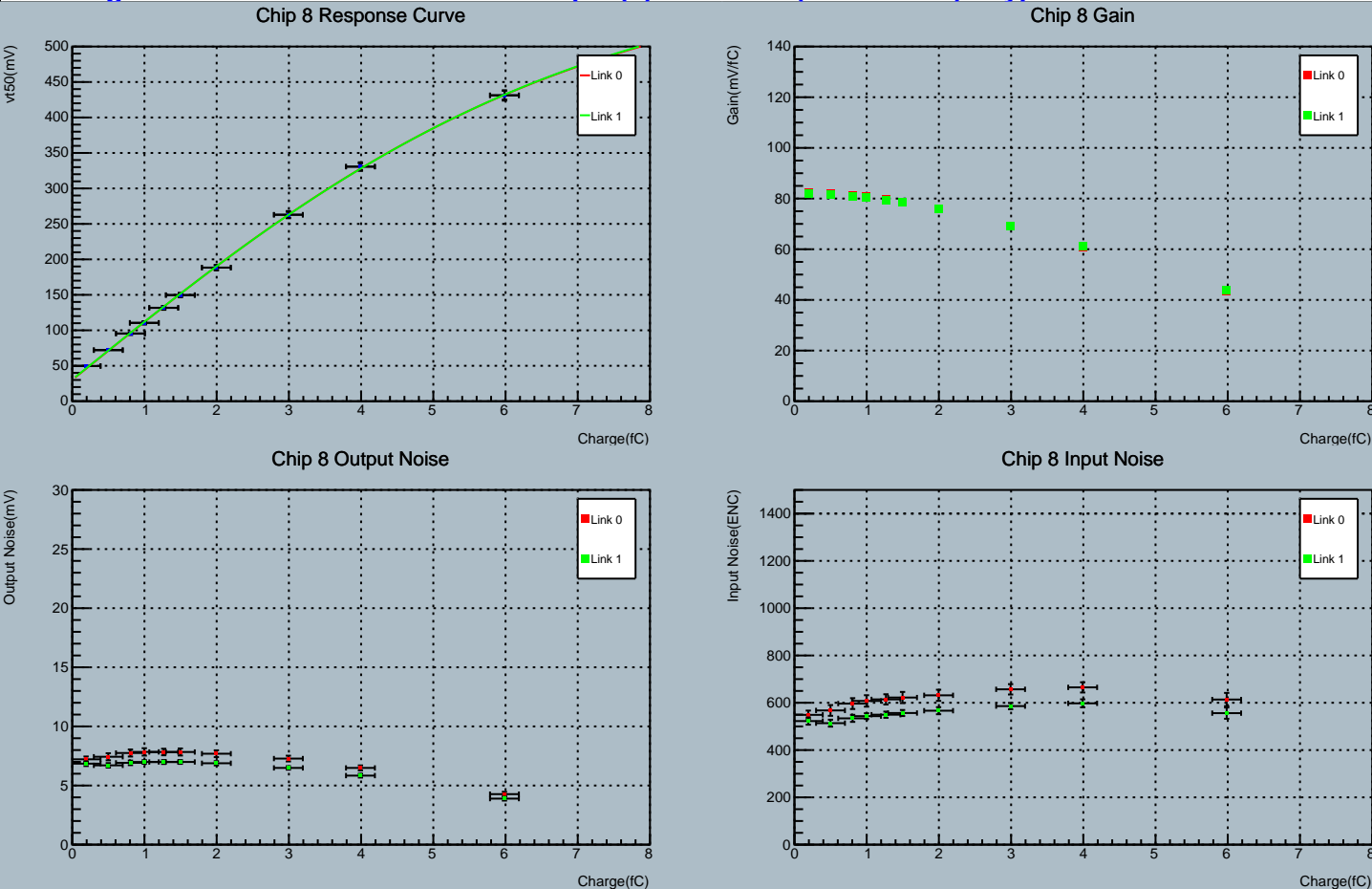


Chip 7 Output Noise

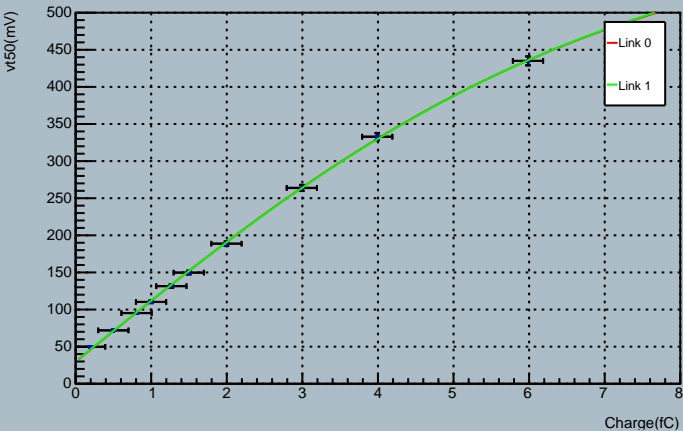


Chip 7 Input Noise

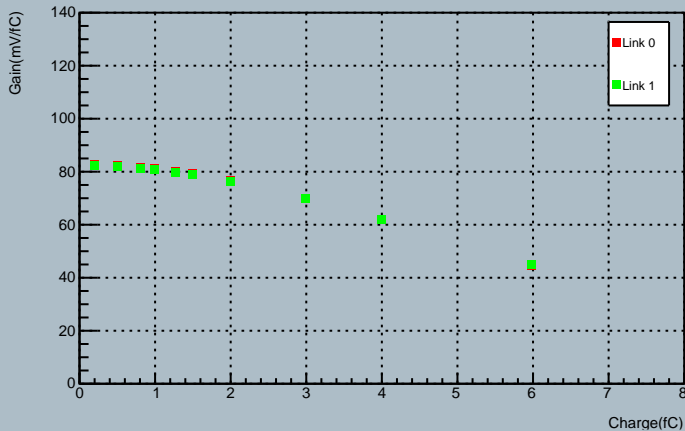




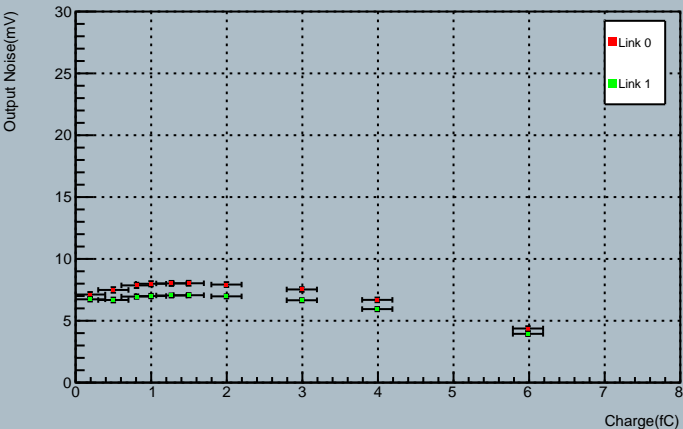
Chip 9 Response Curve



Chip 9 Gain



Chip 9 Output Noise



Chip 9 Input Noise

