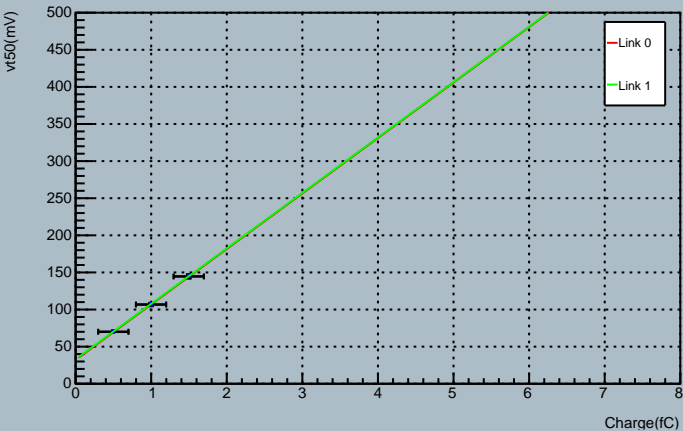
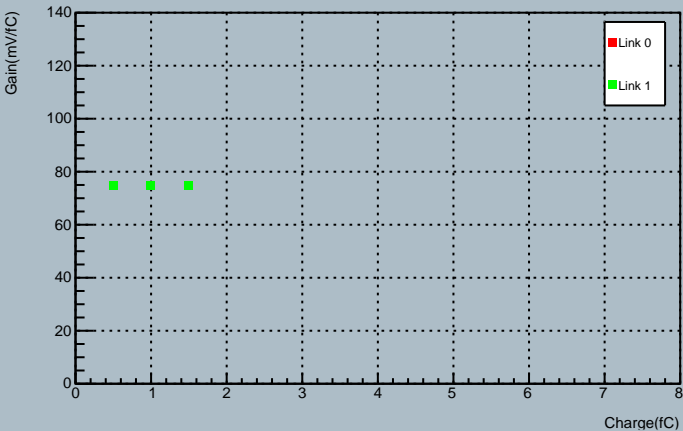


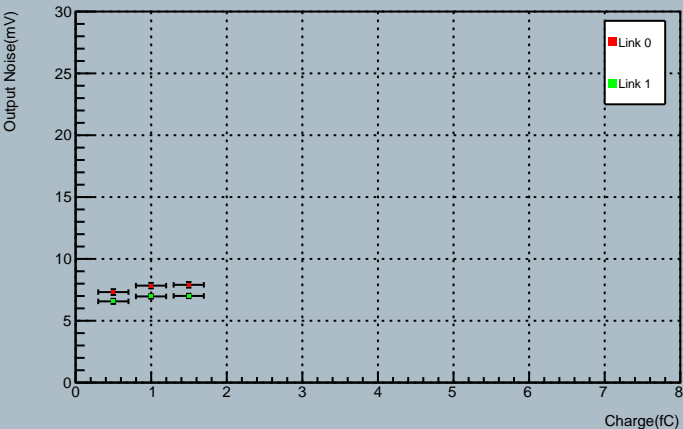
Chip 1 Response Curve



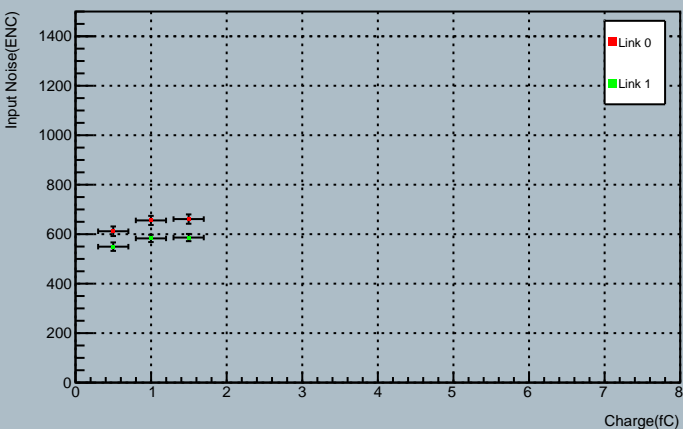
Chip 1 Gain

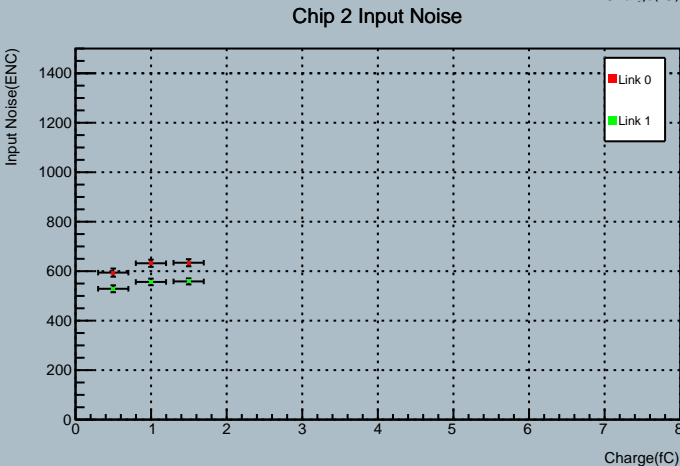
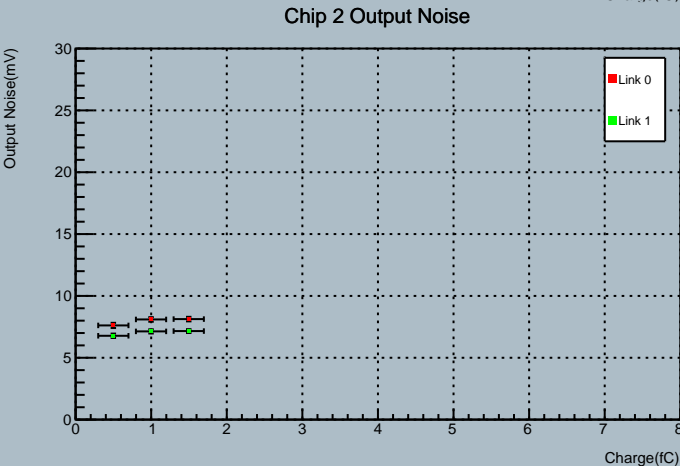
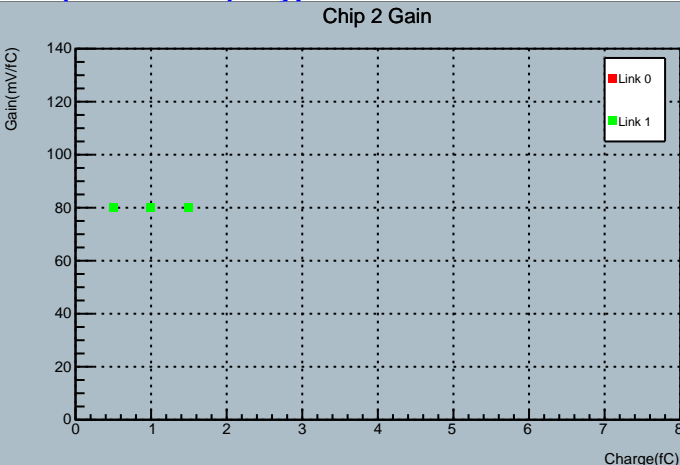
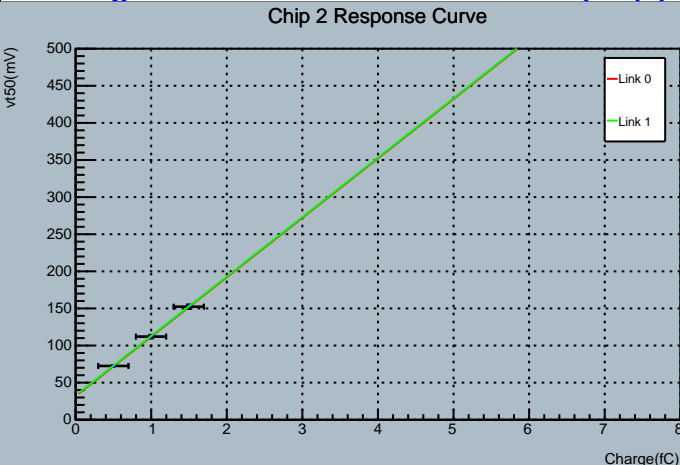


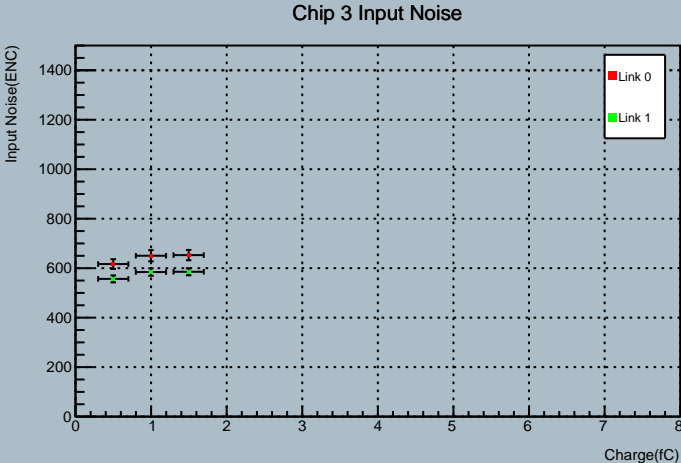
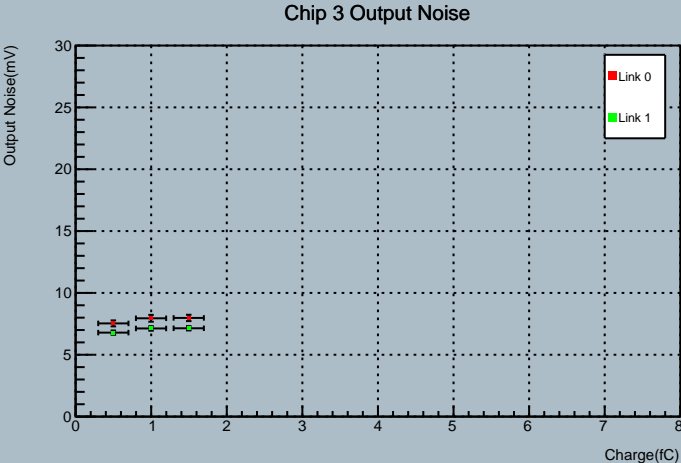
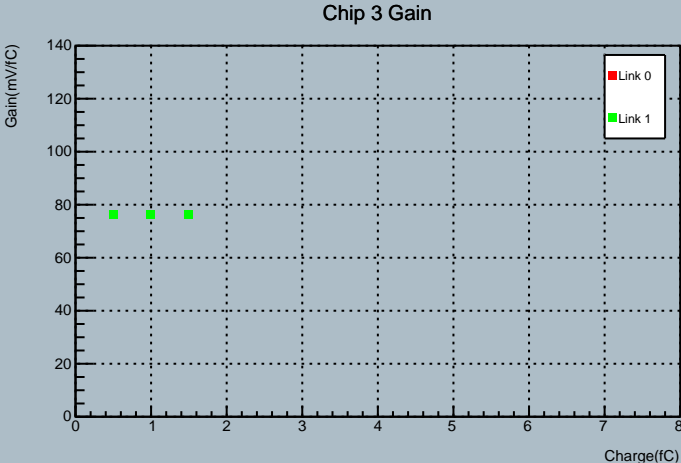
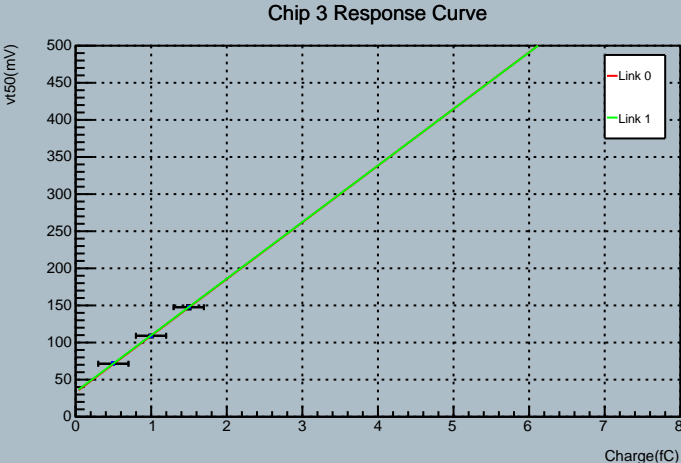
Chip 1 Output Noise



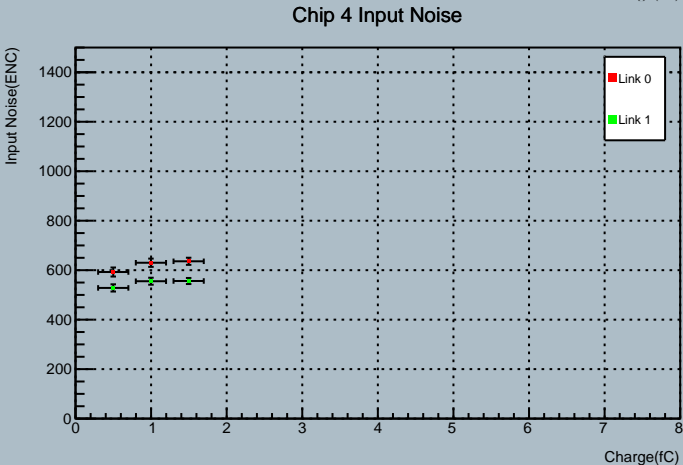
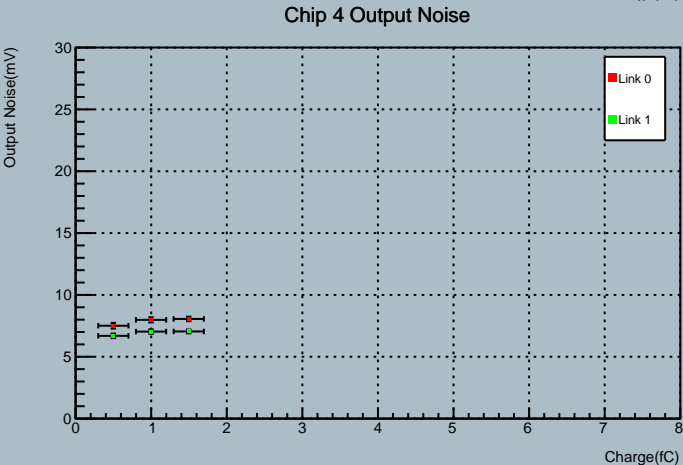
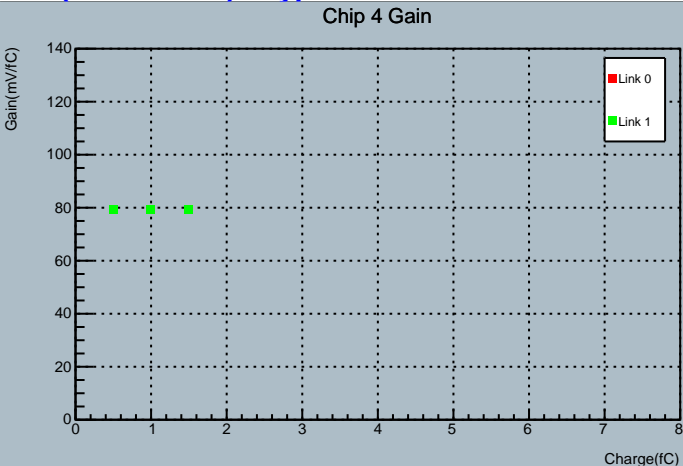
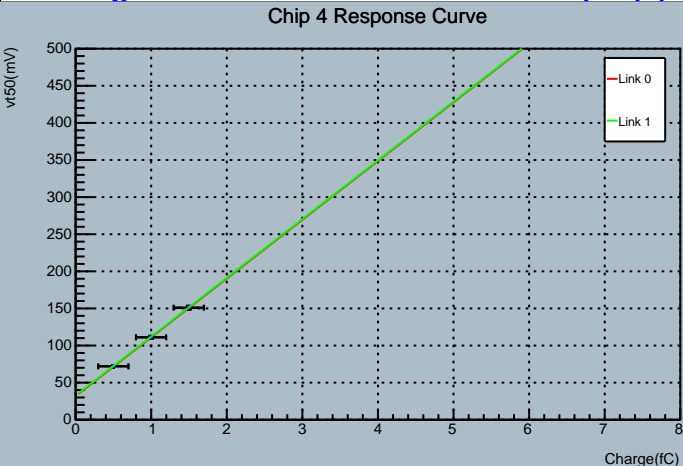
Chip 1 Input Noise

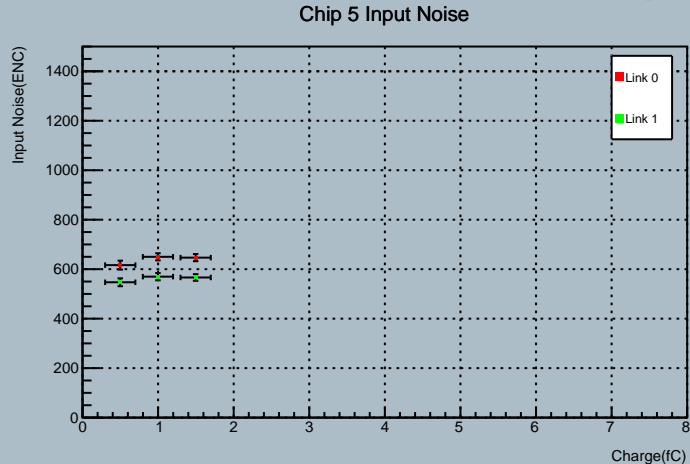
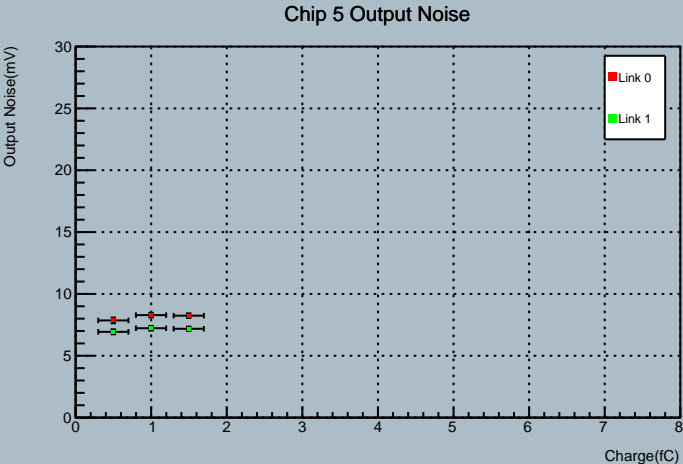
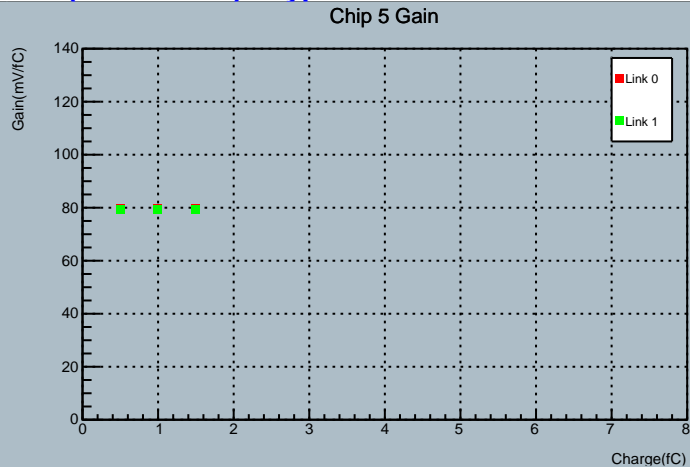
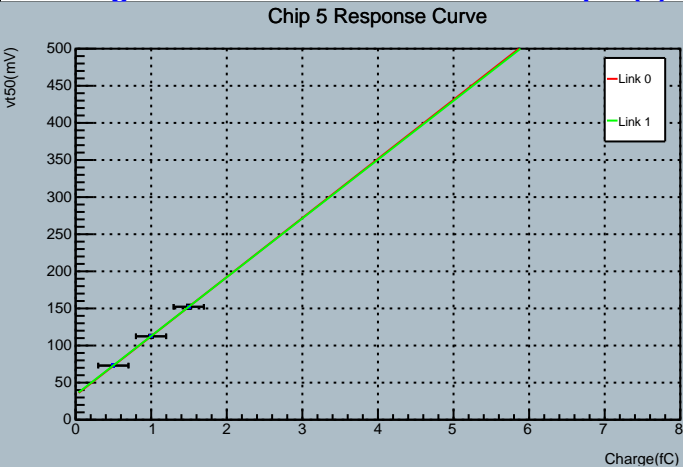




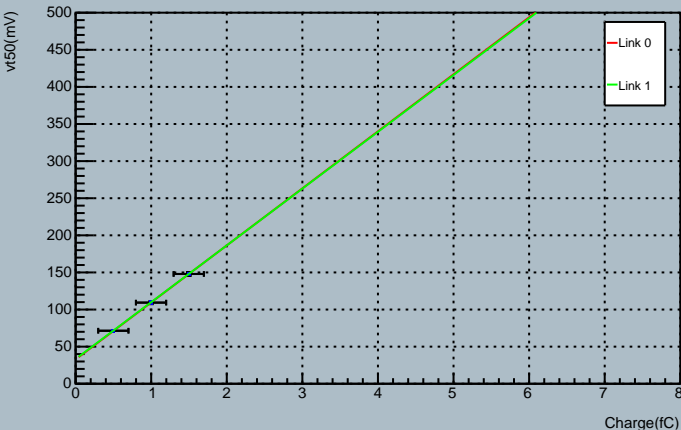




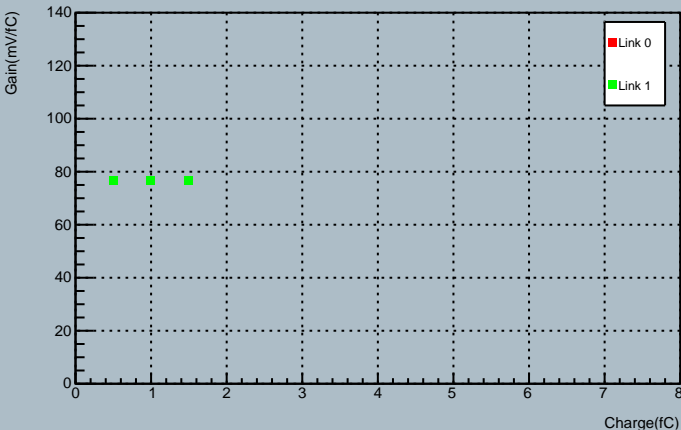




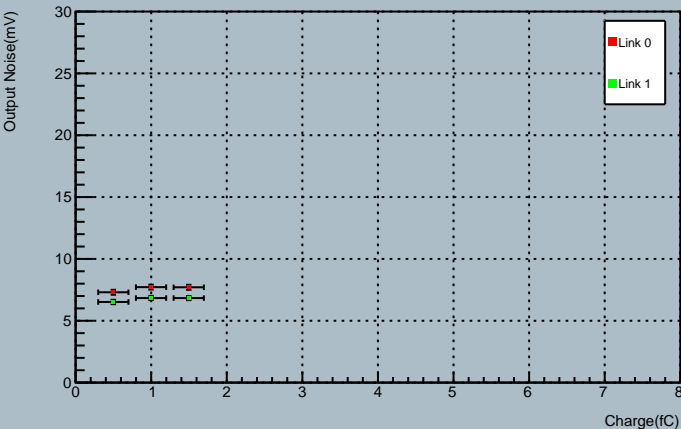
Chip 6 Response Curve



Chip 6 Gain



Chip 6 Output Noise



Chip 6 Input Noise

