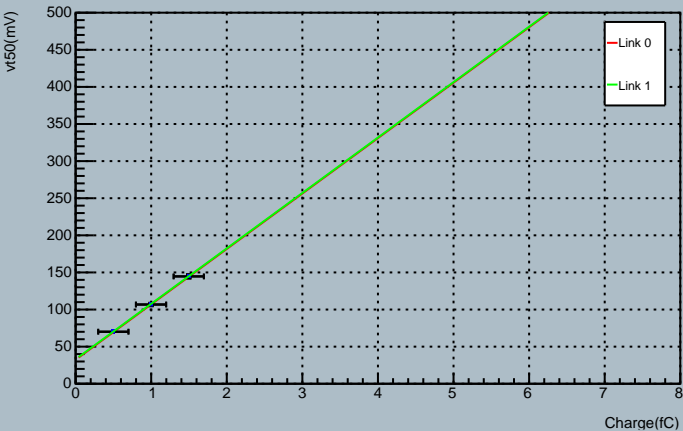
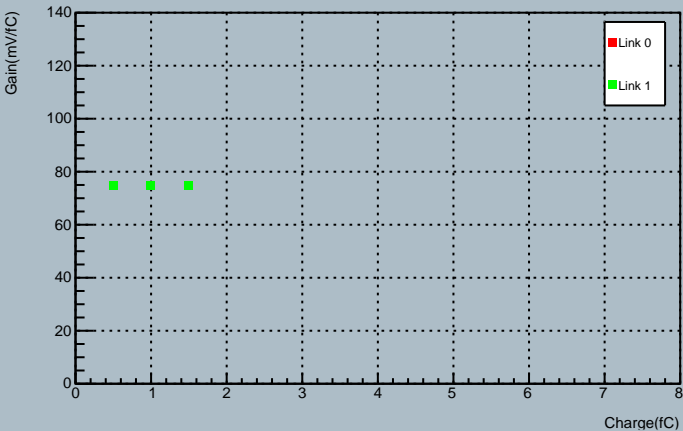


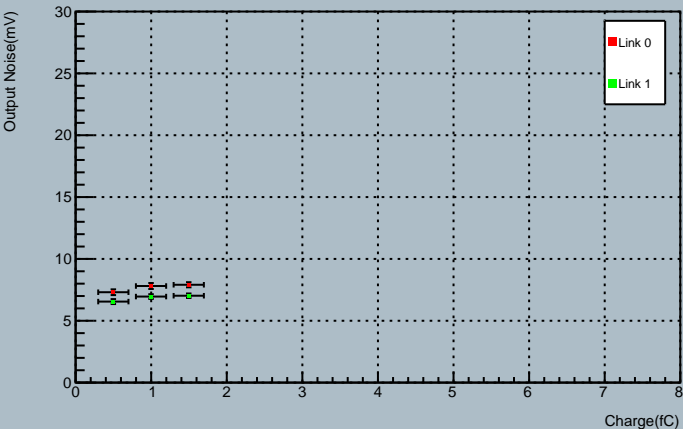
Chip 1 Response Curve



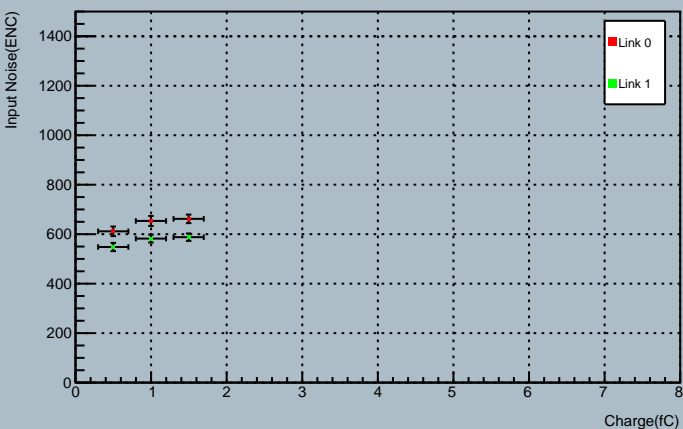
Chip 1 Gain

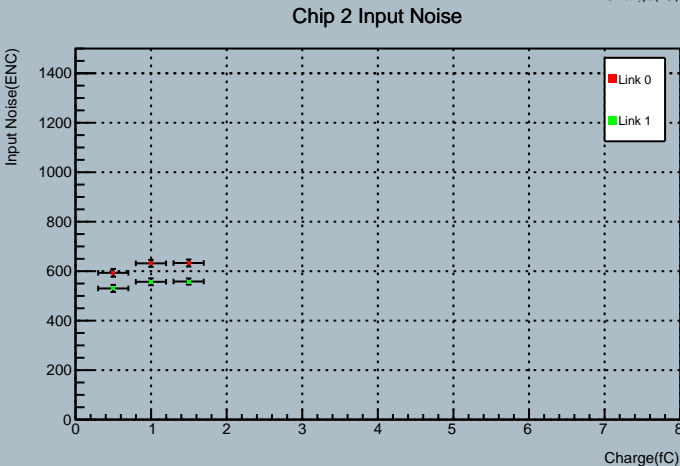
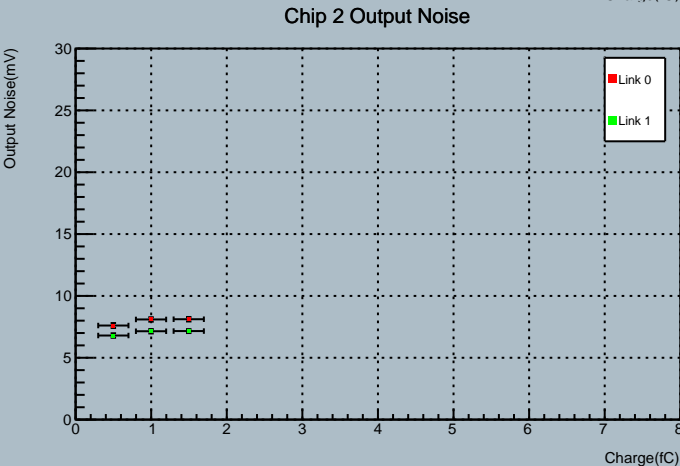
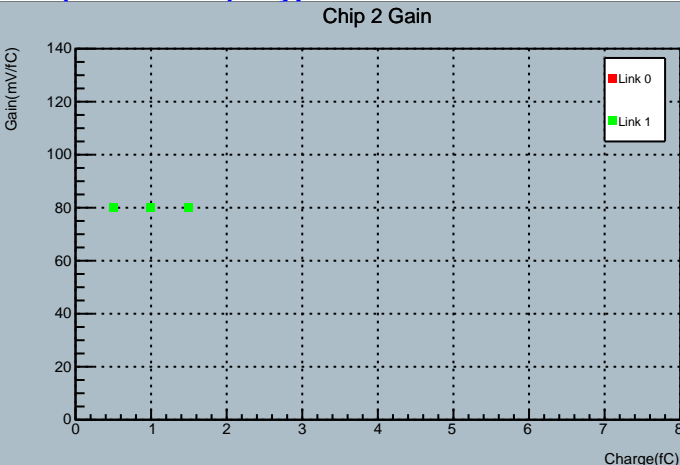
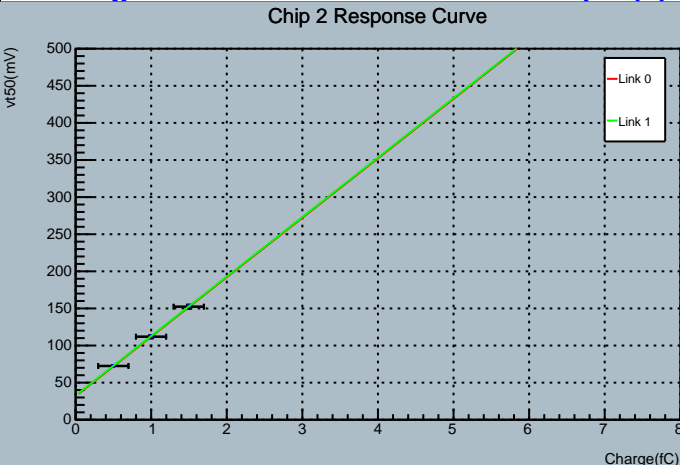


Chip 1 Output Noise

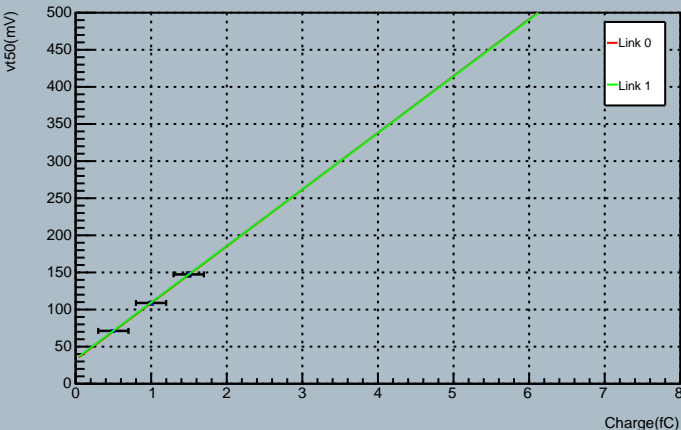


Chip 1 Input Noise

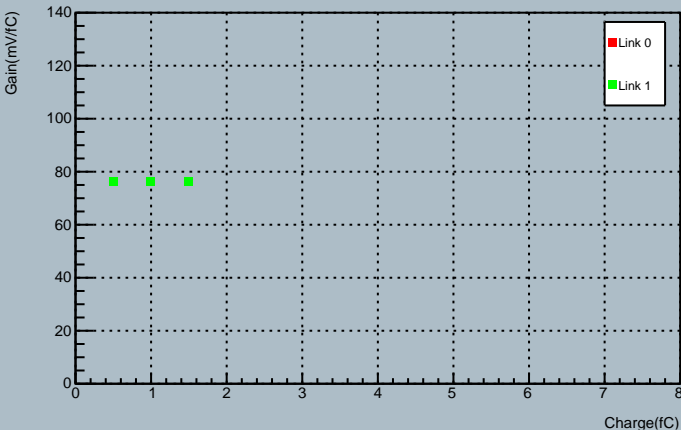




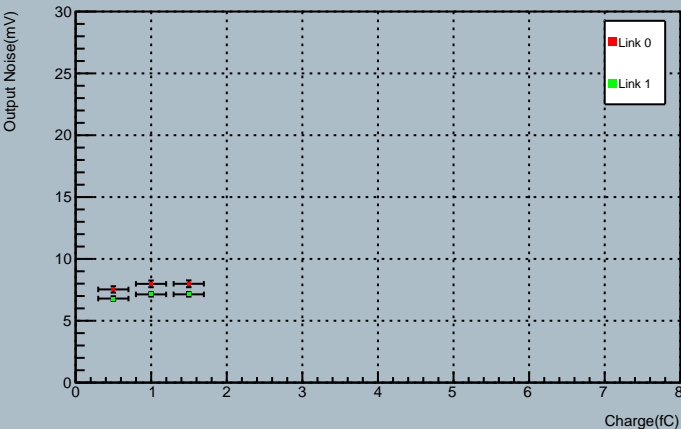
Chip 3 Response Curve



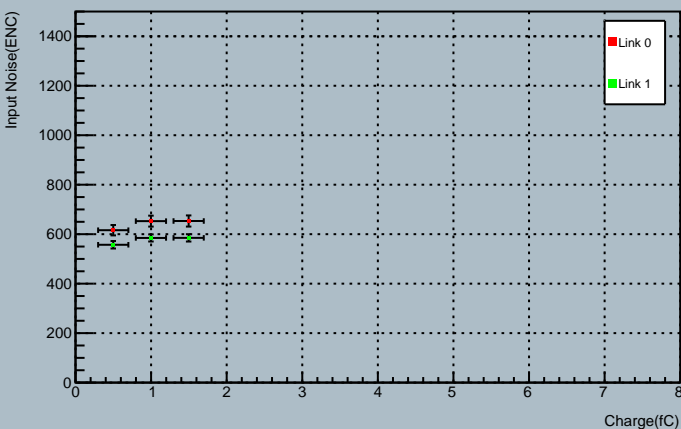
Chip 3 Gain



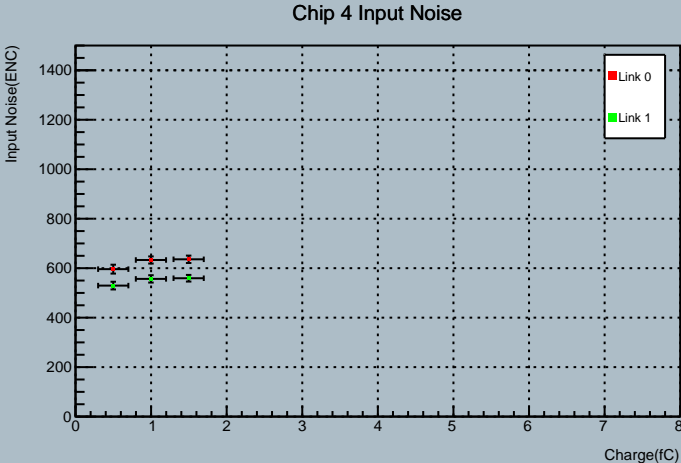
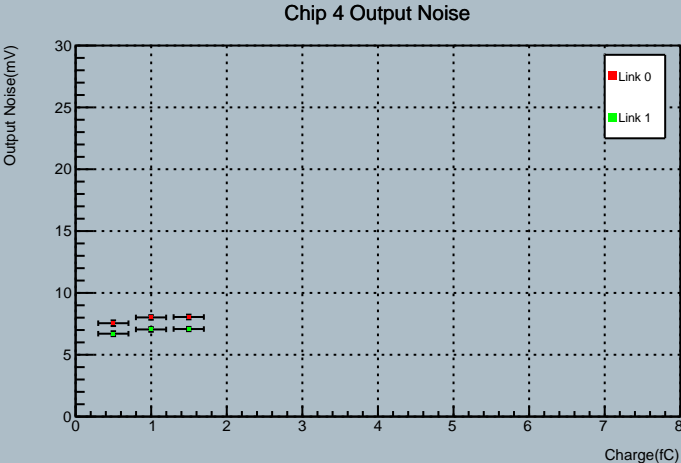
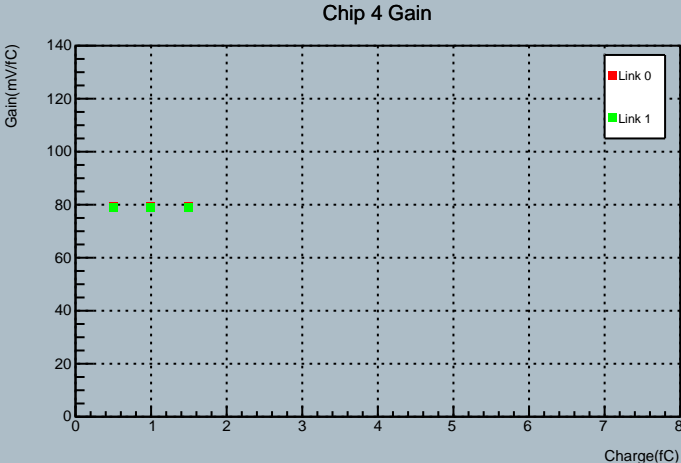
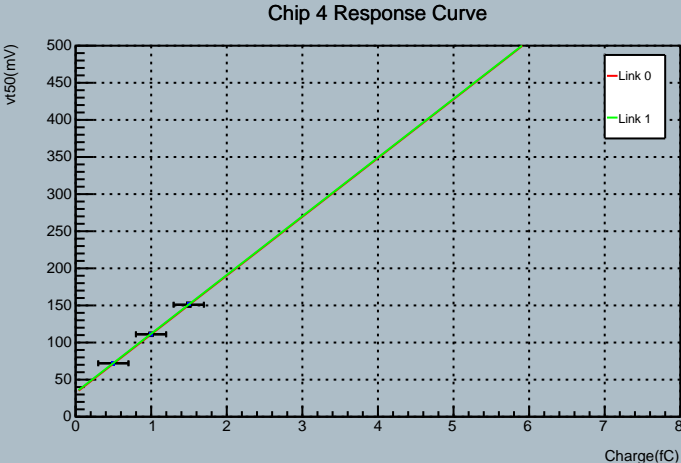
Chip 3 Output Noise

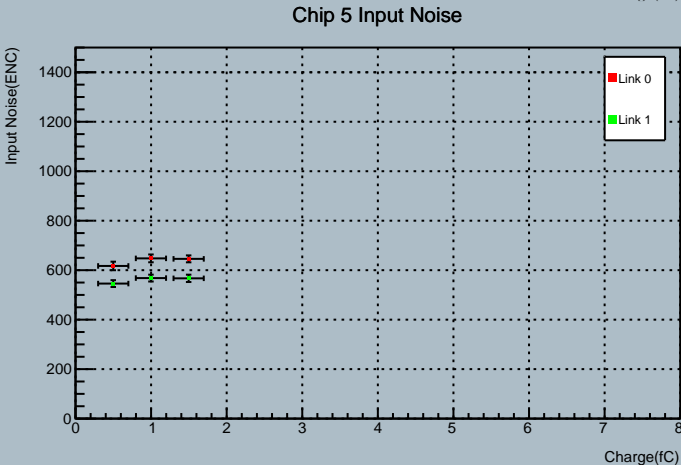
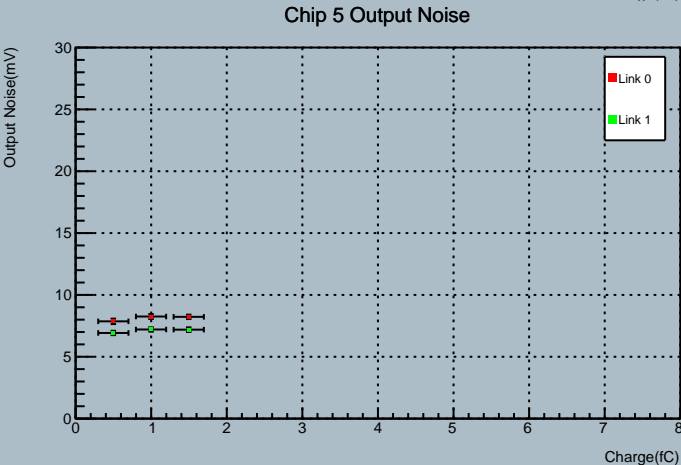
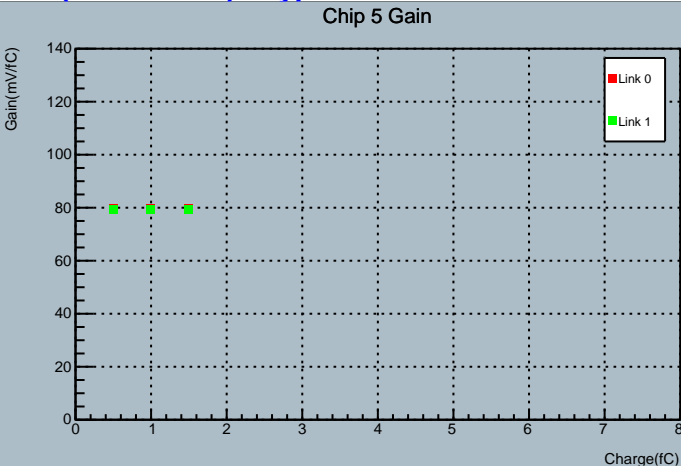
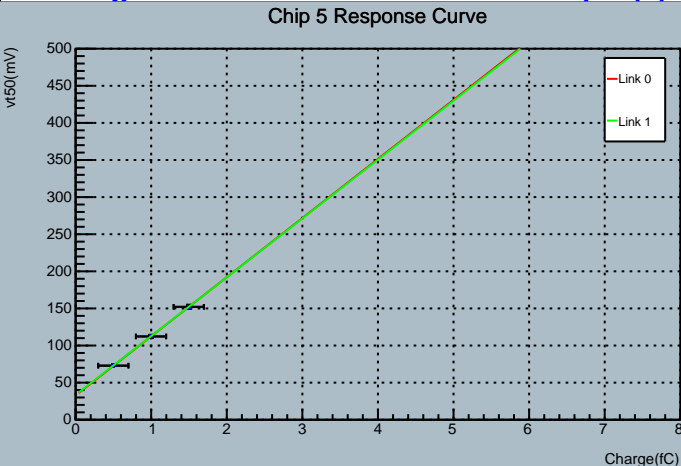


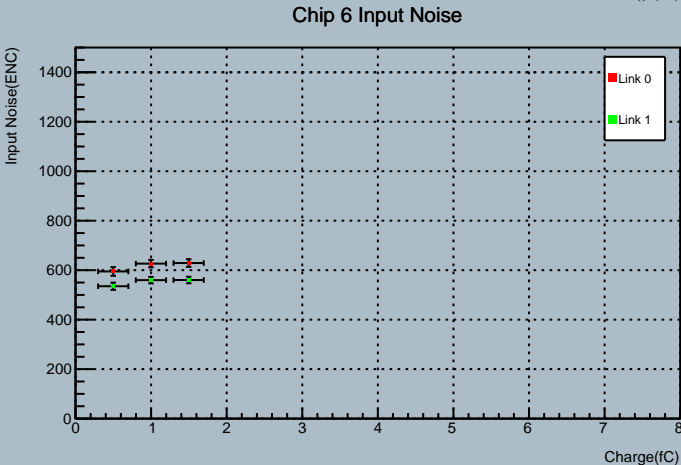
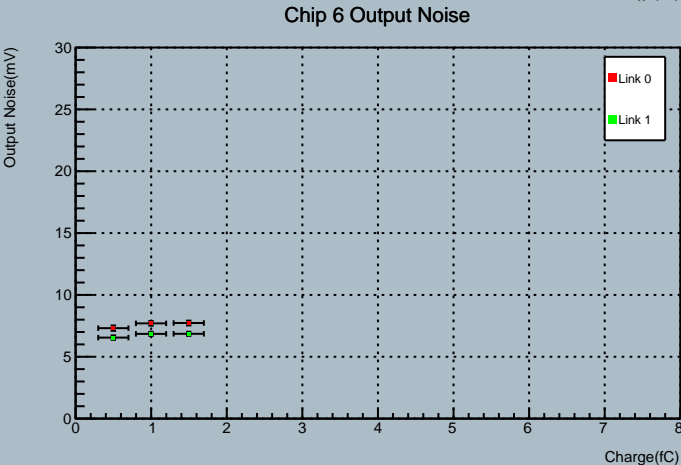
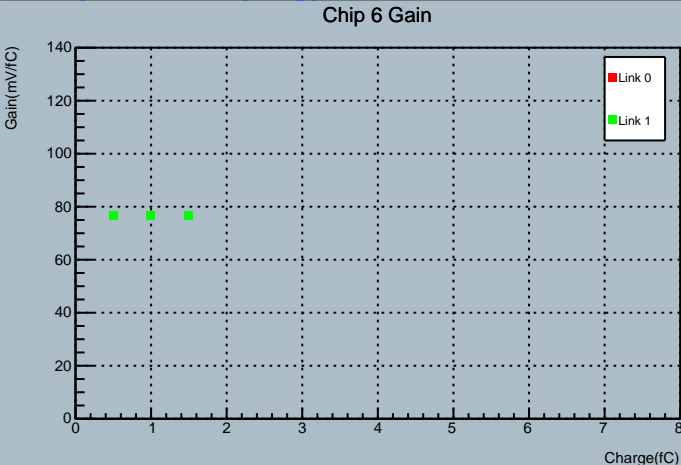
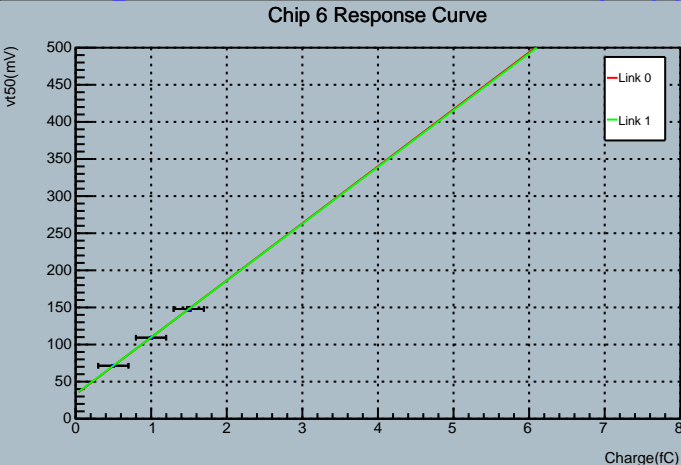
Chip 3 Input Noise

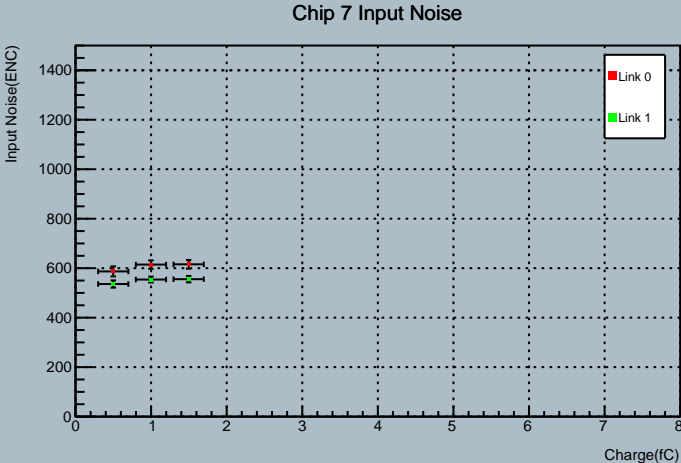
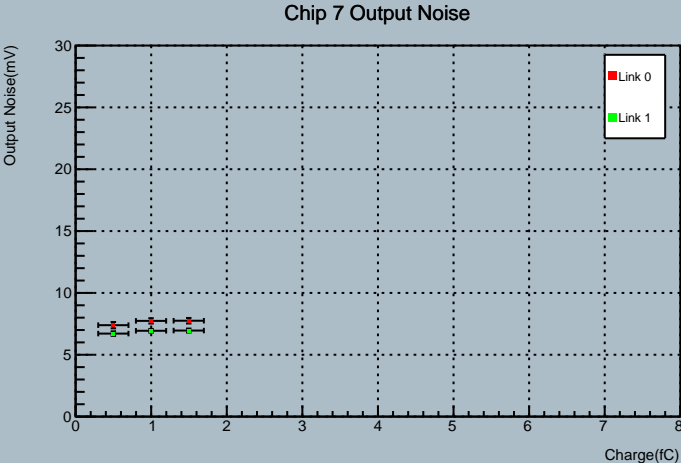
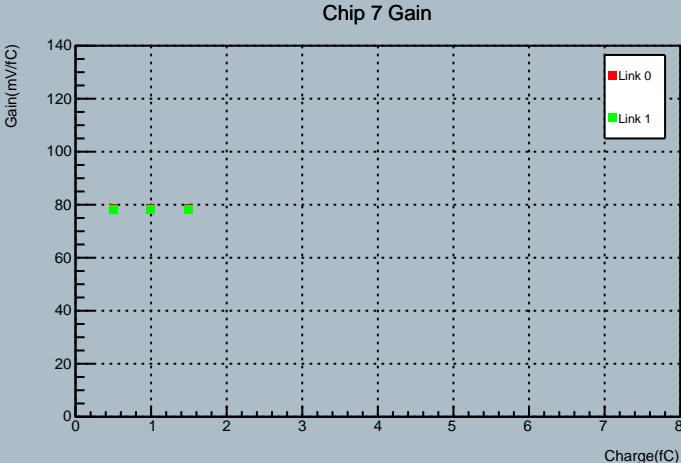
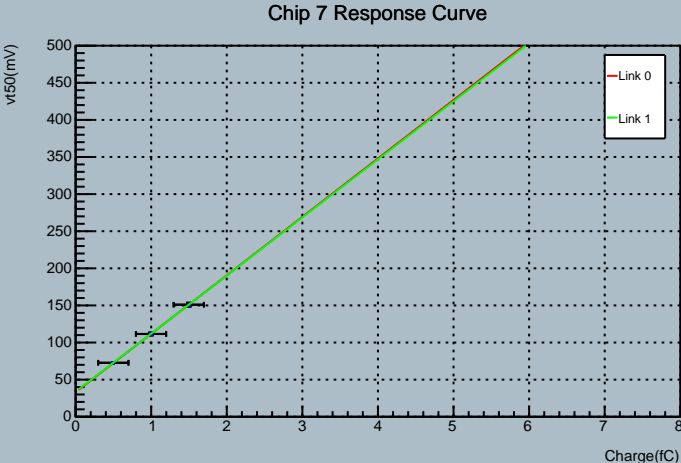


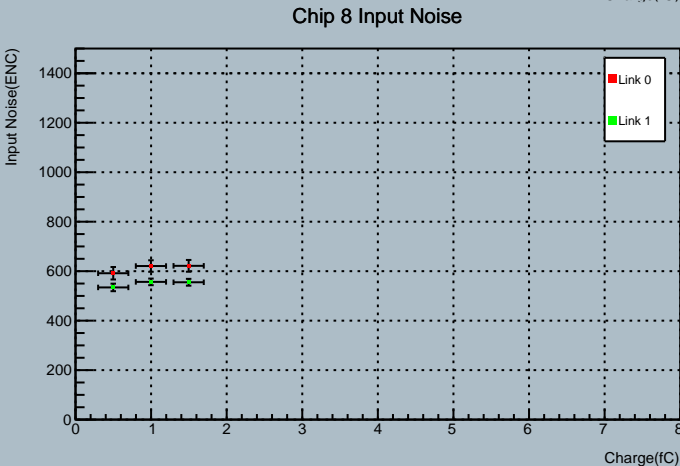
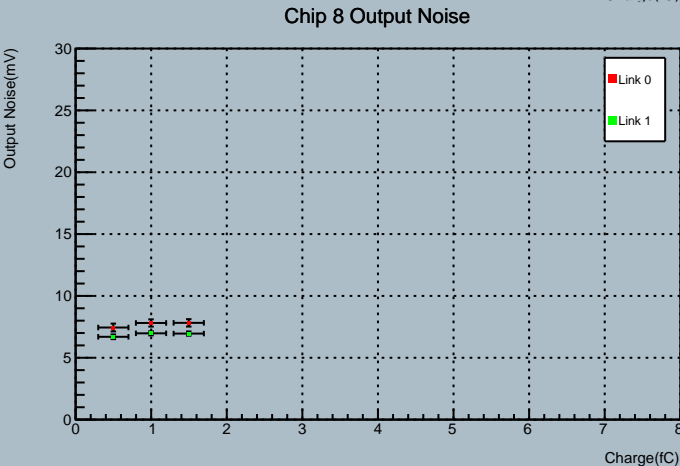
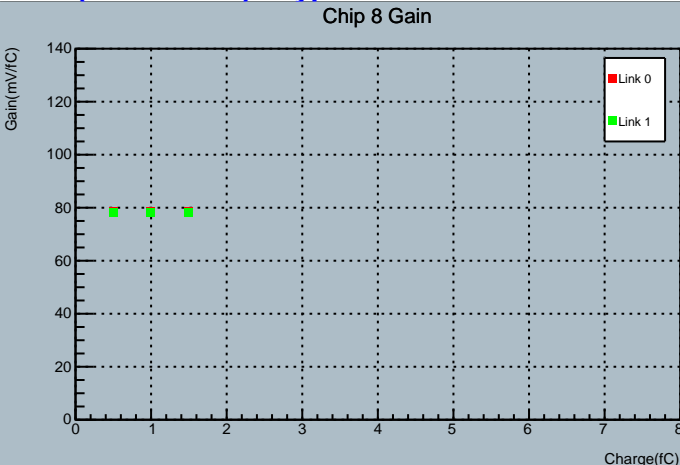
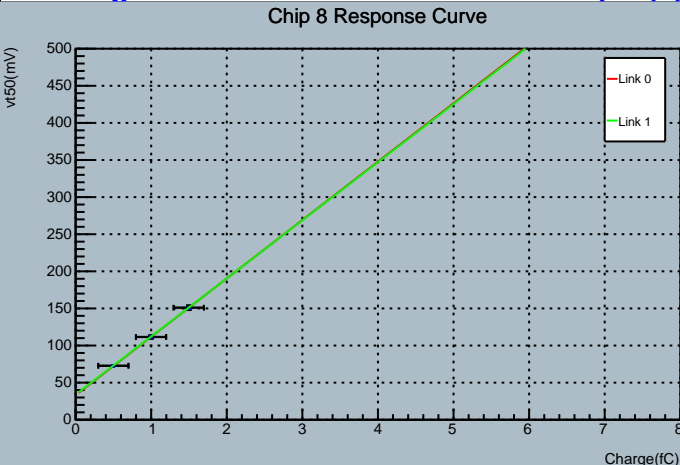




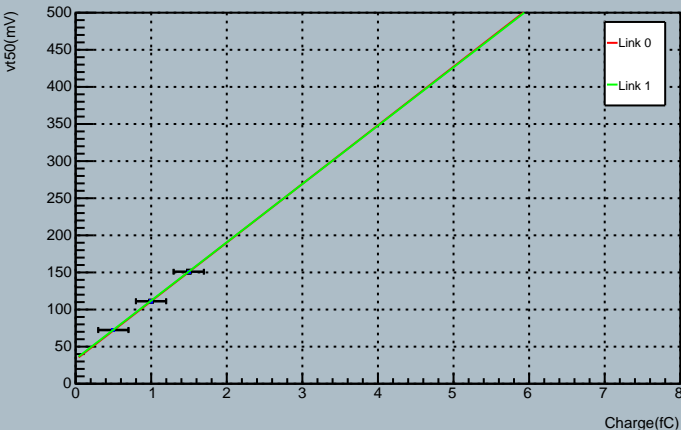




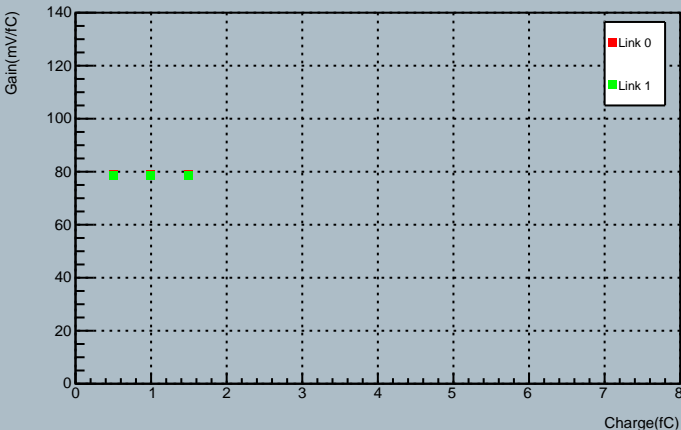




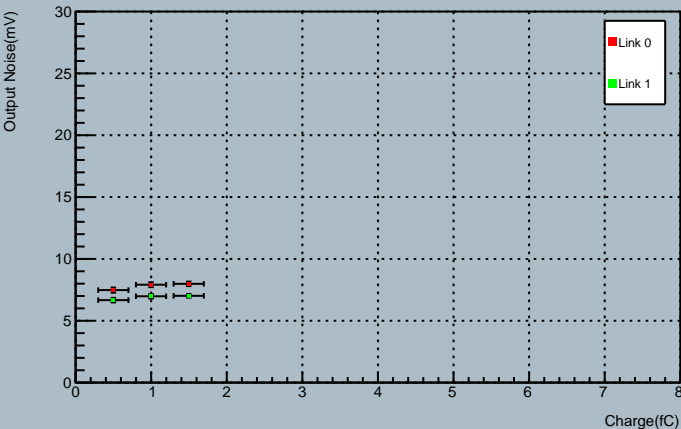
Chip 9 Response Curve



Chip 9 Gain



Chip 9 Output Noise



Chip 9 Input Noise

