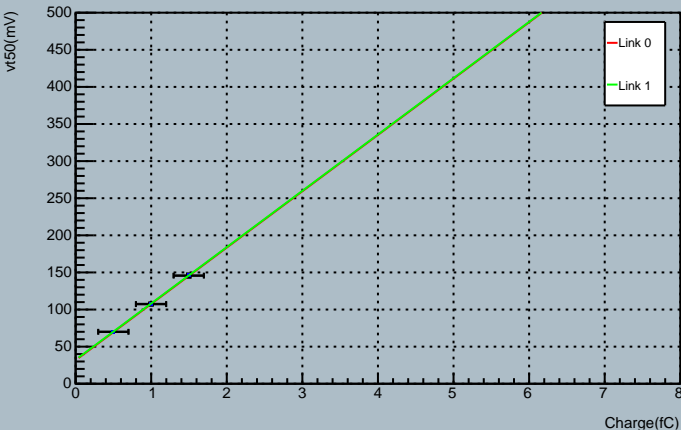
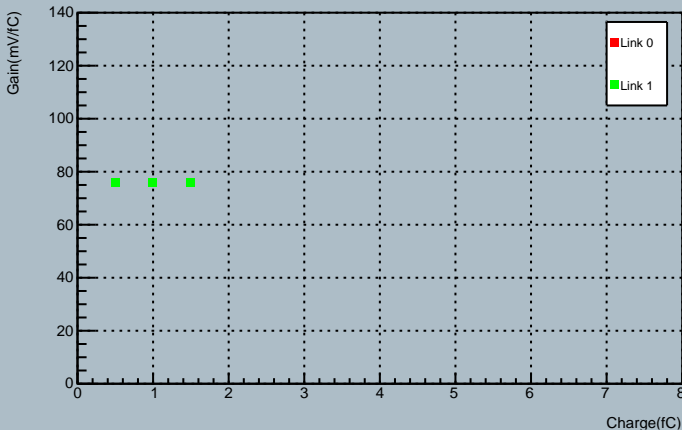


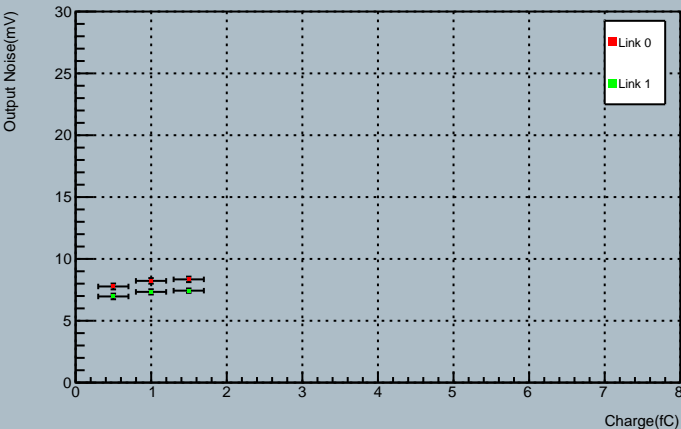
Chip 0 Response Curve



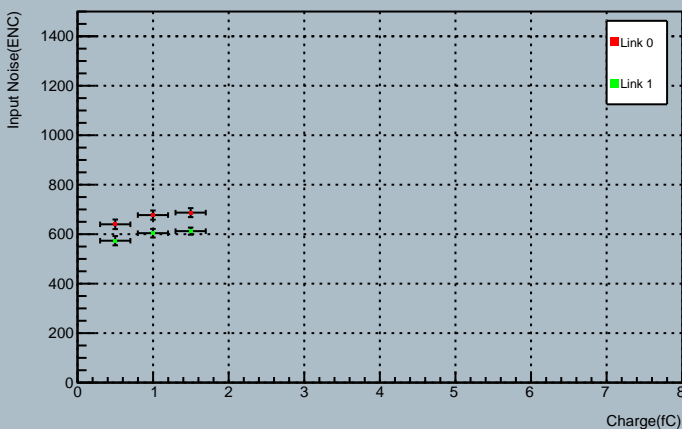
Chip 0 Gain

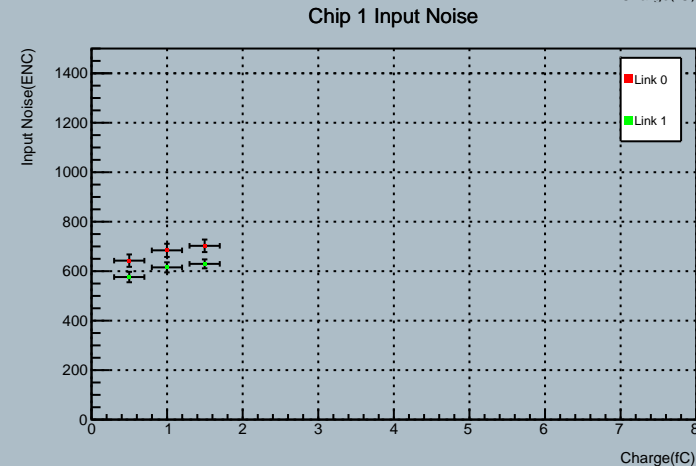
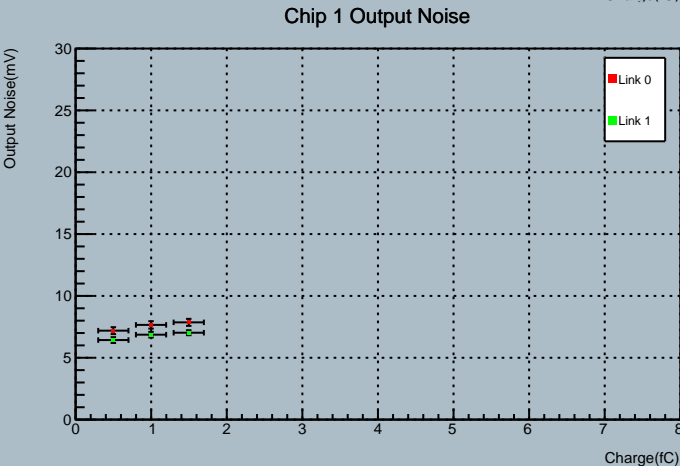
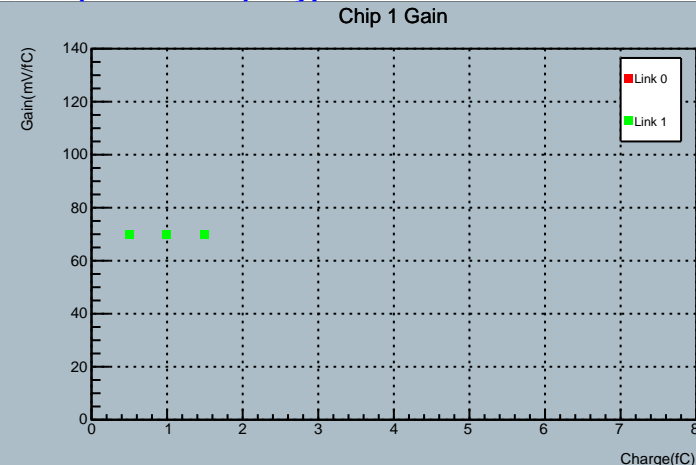
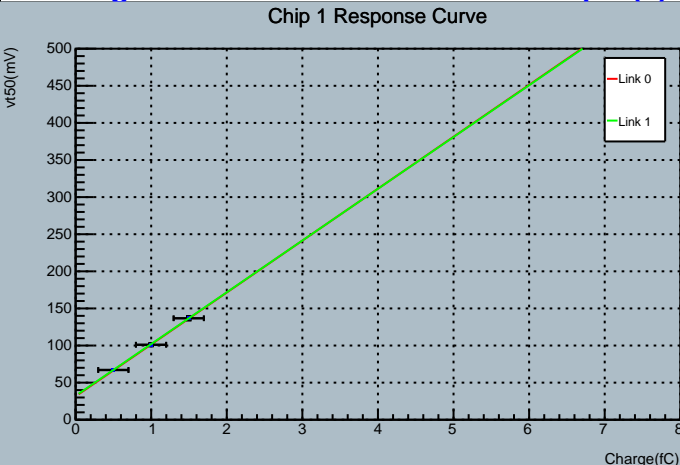


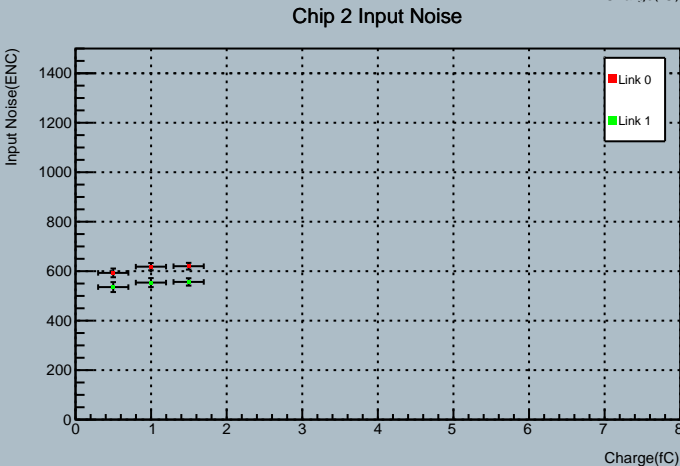
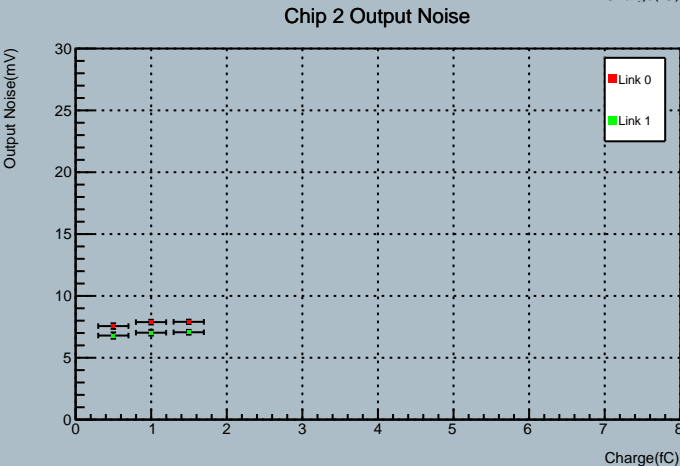
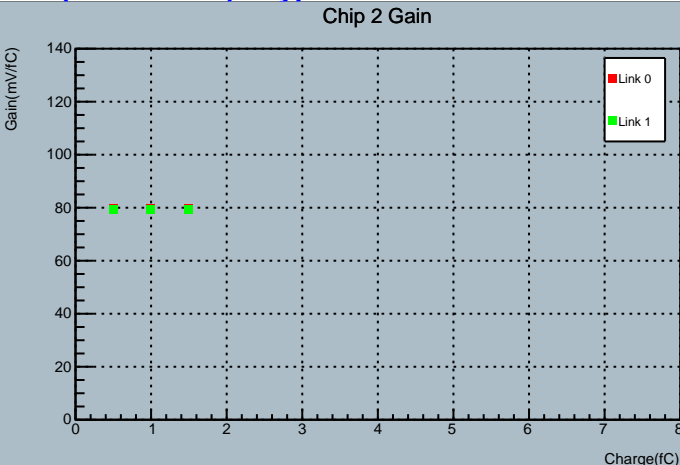
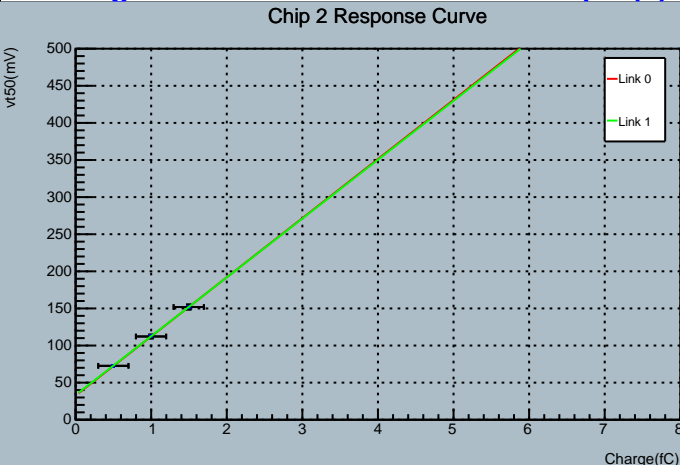
Chip 0 Output Noise

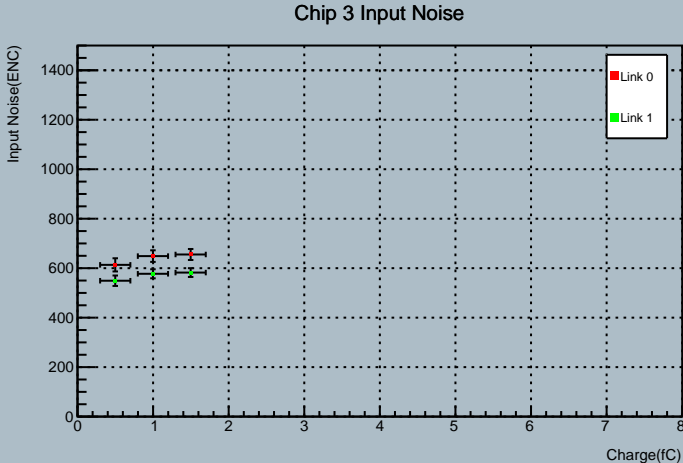
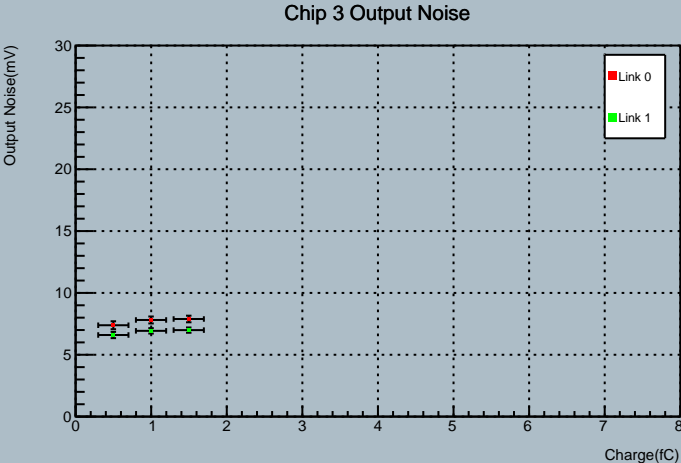
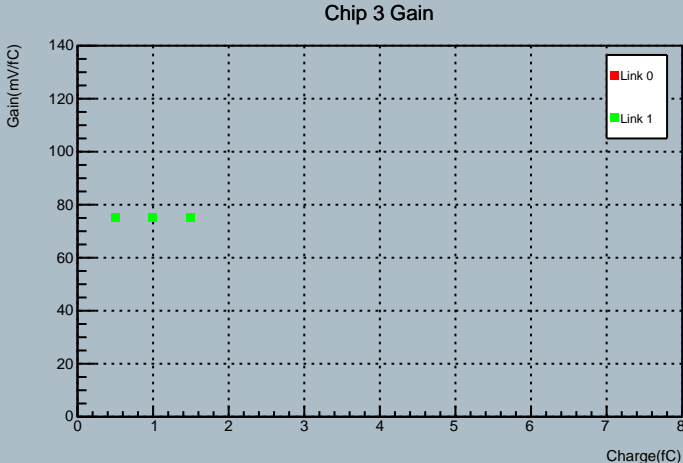
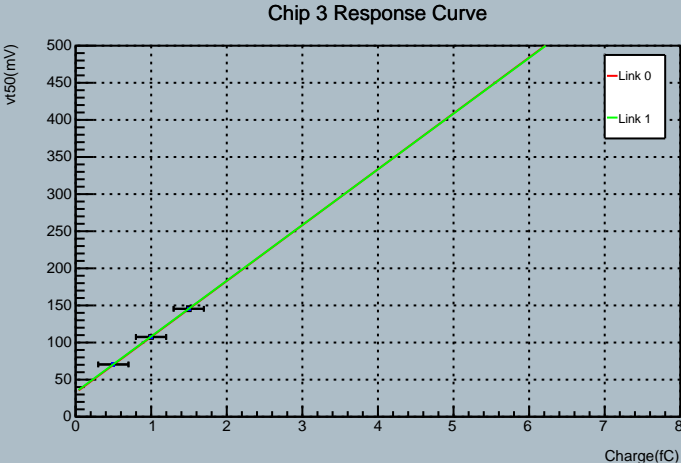


Chip 0 Input Noise

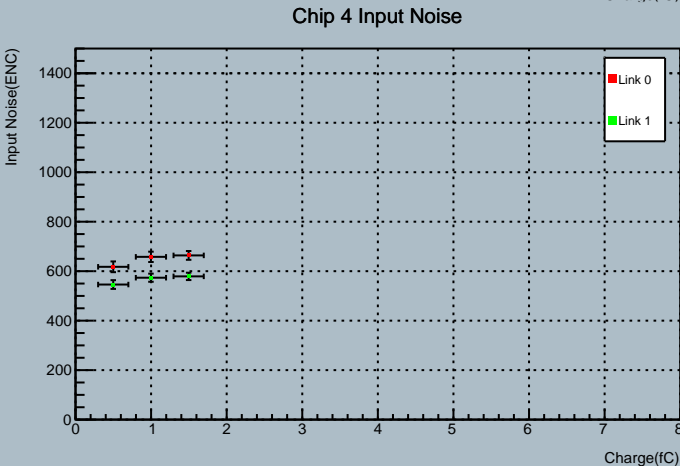
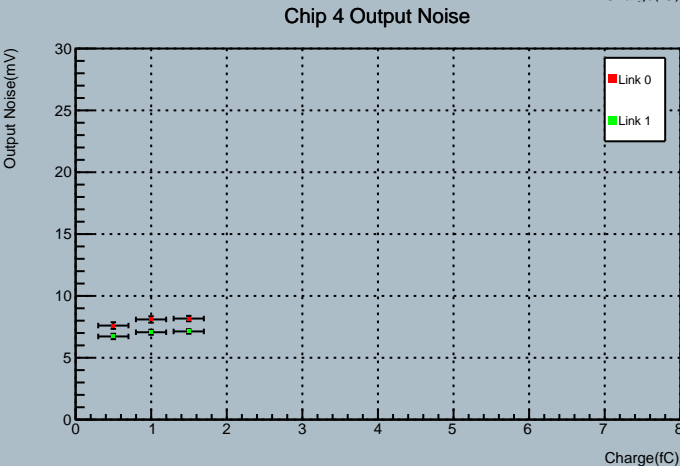
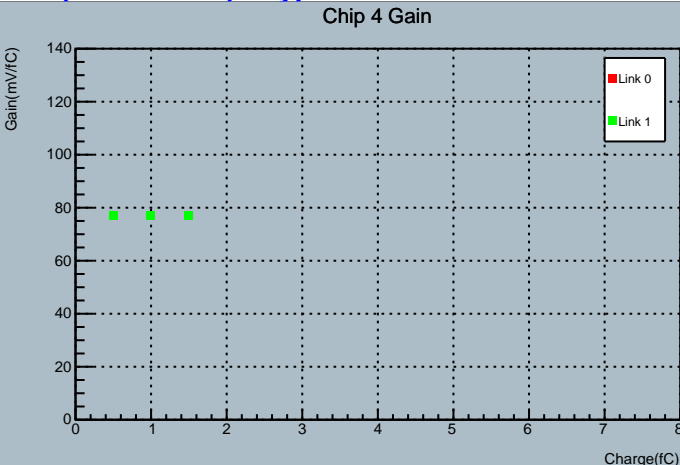
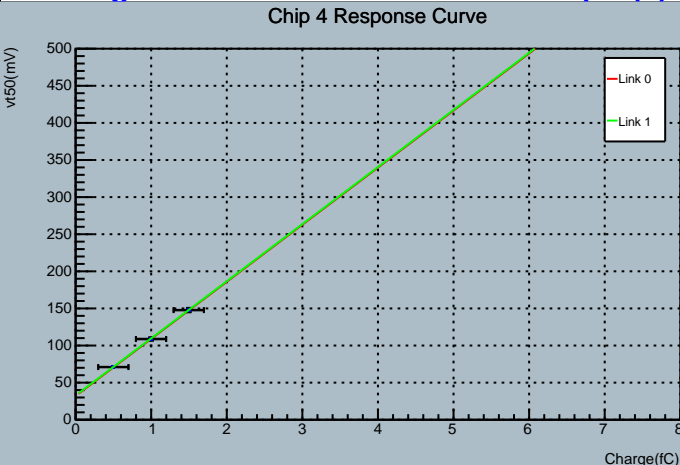




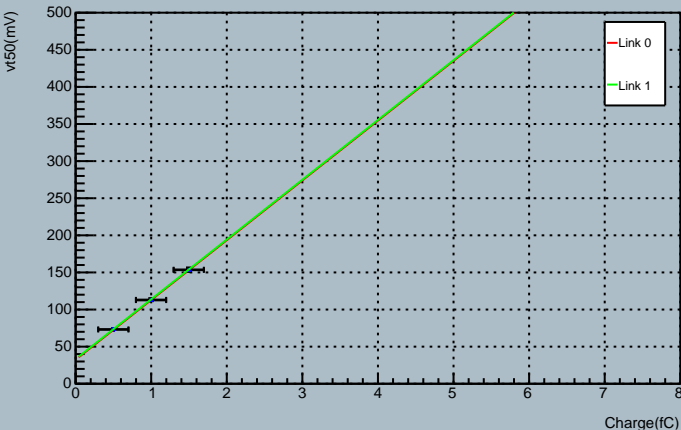




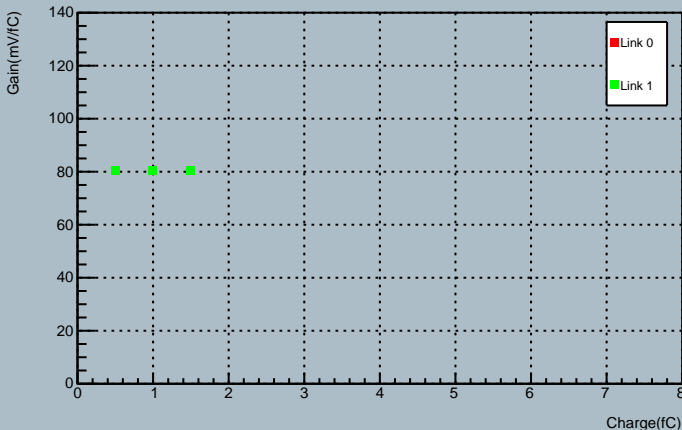




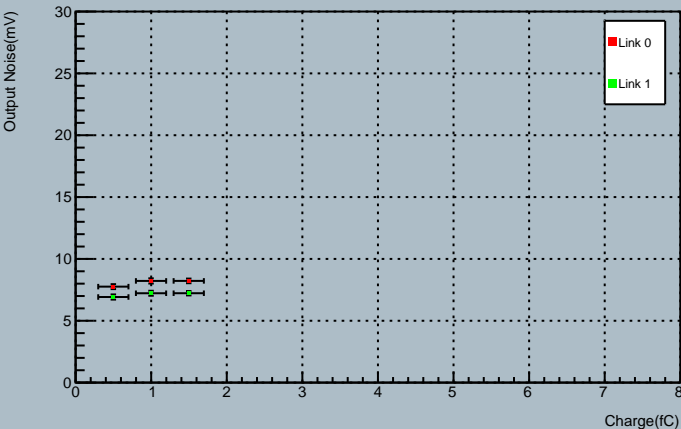
Chip 5 Response Curve



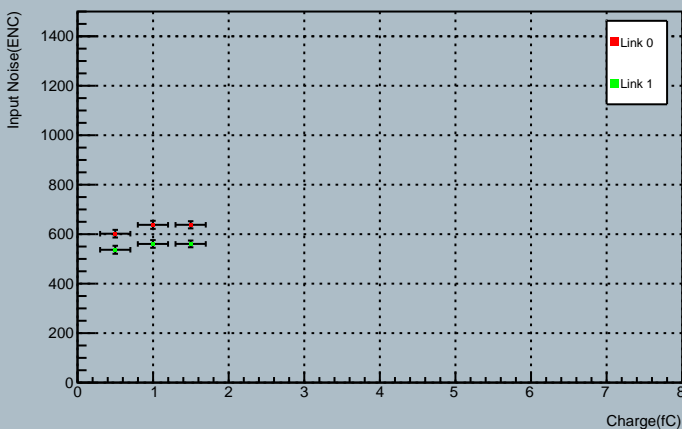
Chip 5 Gain



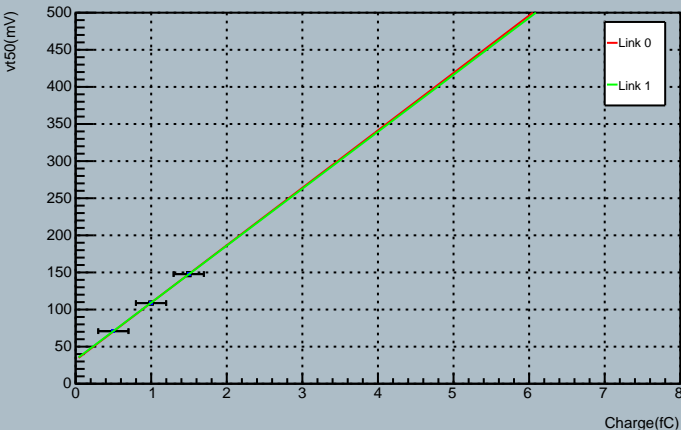
Chip 5 Output Noise



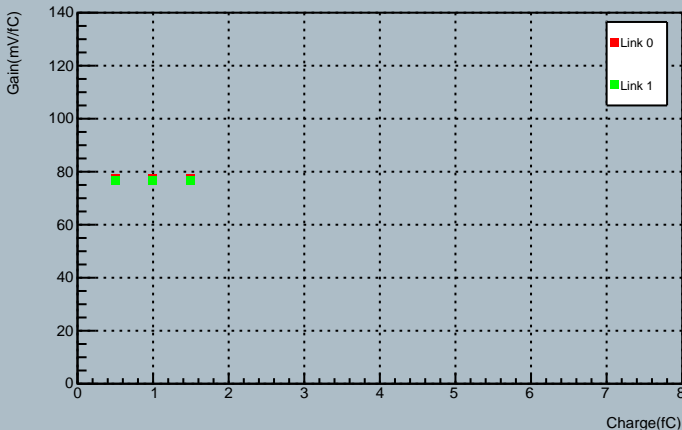
Chip 5 Input Noise



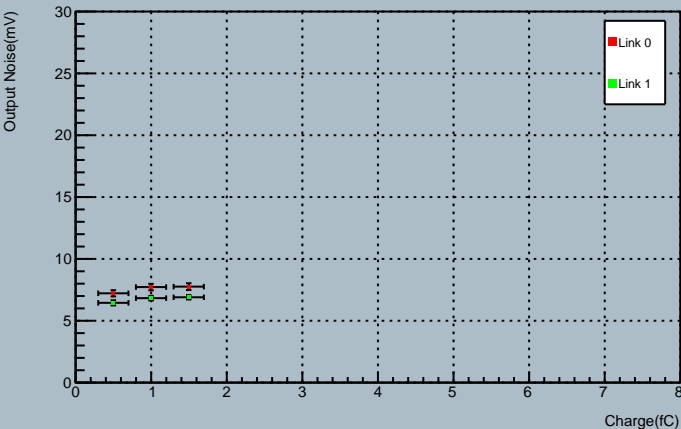
Chip 6 Response Curve



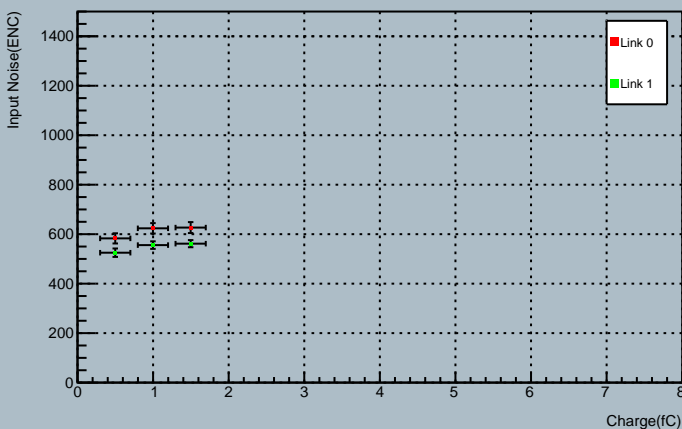
Chip 6 Gain

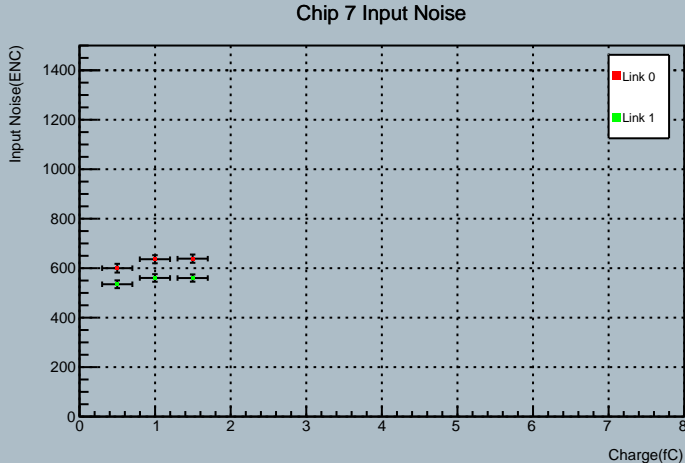
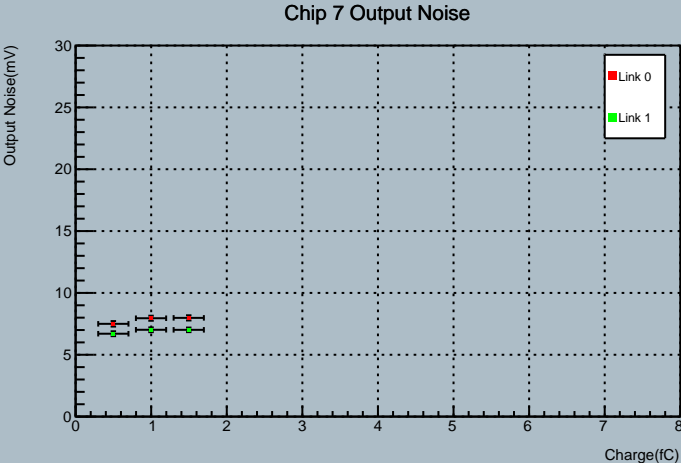
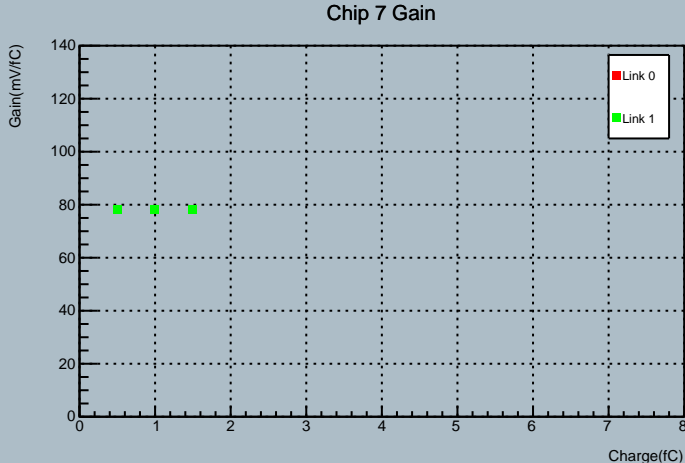
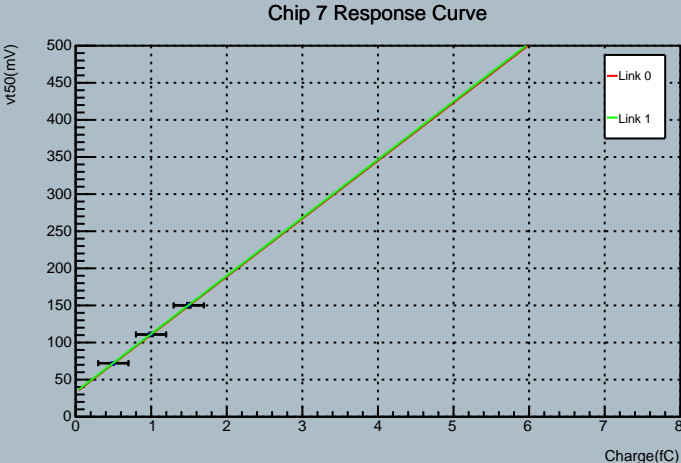


Chip 6 Output Noise

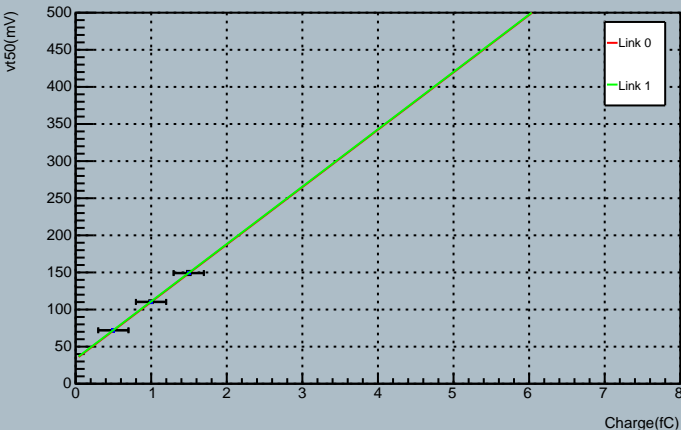


Chip 6 Input Noise

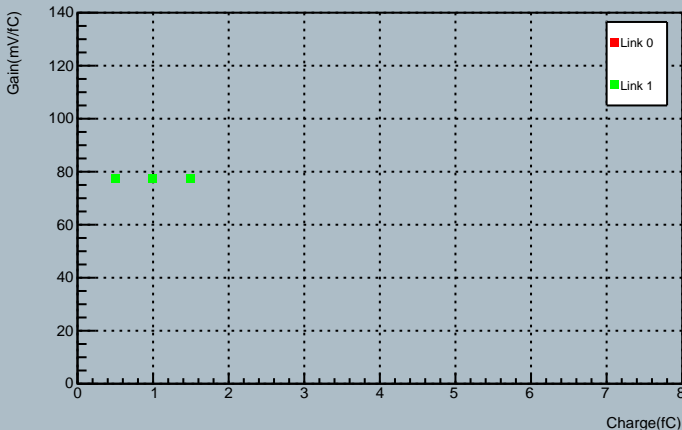




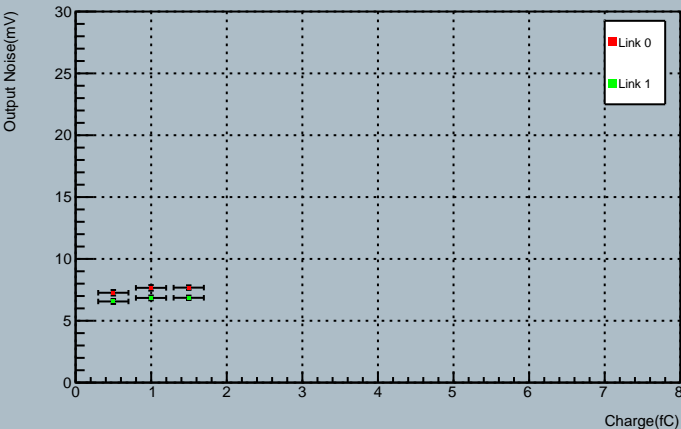
Chip 8 Response Curve



Chip 8 Gain



Chip 8 Output Noise



Chip 8 Input Noise

