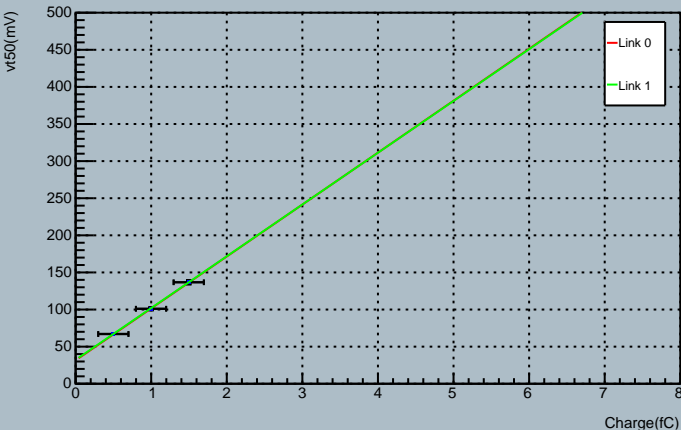
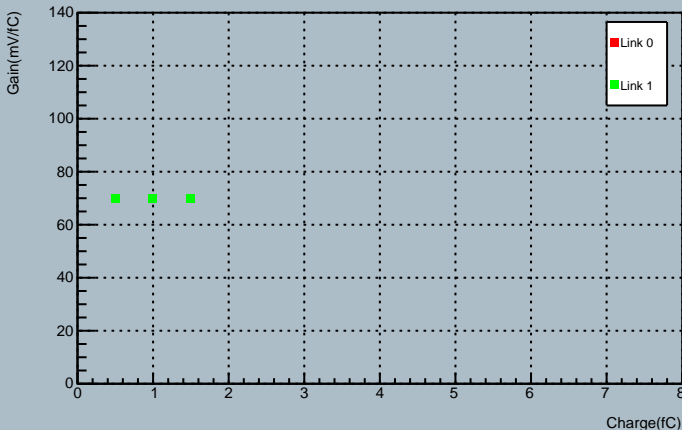


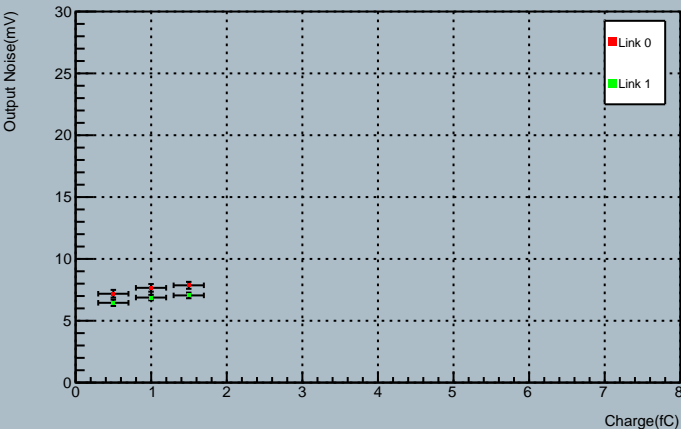
Chip 1 Response Curve



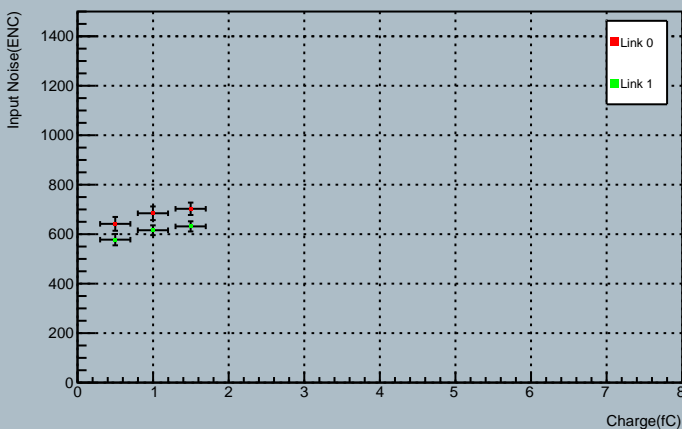
Chip 1 Gain



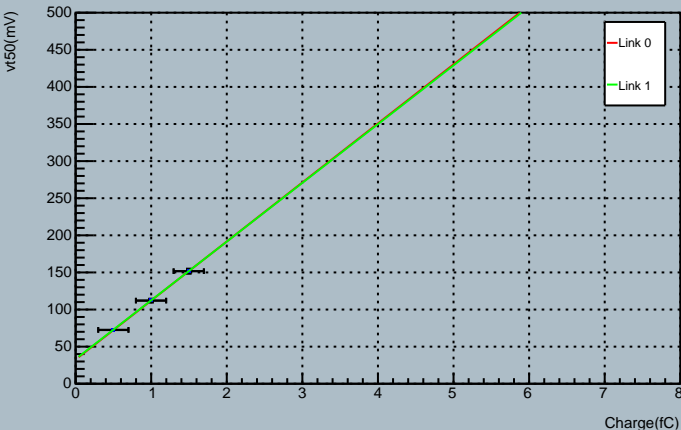
Chip 1 Output Noise



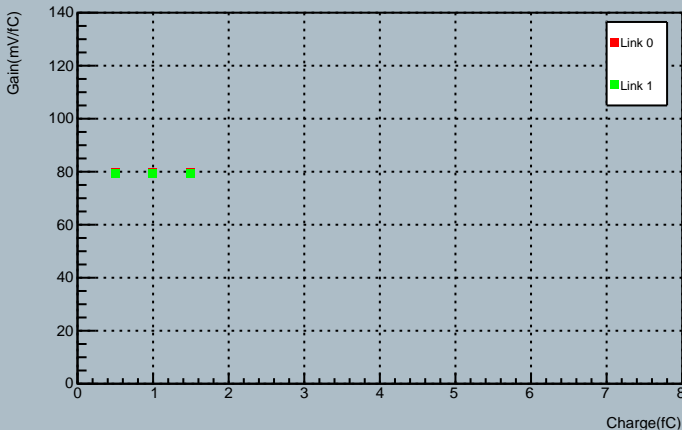
Chip 1 Input Noise



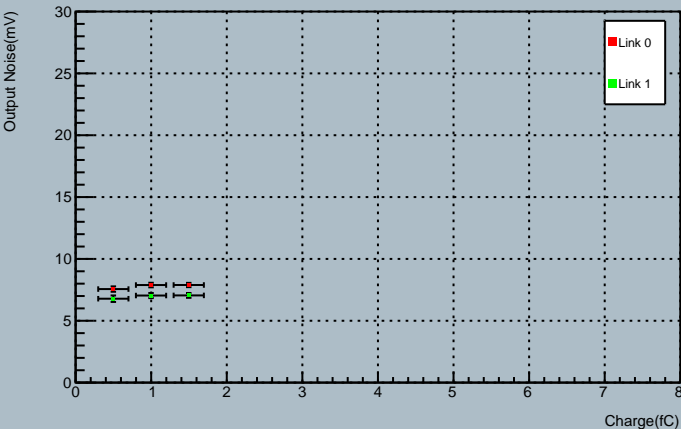
Chip 2 Response Curve



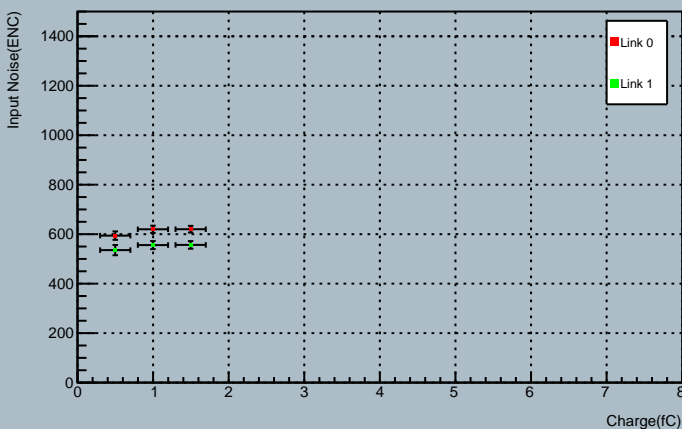
Chip 2 Gain



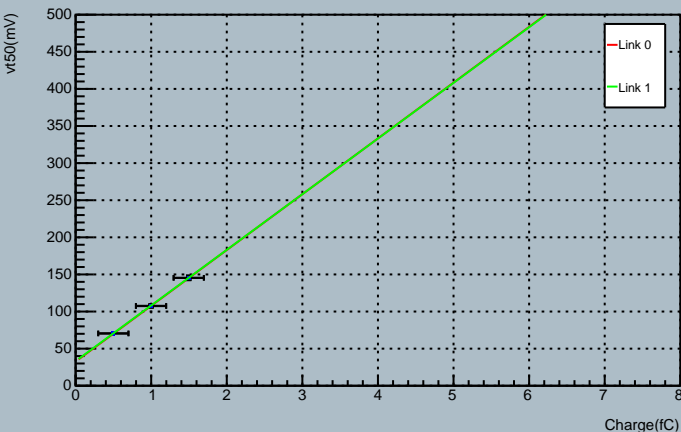
Chip 2 Output Noise



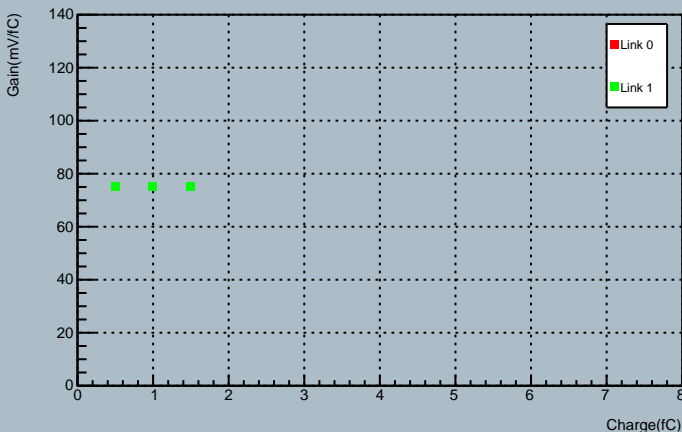
Chip 2 Input Noise



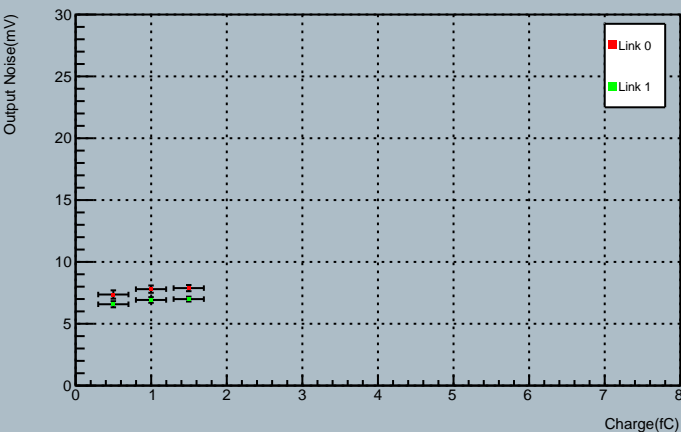
Chip 3 Response Curve



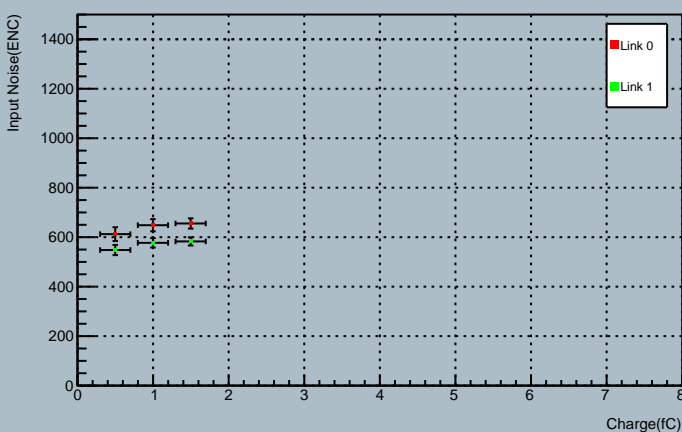
Chip 3 Gain



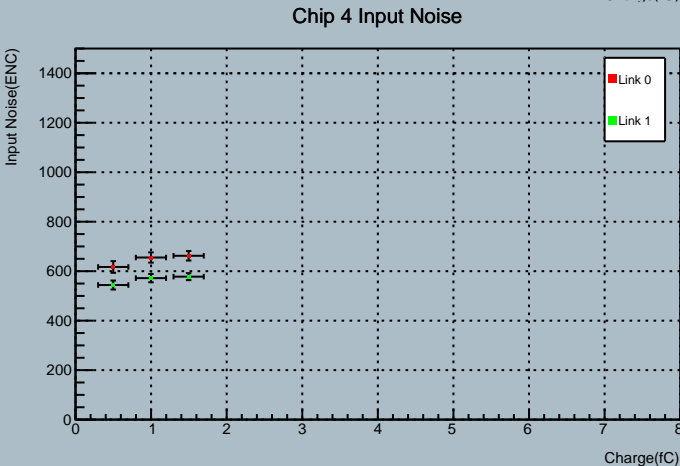
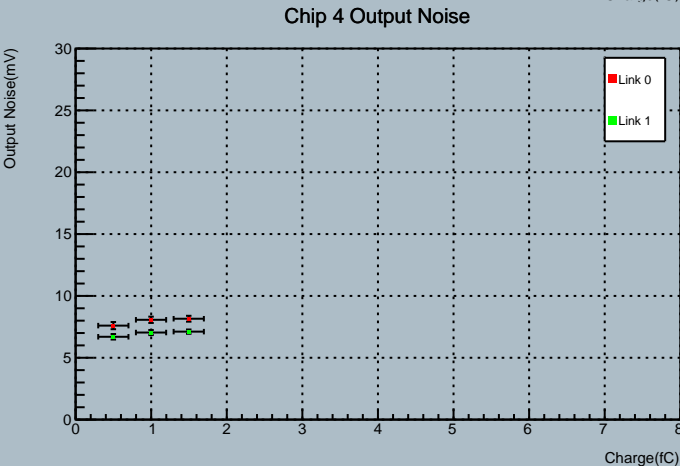
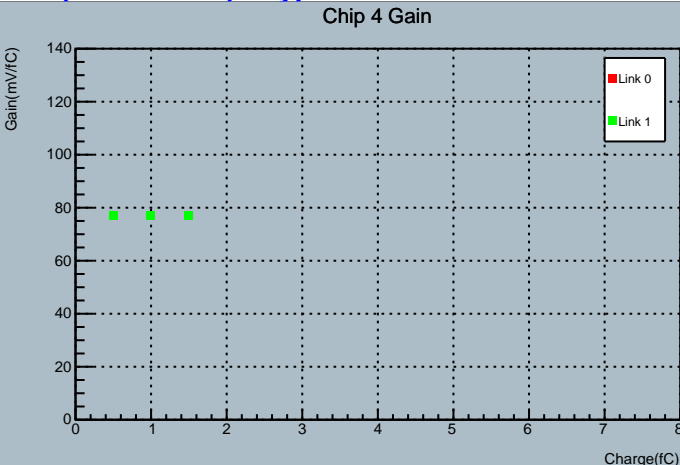
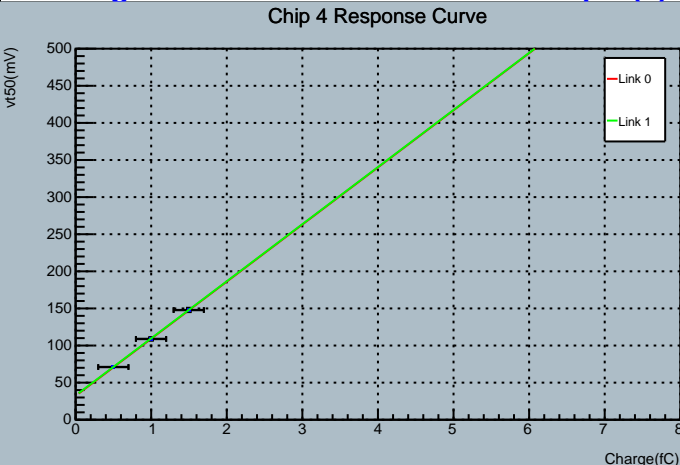
Chip 3 Output Noise

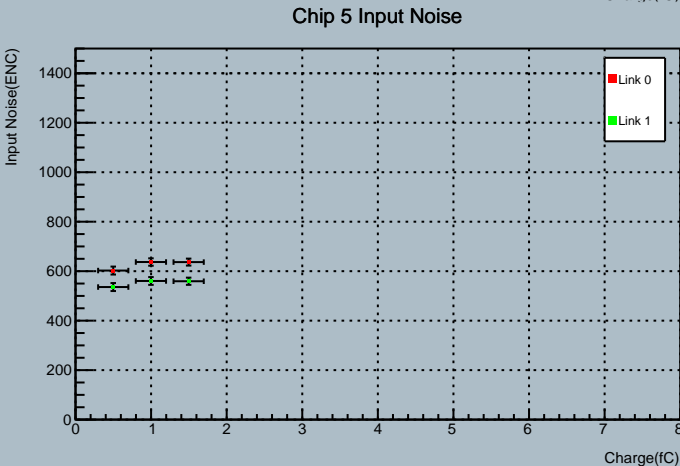
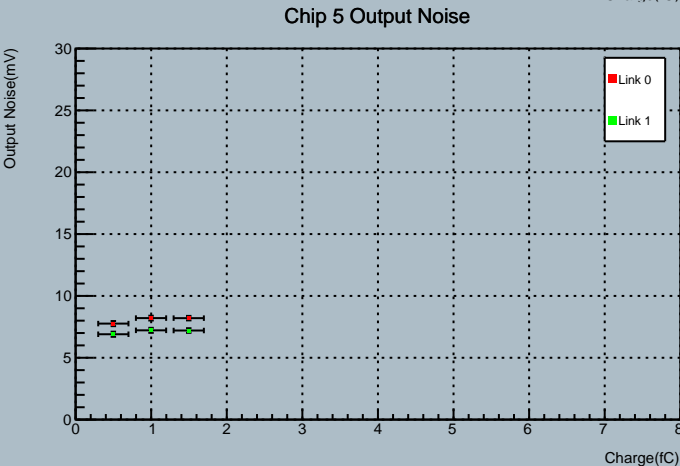
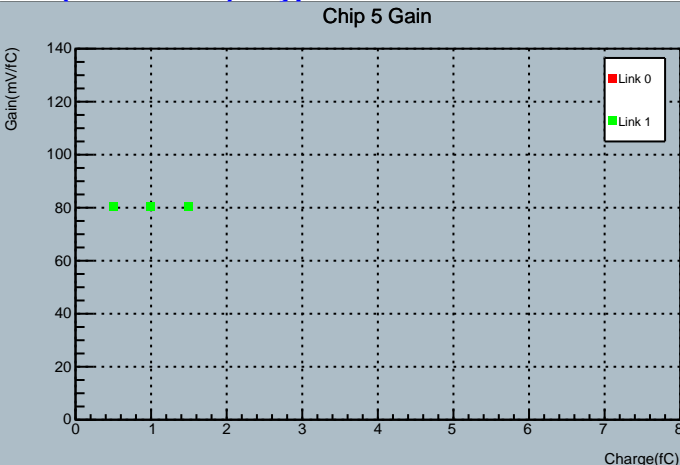
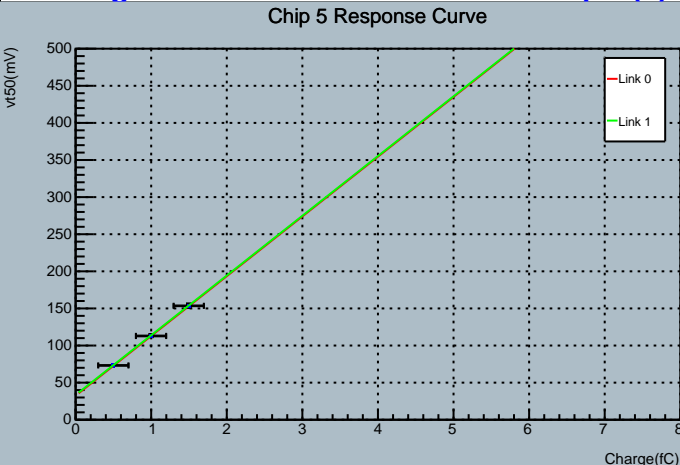


Chip 3 Input Noise

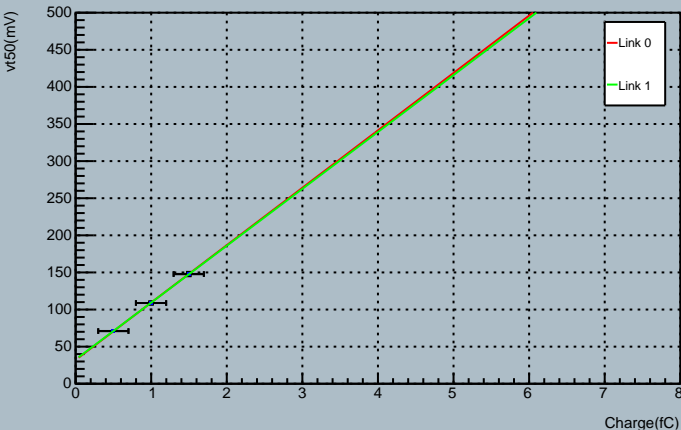




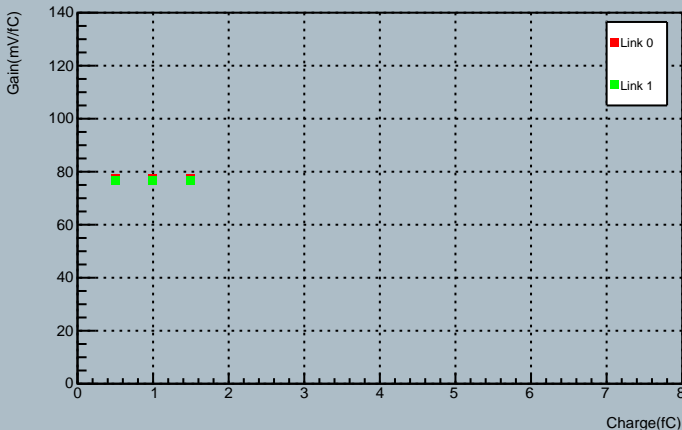




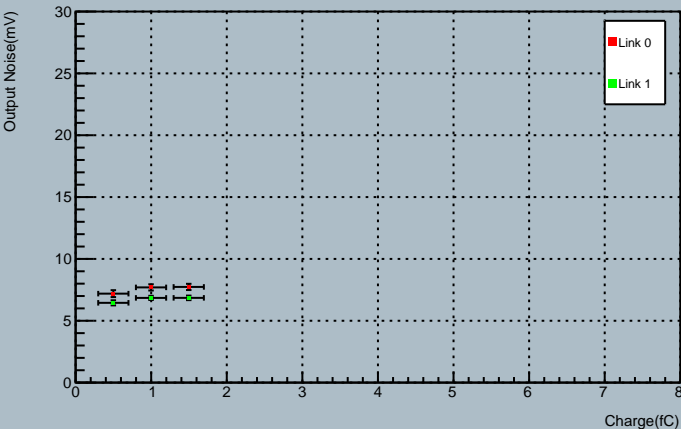
Chip 6 Response Curve



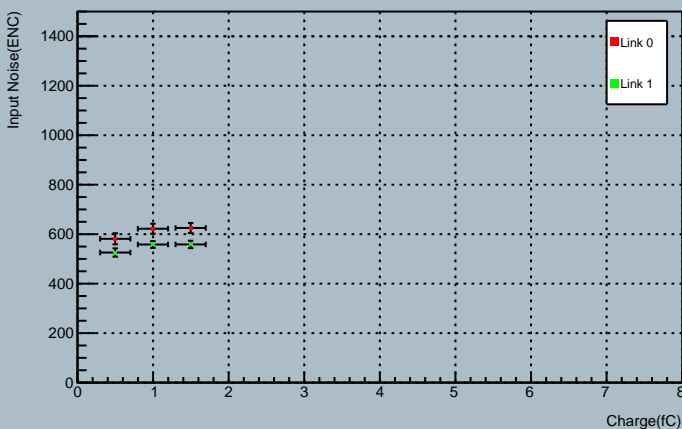
Chip 6 Gain

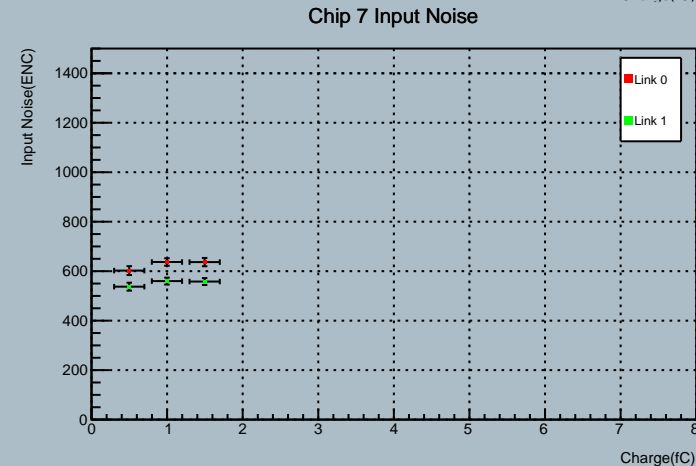
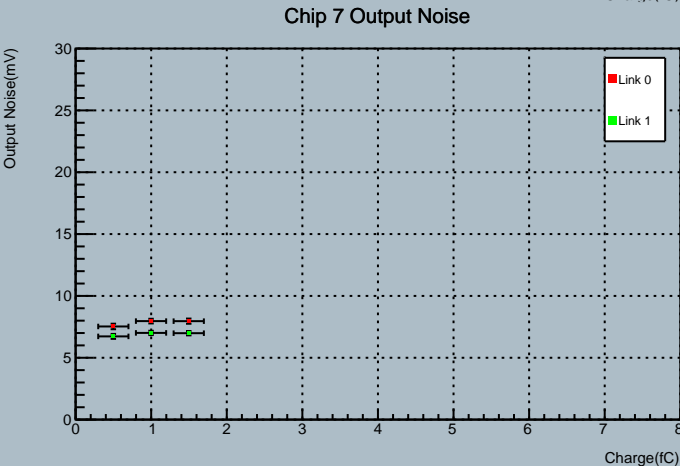
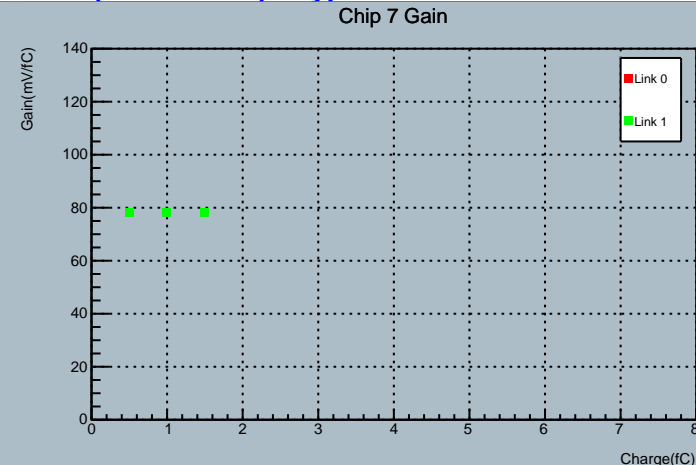
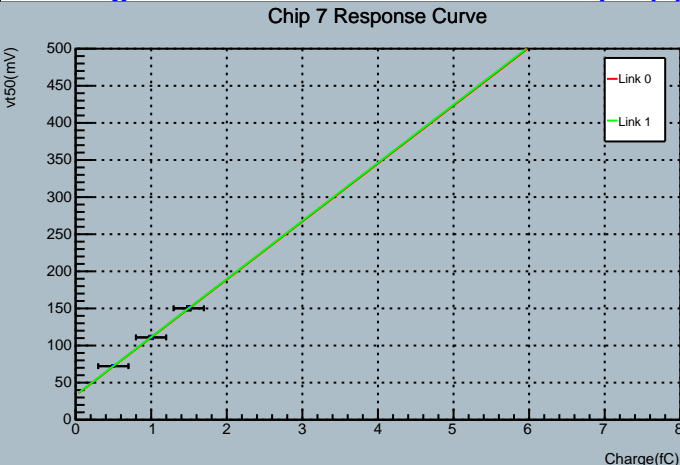


Chip 6 Output Noise

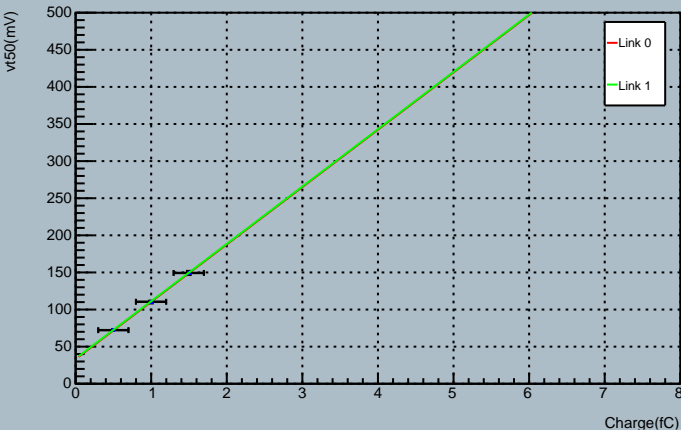


Chip 6 Input Noise

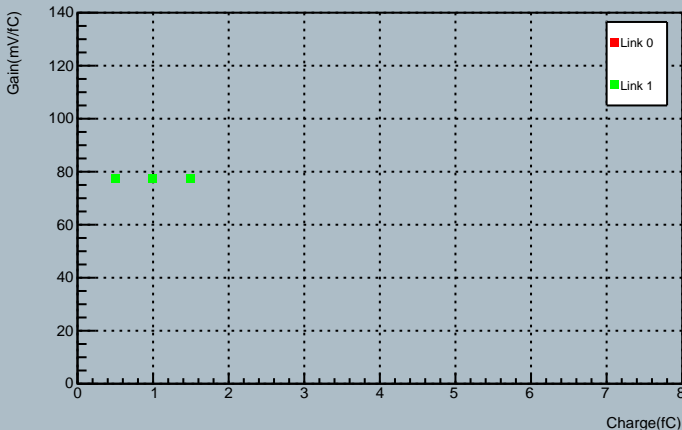




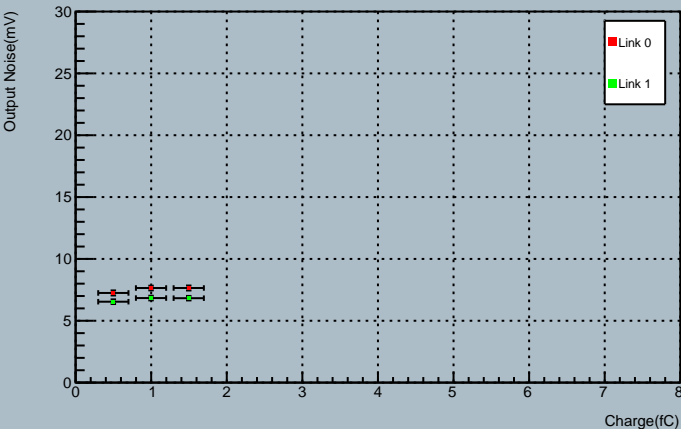
Chip 8 Response Curve



Chip 8 Gain



Chip 8 Output Noise



Chip 8 Input Noise

