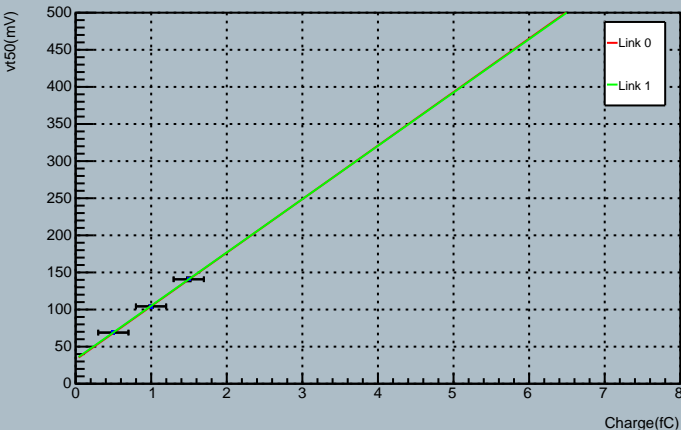
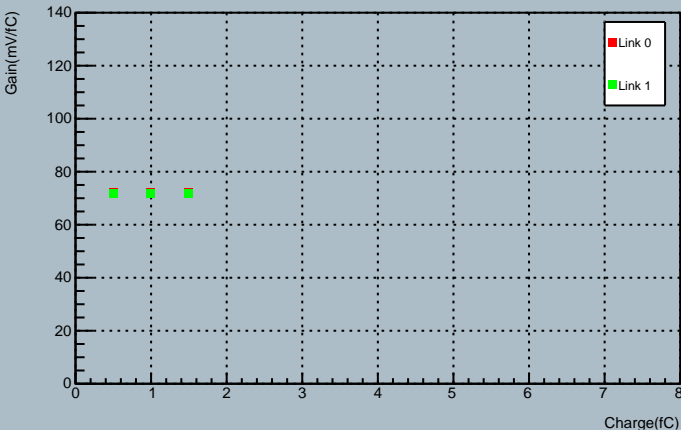


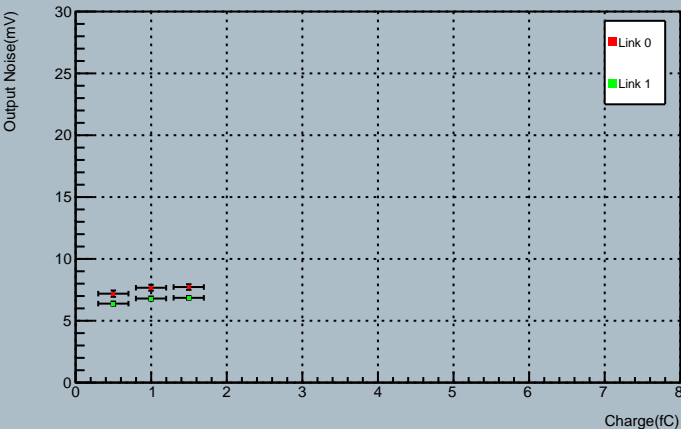
Chip 0 Response Curve



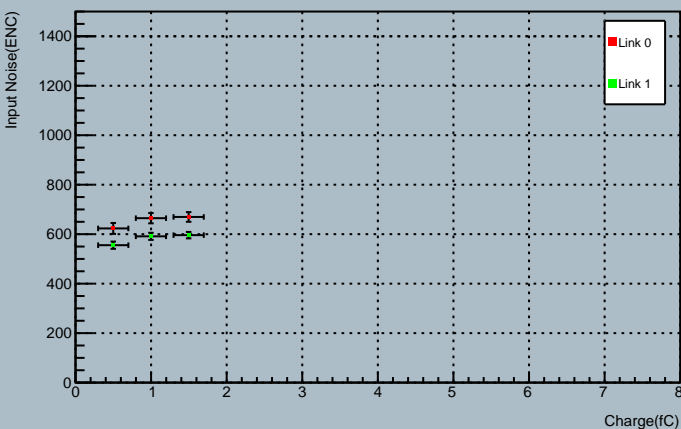
Chip 0 Gain

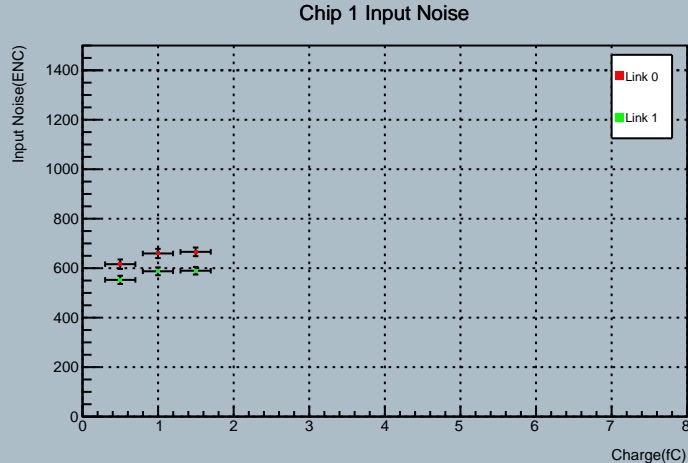
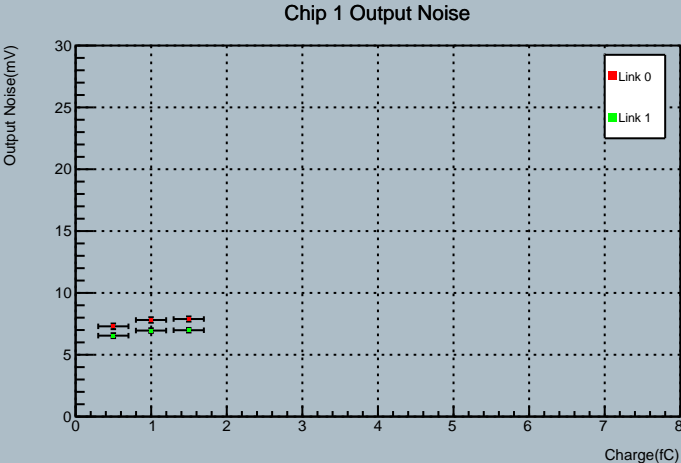
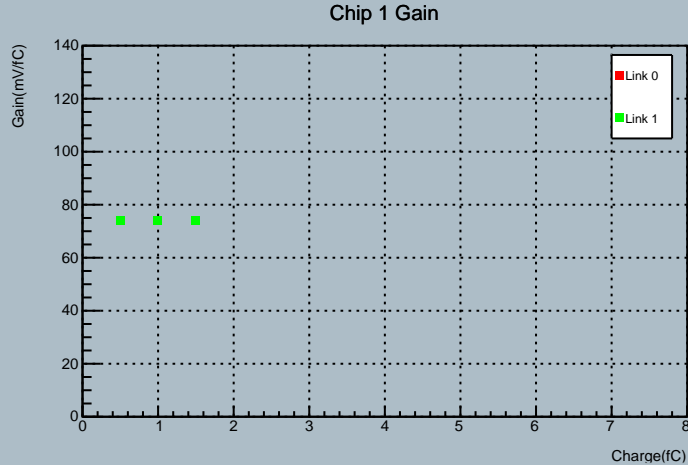
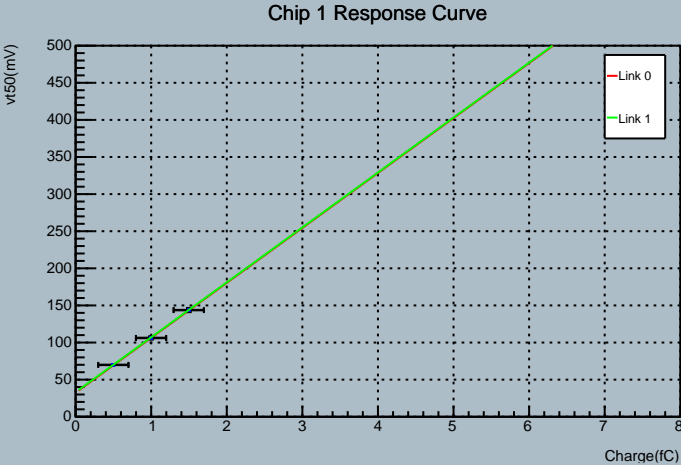


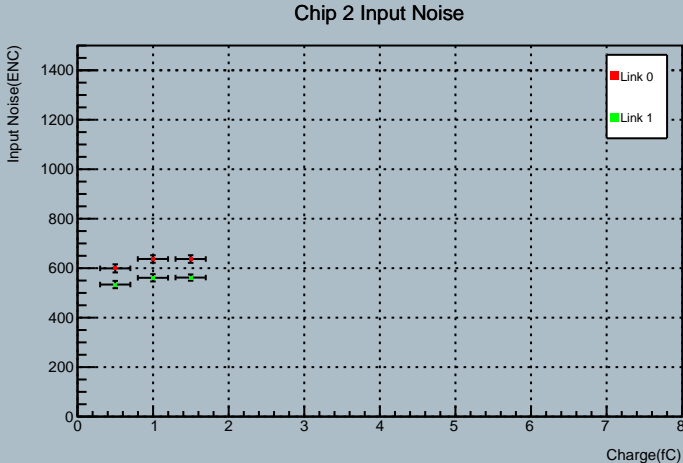
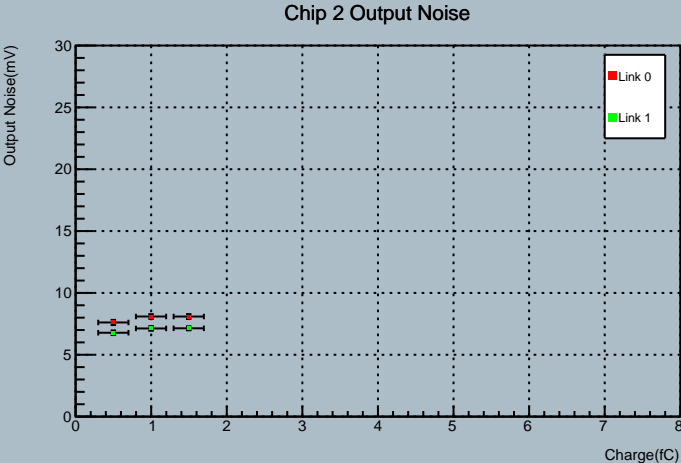
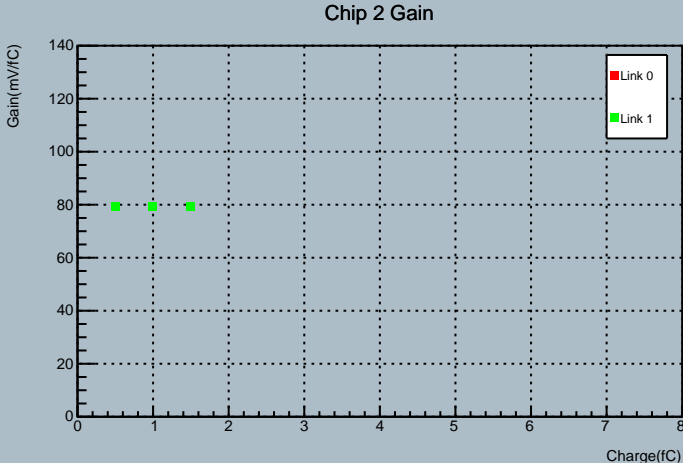
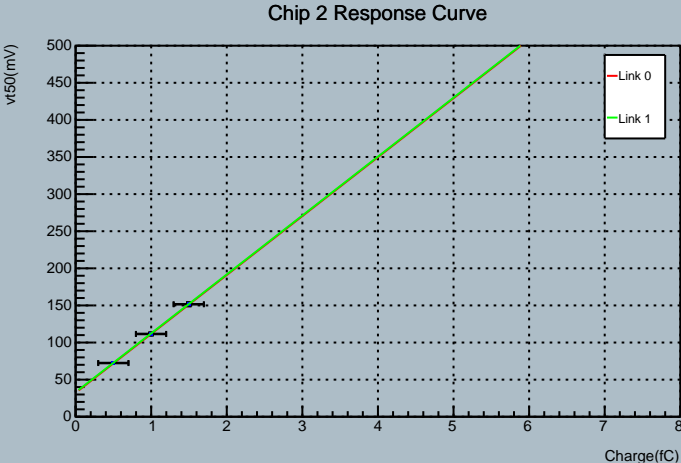
Chip 0 Output Noise



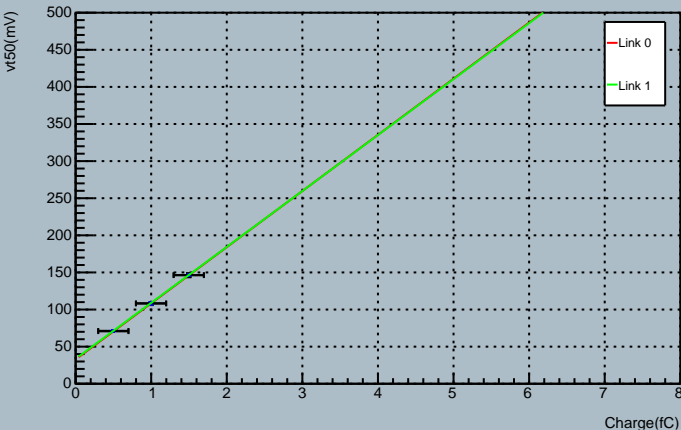
Chip 0 Input Noise



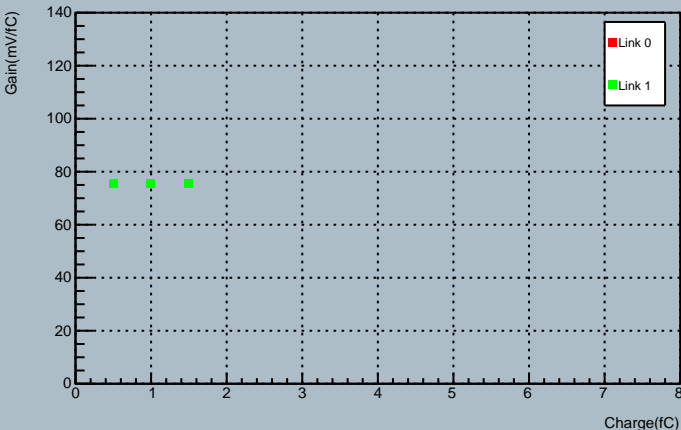




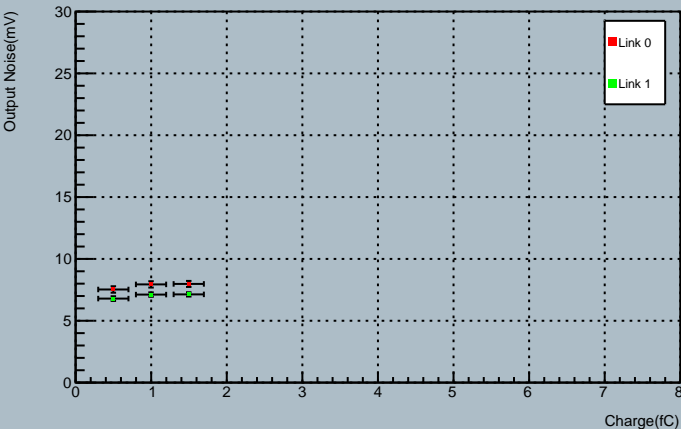
Chip 3 Response Curve



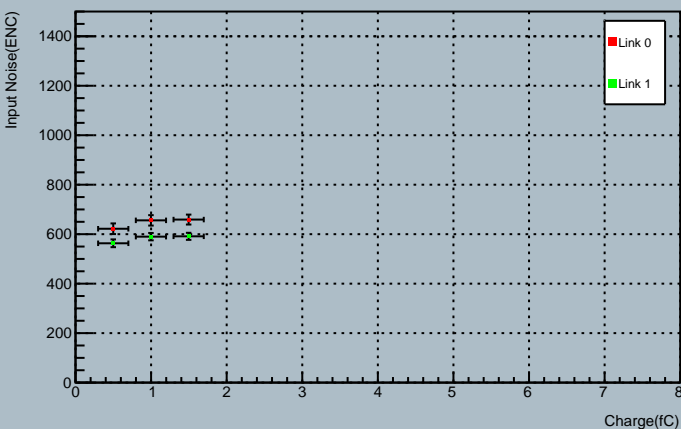
Chip 3 Gain



Chip 3 Output Noise

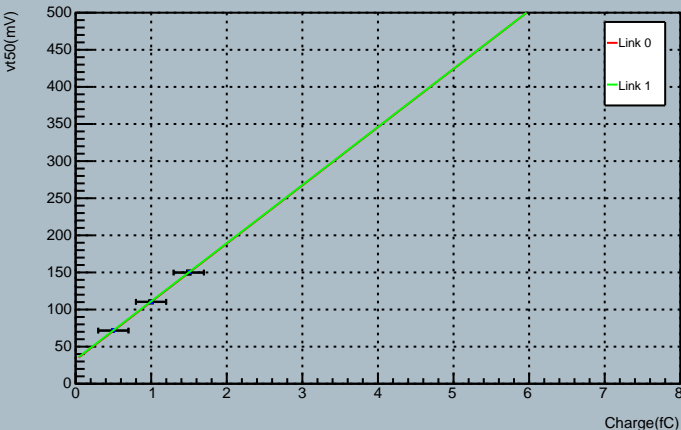


Chip 3 Input Noise

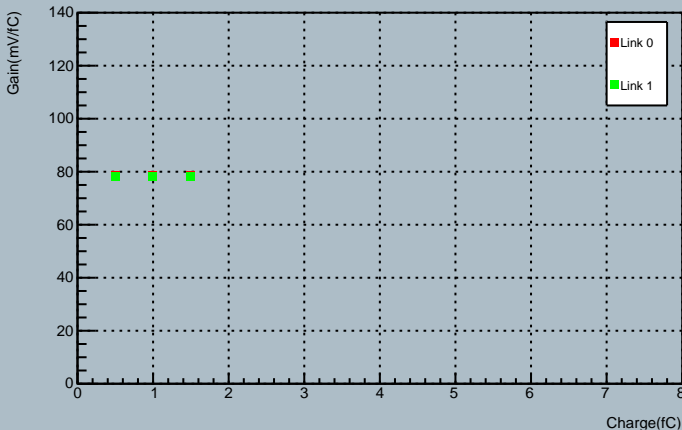




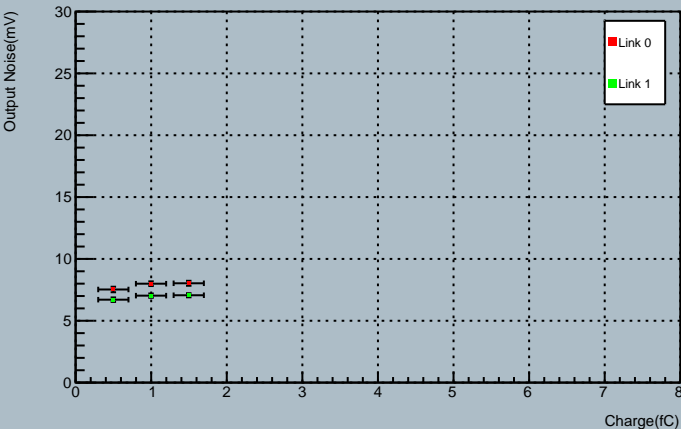
Chip 4 Response Curve



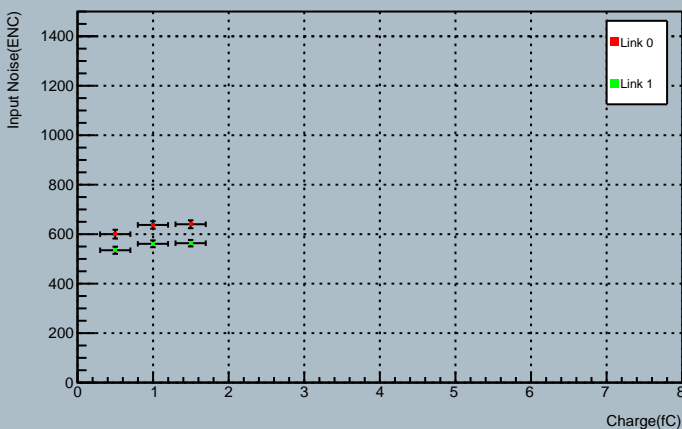
Chip 4 Gain



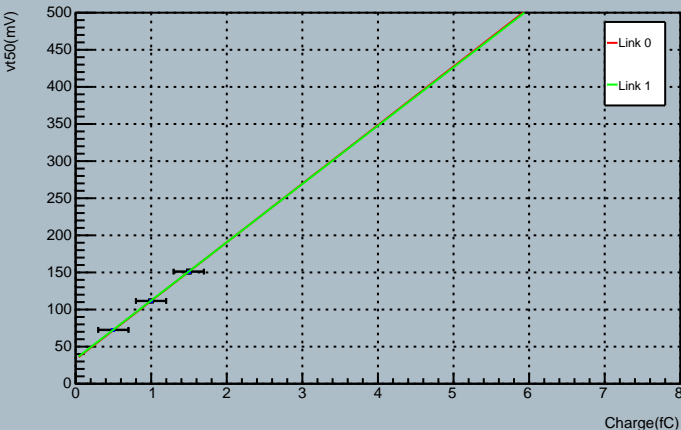
Chip 4 Output Noise



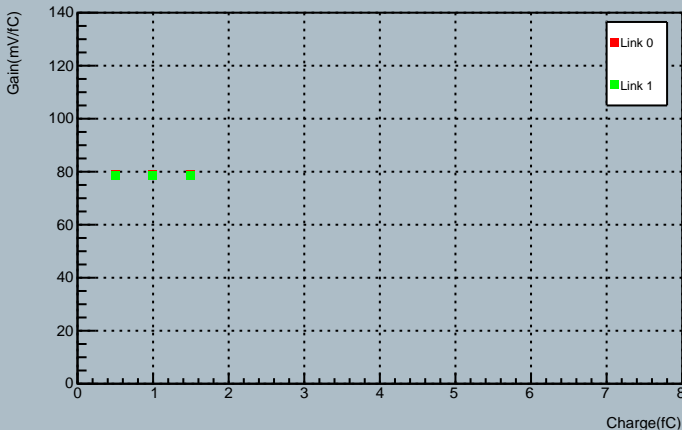
Chip 4 Input Noise



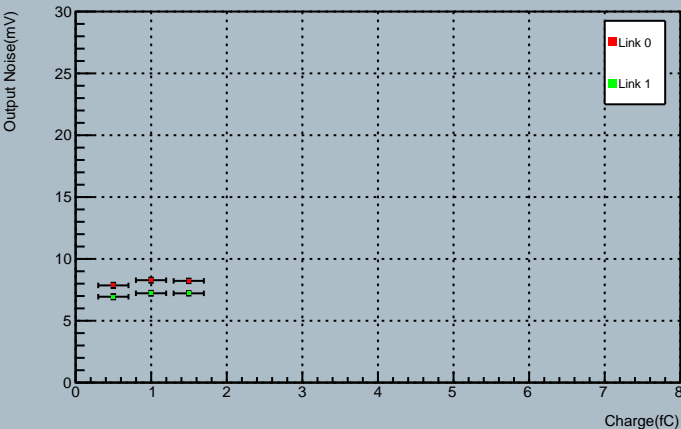
Chip 5 Response Curve



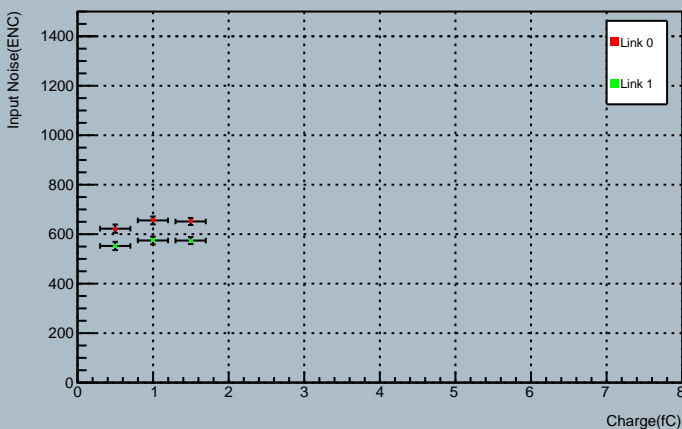
Chip 5 Gain



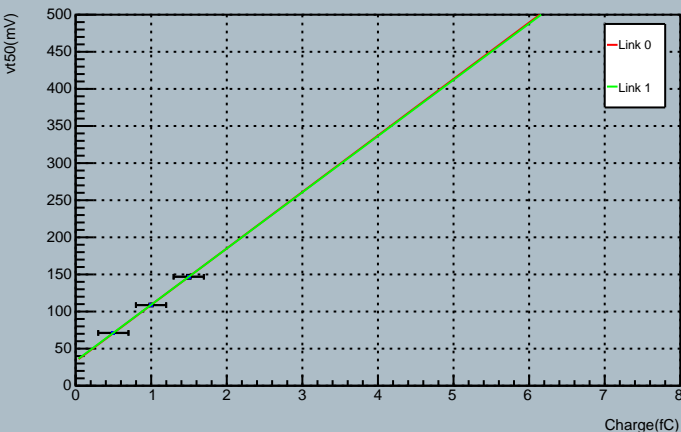
Chip 5 Output Noise



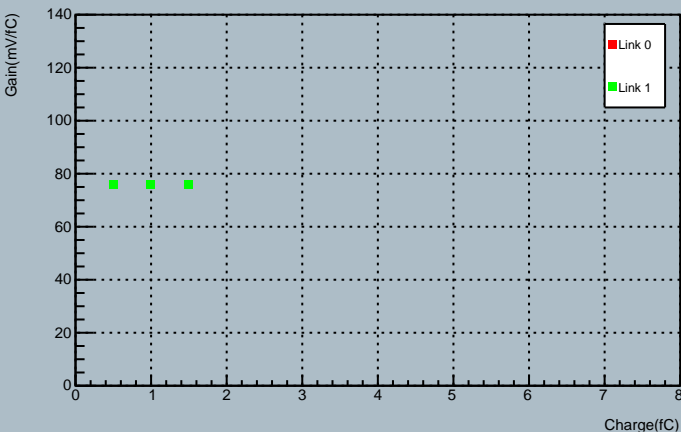
Chip 5 Input Noise



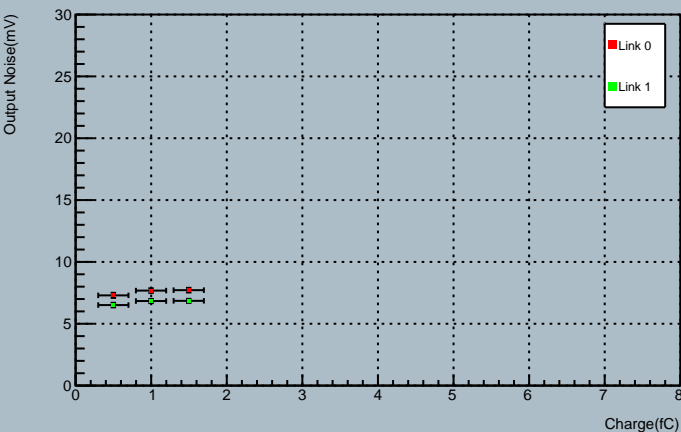
Chip 6 Response Curve



Chip 6 Gain



Chip 6 Output Noise



Chip 6 Input Noise

