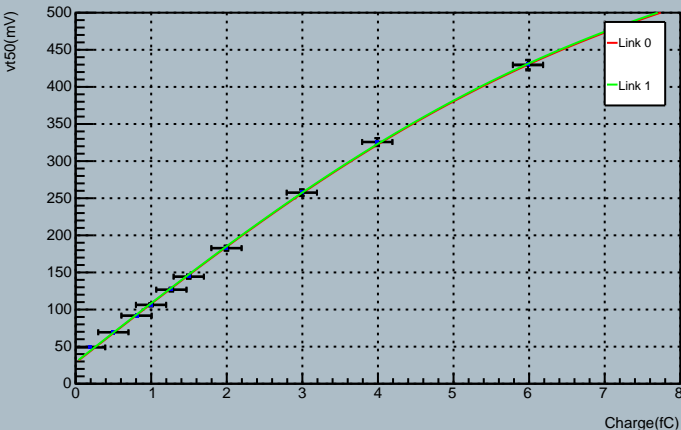
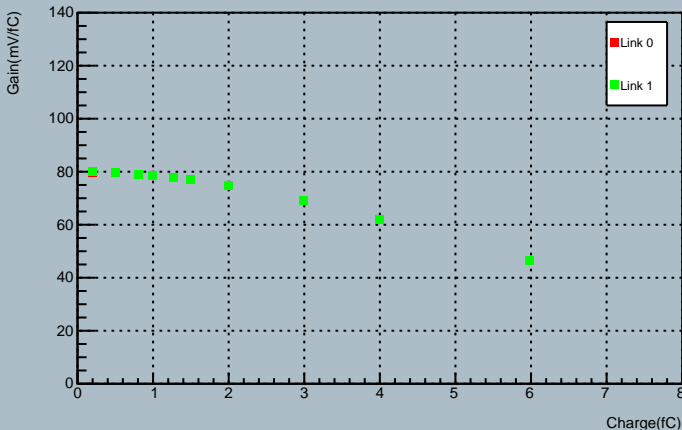


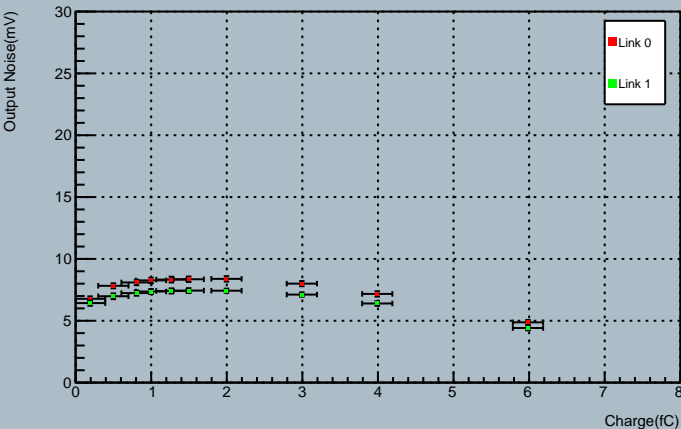
Chip 0 Response Curve



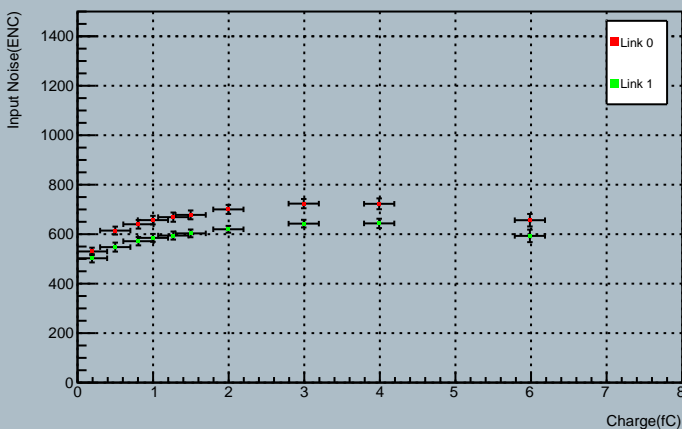
Chip 0 Gain

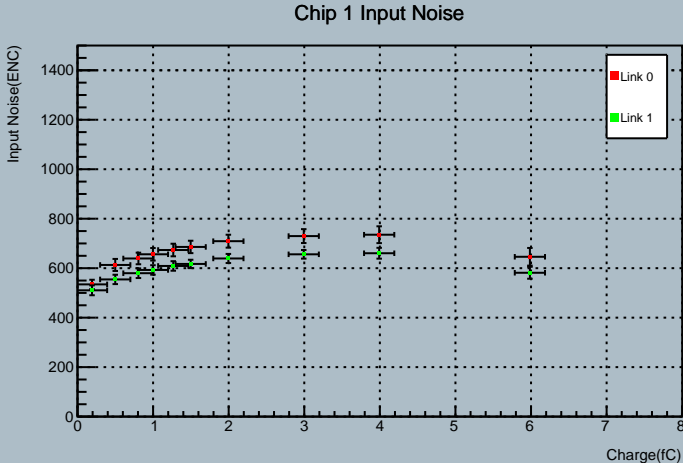
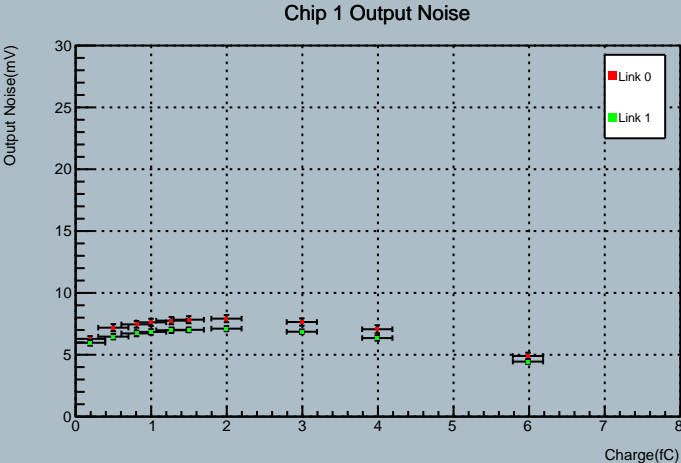
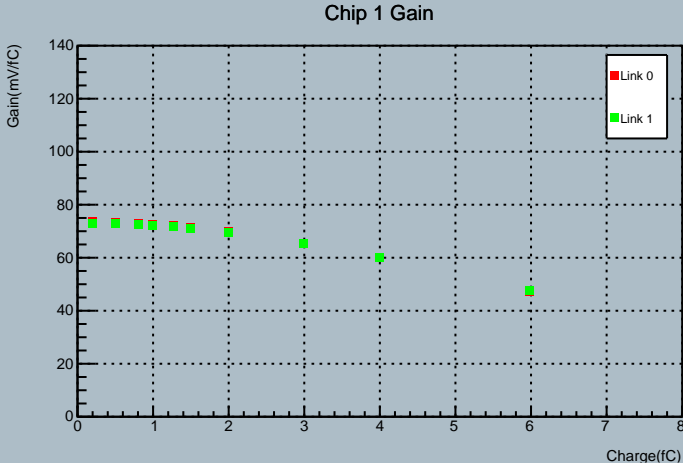
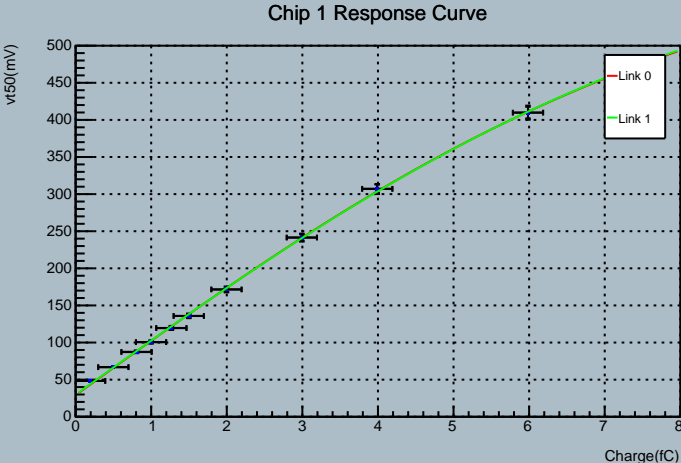


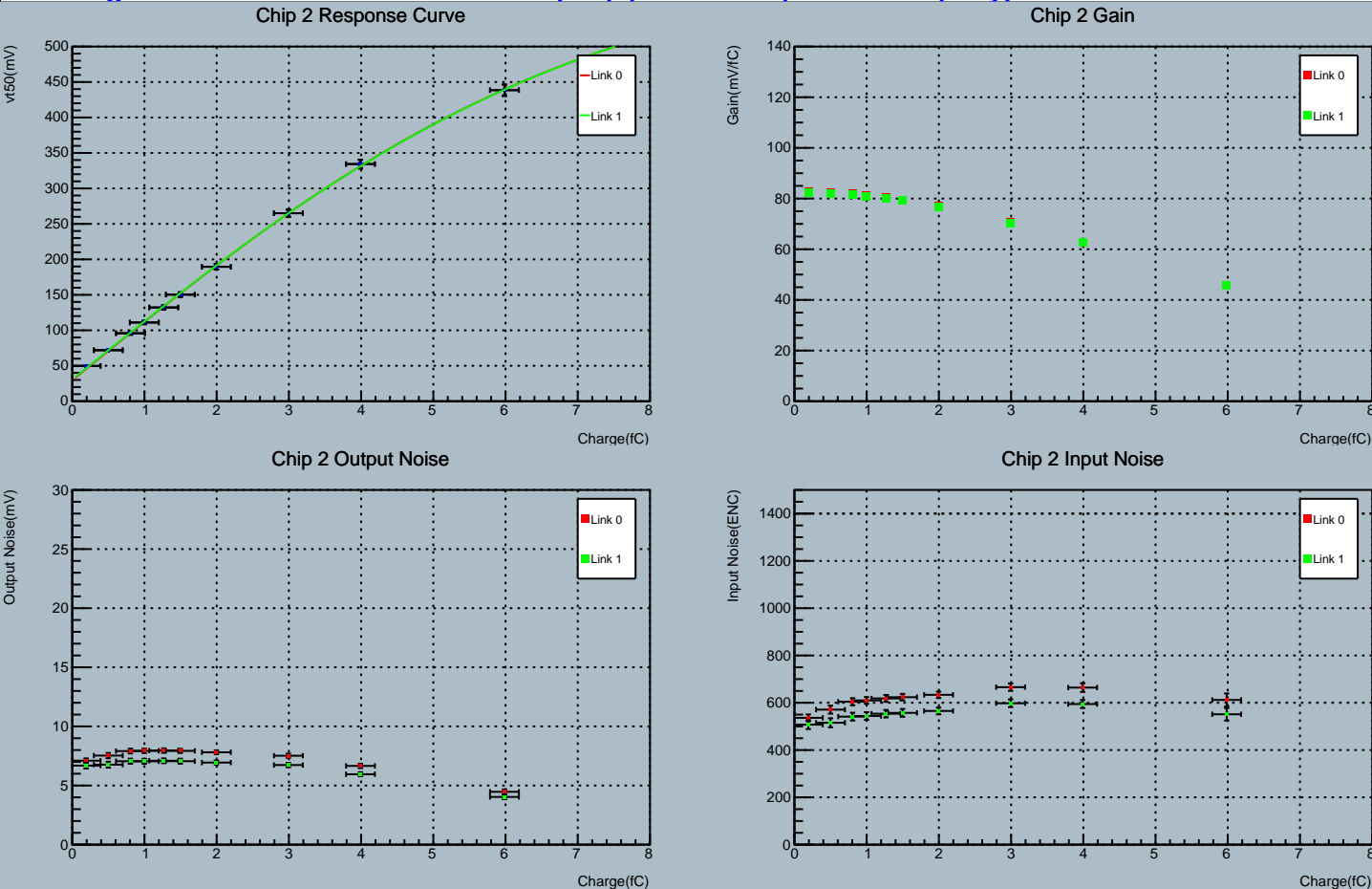
Chip 0 Output Noise

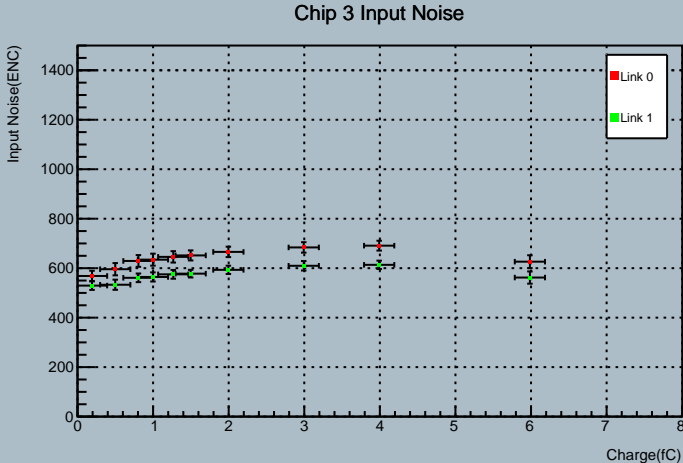
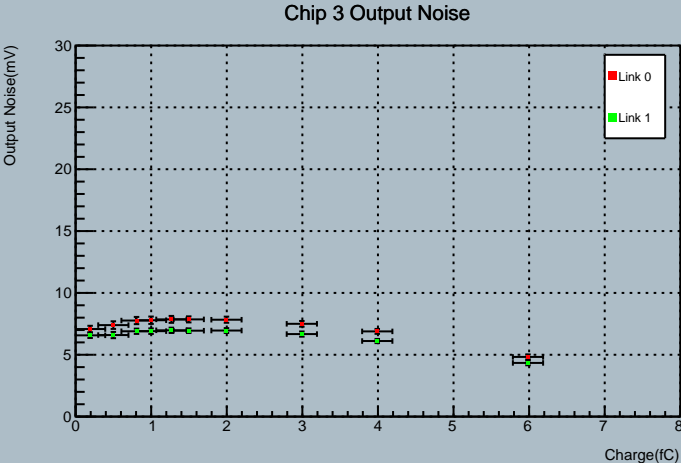
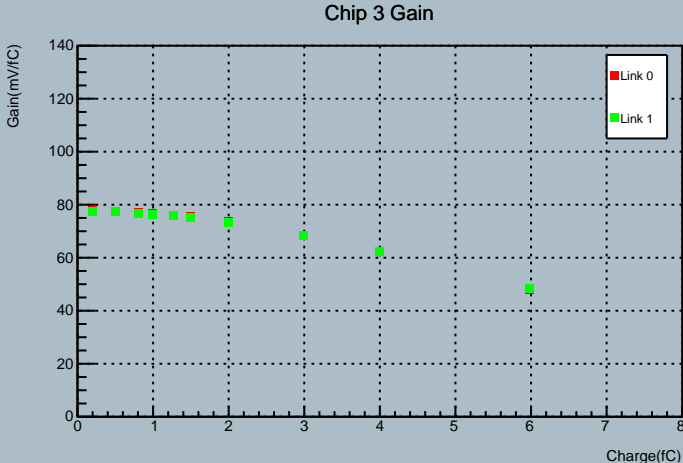
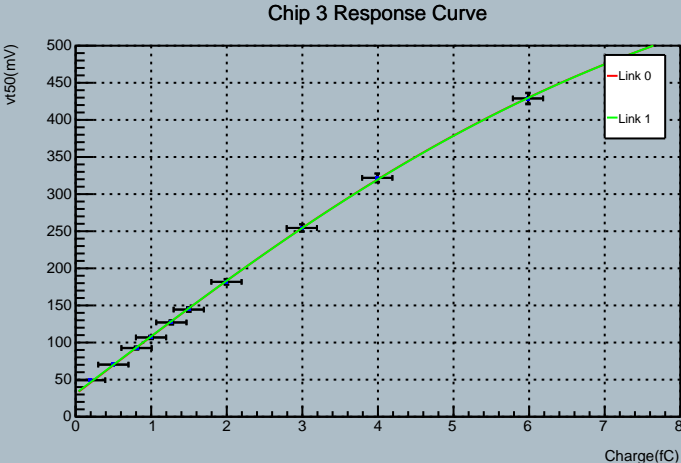


Chip 0 Input Noise

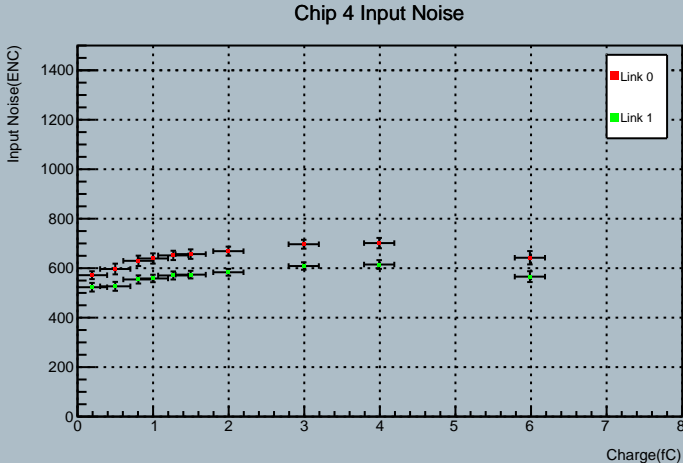
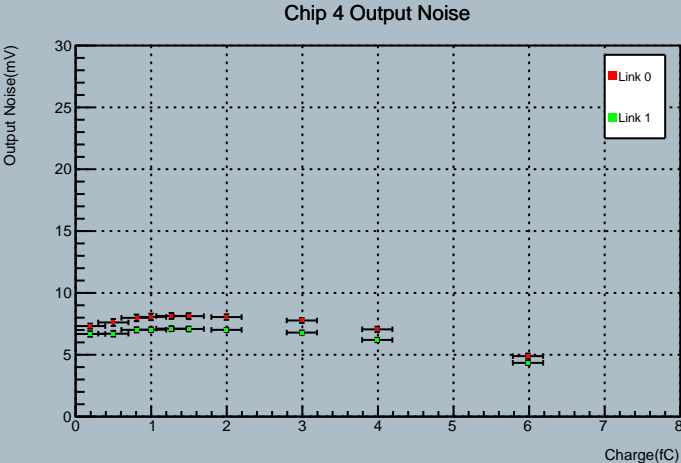
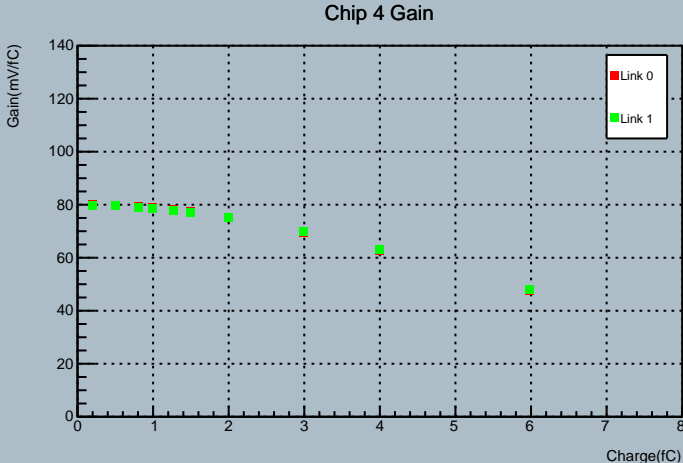
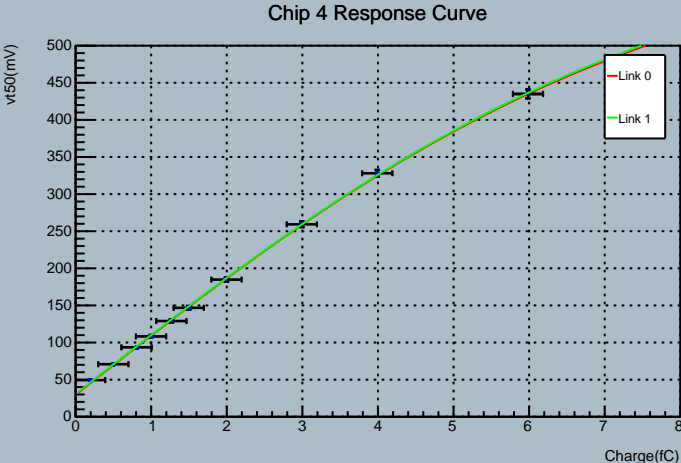




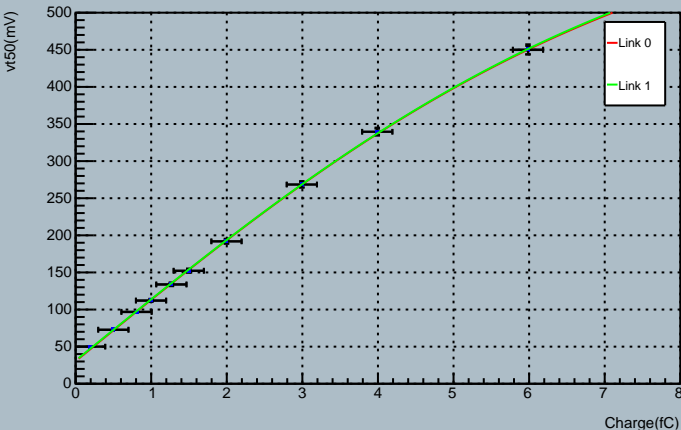




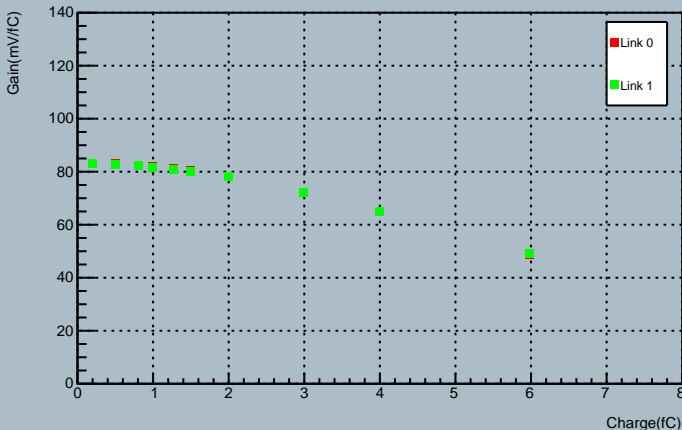




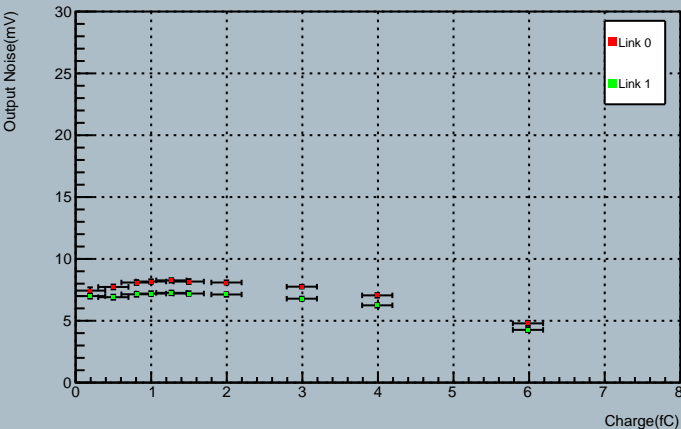
Chip 5 Response Curve



Chip 5 Gain



Chip 5 Output Noise



Chip 5 Input Noise

