1. What hardware component is responsible to the mapping of virtual address to physical address in the CPU?(6)
2. What is the situation where a set of processes are in a state of wait?(6)
3. \_\_\_\_\_\_\_\_\_\_ seeks the attention of the CPU and suspends the current processing of the CPU. (7)
4. ~r -> p <-> (p -> q) -> r . {p=1, r=1}
5. What is the A in ACID stands for? (2)
6. Which type of network is vulnerable to single point attack? (2)
7. Sql is a language used to manage \_\_\_\_\_\_\_? (2)
8. What TCP stands for? (13)
9. Physical Layer is the first layer of protocol architecture model. (0010001)
10. Fjasdfjkl sdfasdfas errtiojkl;gad (13h)
11. Which software development model uses minimal planning and favours rapid developing? (1)
12. Which gate(s) can be use by itself multiple times to produce every other gates?(1)
13. Stack = [a,g,y,e,q,a,u,e,a,j,l,c]. letter=stack.pop() letter=stack.pop() letter=stack.pop() letter=stack.pop() letter=stack.pop() stack.push(t) stack.push(p) letter=stack.pop()

What letter equal to?

1. Chr(109). (Python)
2. Write the requirement and the answer. (need above to answer this one)

Your teacher name

1. Memory management unit
2. Deadlock
3. Interrupt
4. True (T)
5. Atomicity
6. Centralized
7. Data
8. Transmission control protocol
9. 17
10. 19
11. Rapid application development
12. Nor/Nand
13. a
14. m