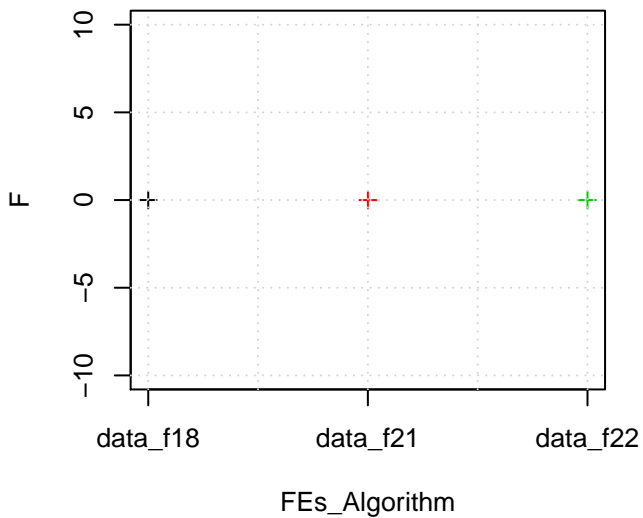
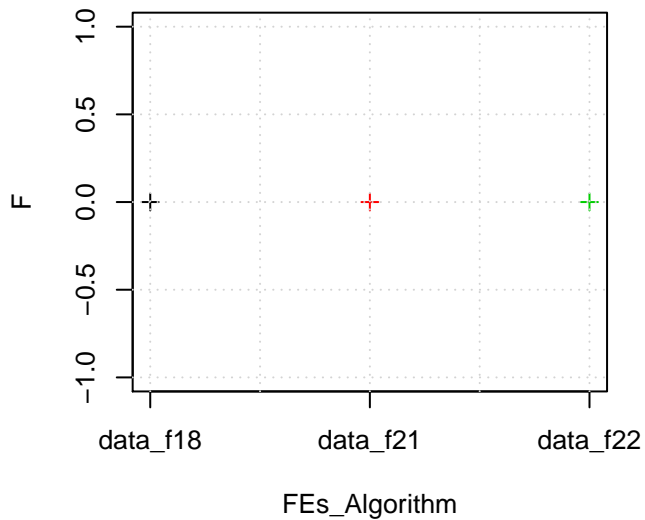


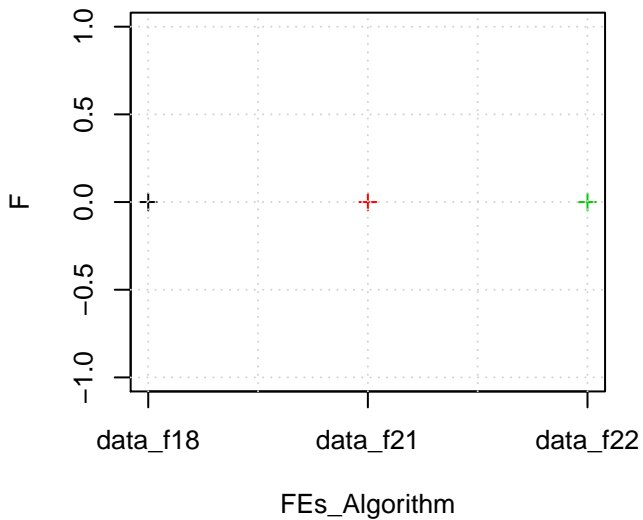
**DIM2-ga100-a**



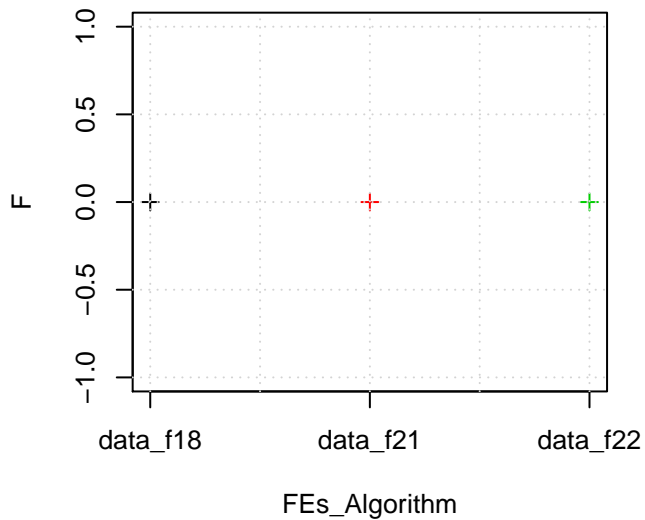
**DIM2-ga100-b**



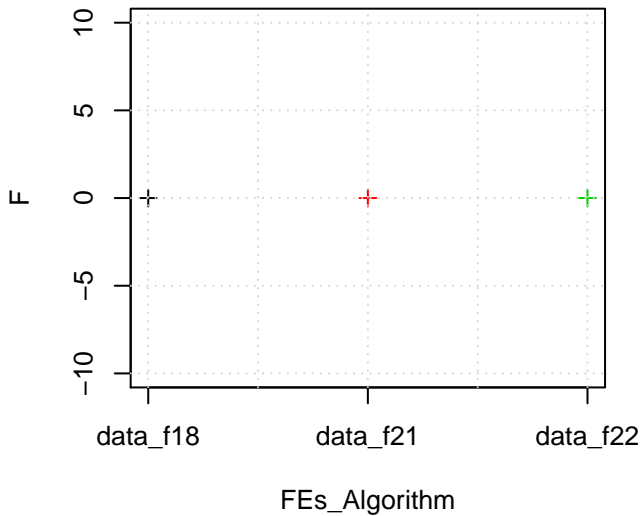
**DIM2-ga100-c**



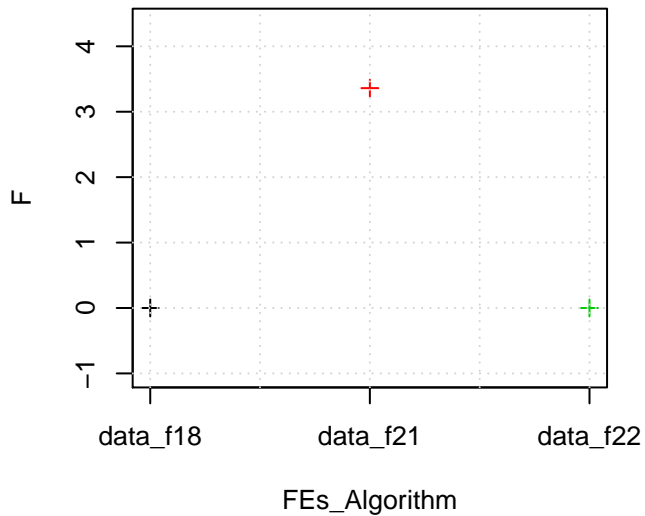
**DIM2-ga100-d**



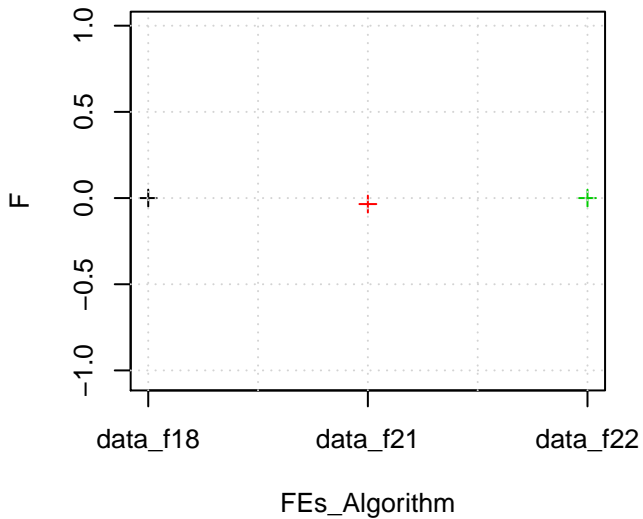
**DIM2-hill-a**



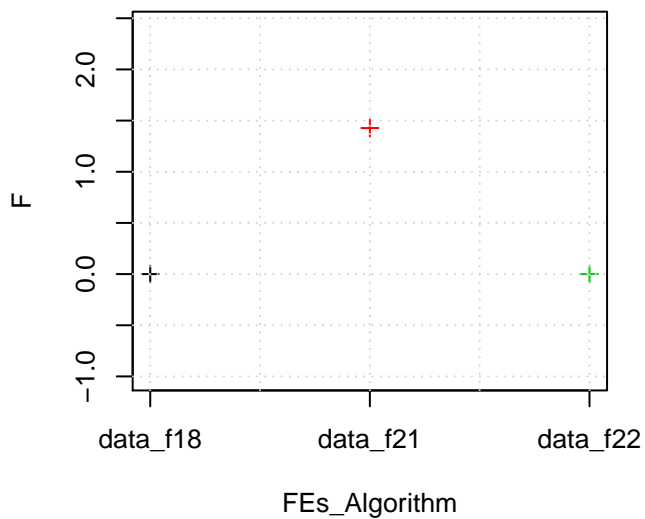
**DIM2-hill-b**



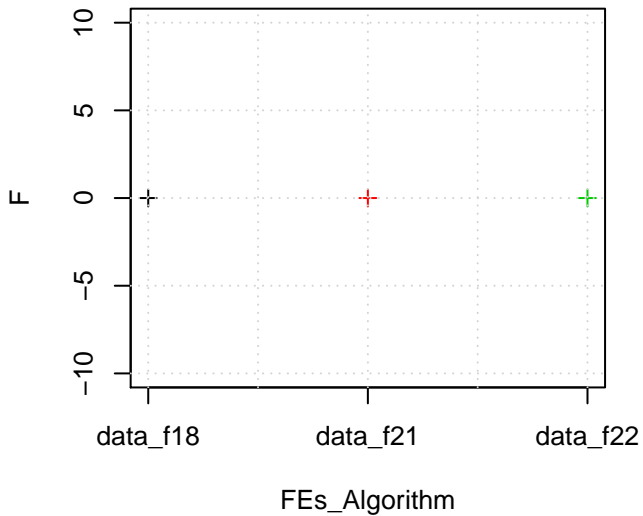
**DIM2-hill-c**



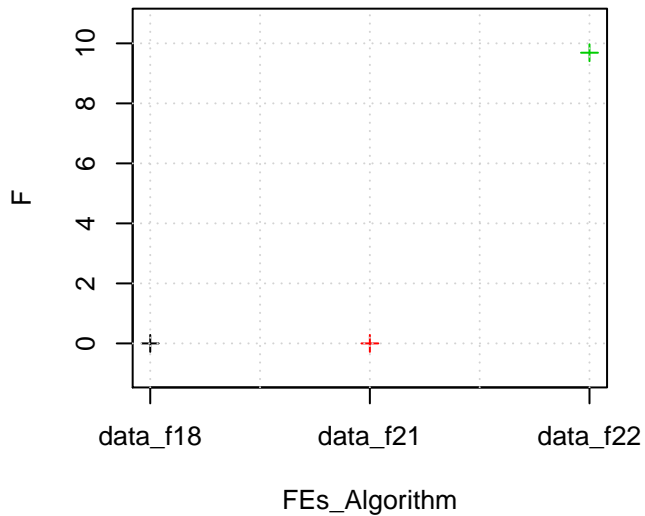
**DIM2-hill-d**



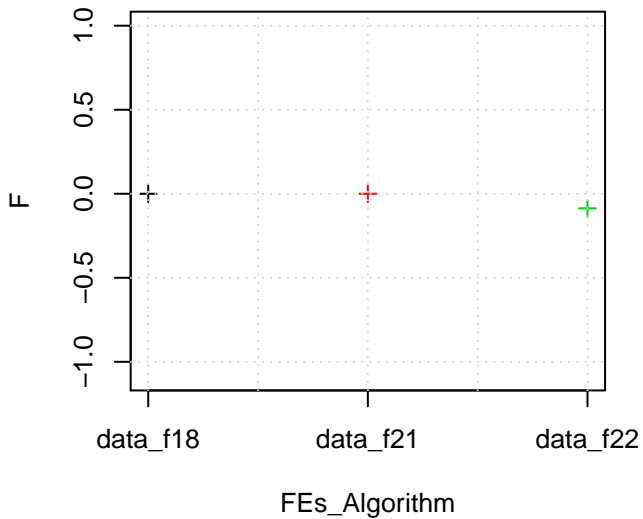
**DIM2-CMAES-a**



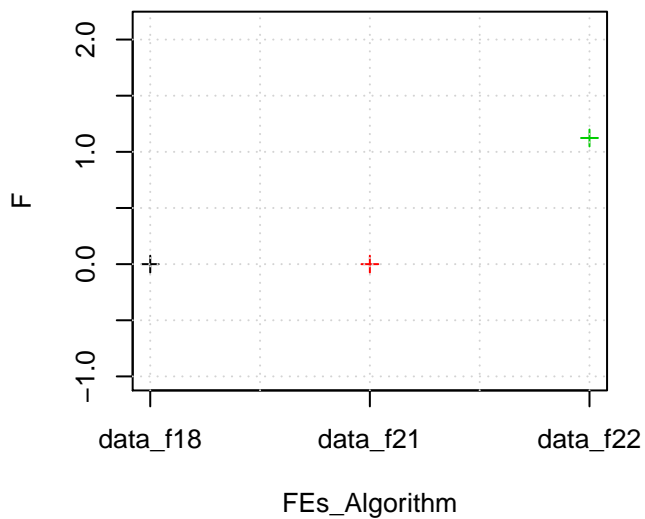
**DIM2-CMAES-b**



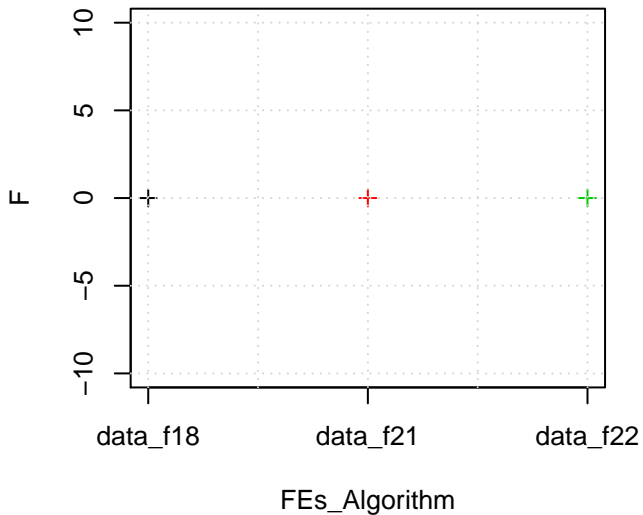
**DIM2-CMAES-c**



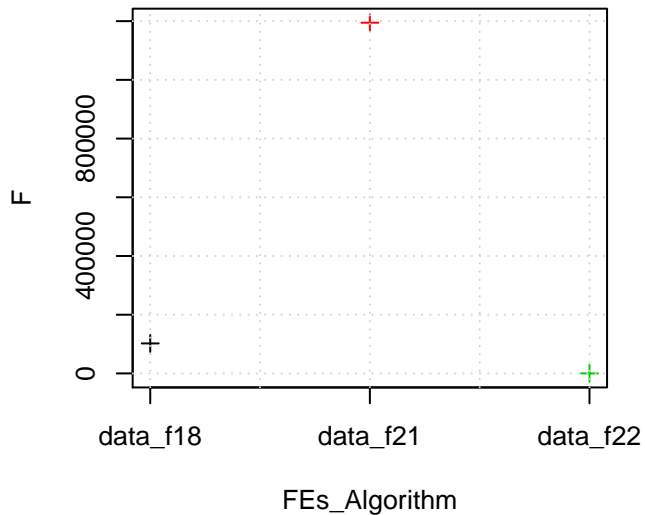
**DIM2-CMAES-d**



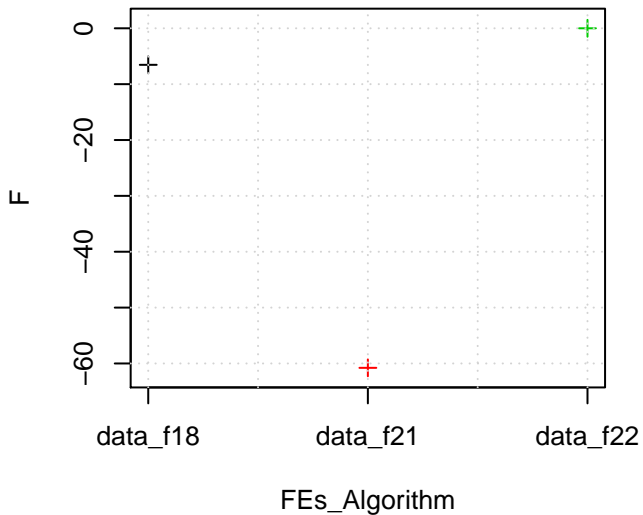
**DIM2-IPOP-a**



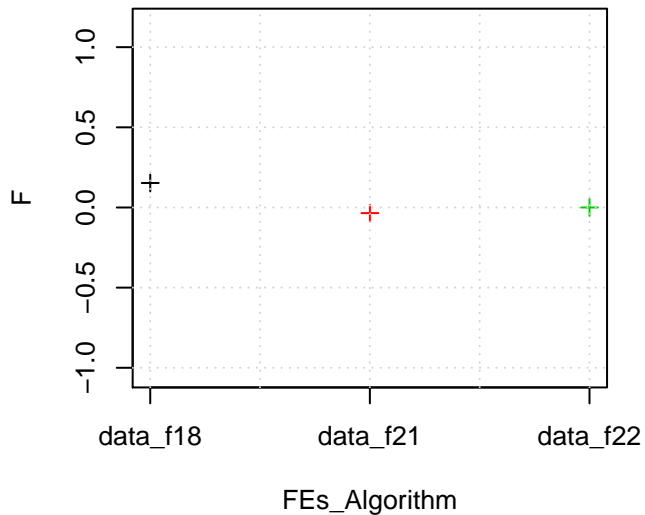
**DIM2-IPOP-b**



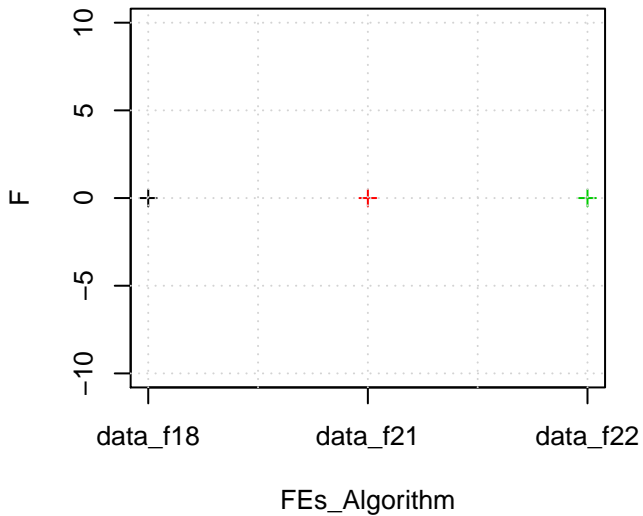
**DIM2-IPOP-c**



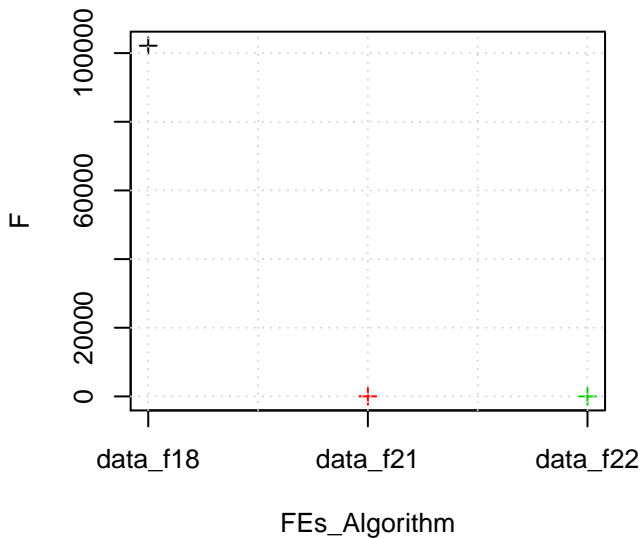
**DIM2-IPOP-d**



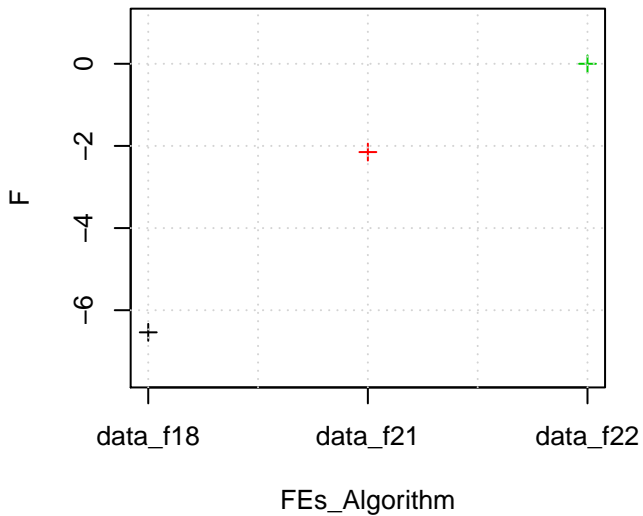
**DIM2-IPOP-500-a**



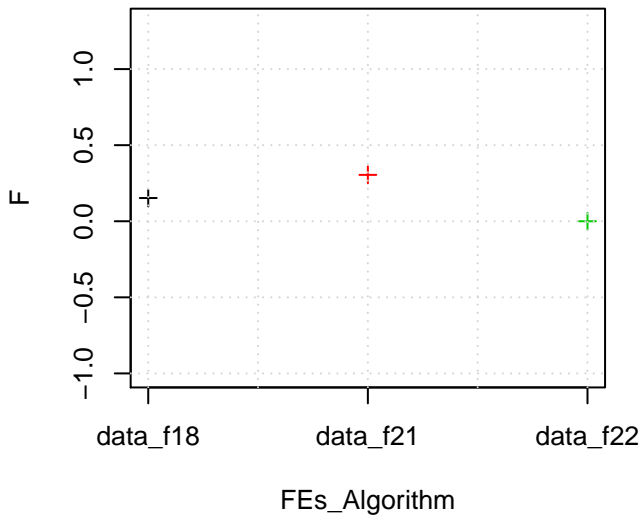
**DIM2-IPOP-500-b**



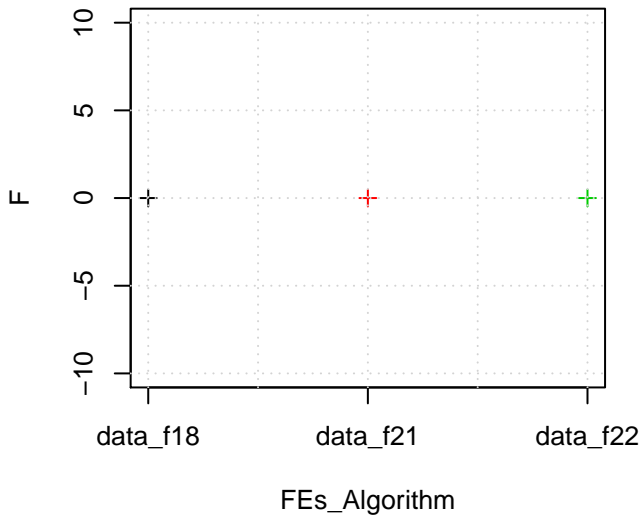
**DIM2-IPOP-500-c**



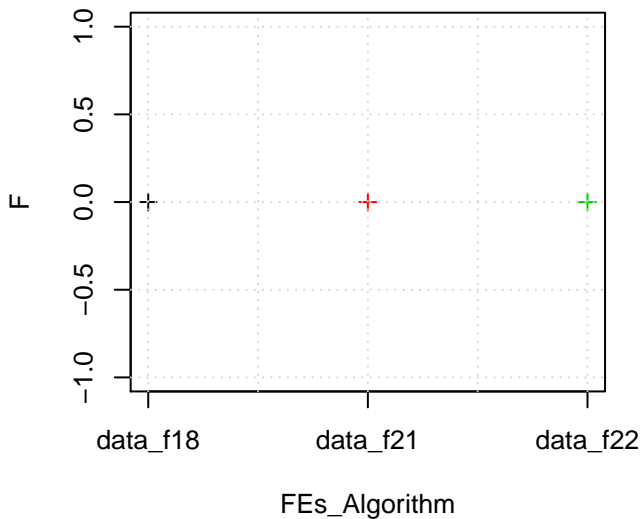
**DIM2-IPOP-500-d**



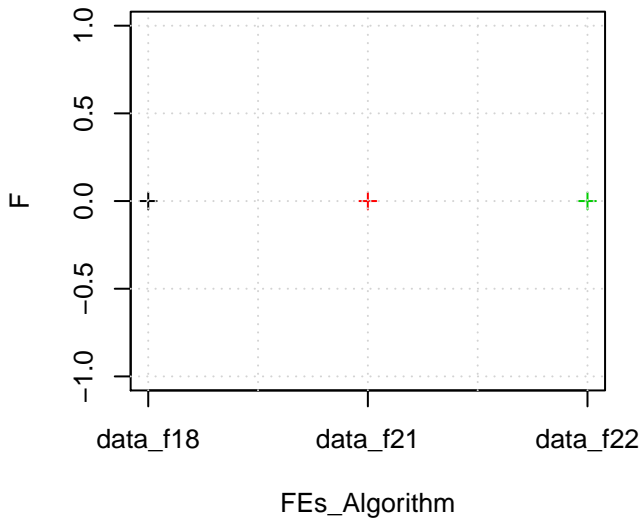
**DIM2-IPOP-tany-a**



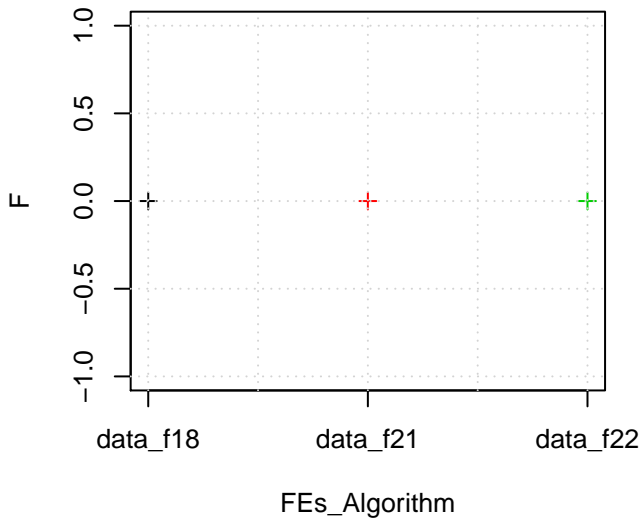
**DIM2-IPOP-tany-b**



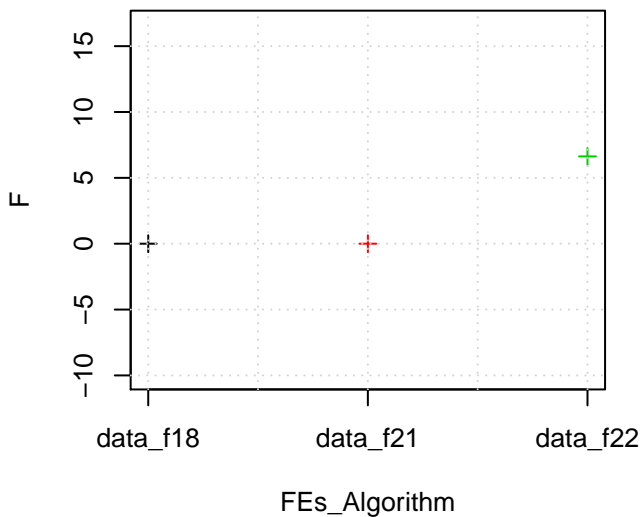
**DIM2-IPOP-tany-c**



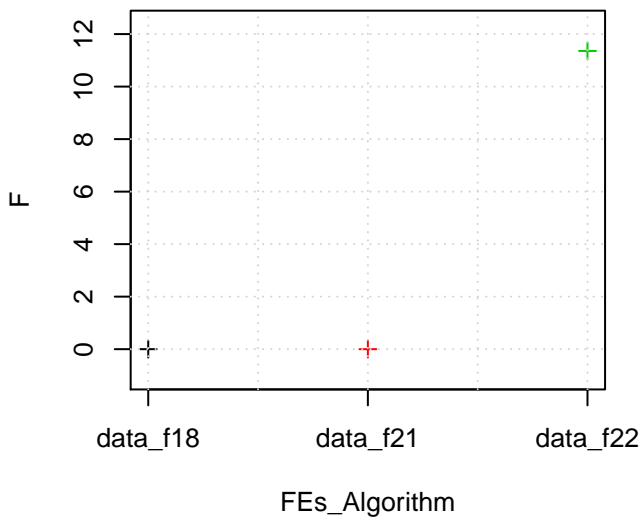
**DIM2-IPOP-tany-d**



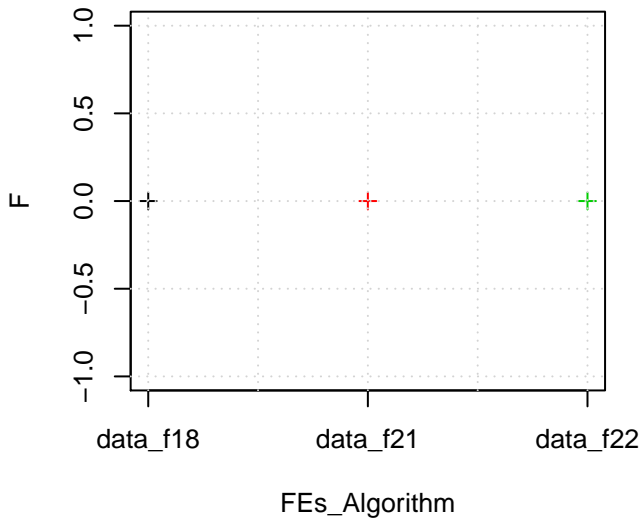
**DIM2-IPOP-texp-a**



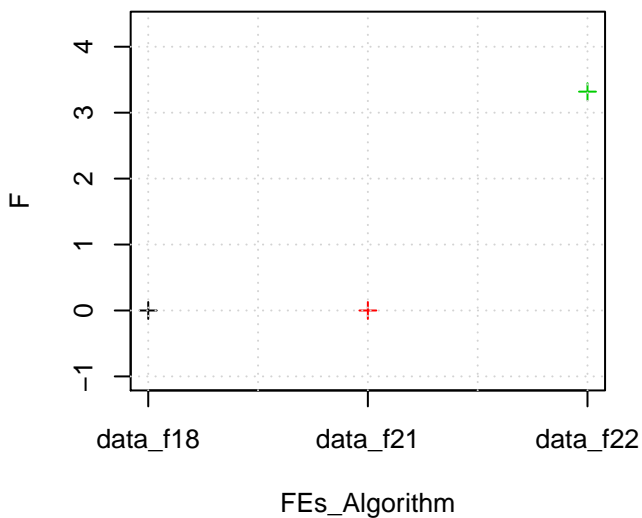
**DIM2-IPOP-texp-b**



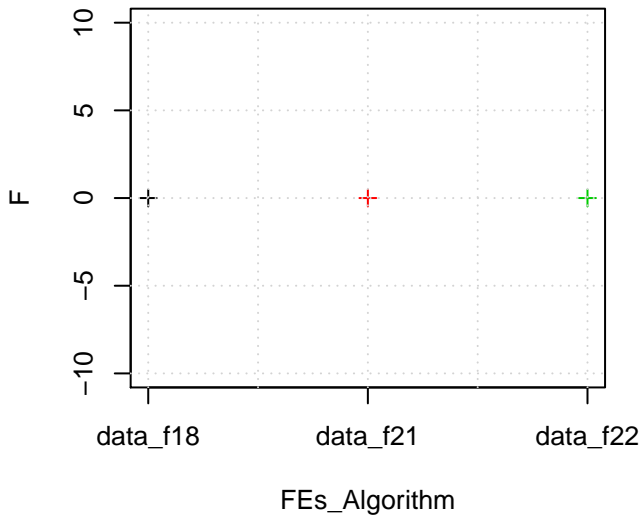
**DIM2-IPOP-texp-c**



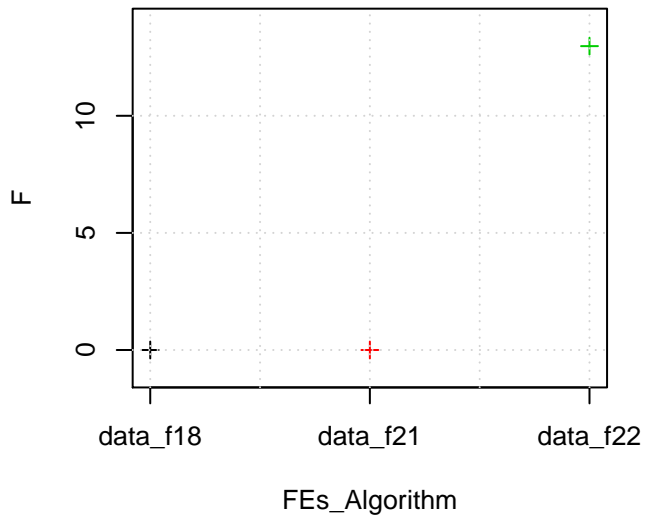
**DIM2-IPOP-texp-d**



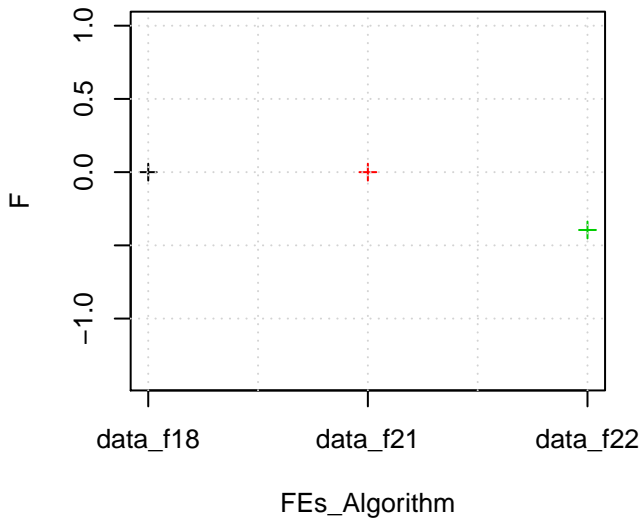
**DIM2-P-DCN-a**



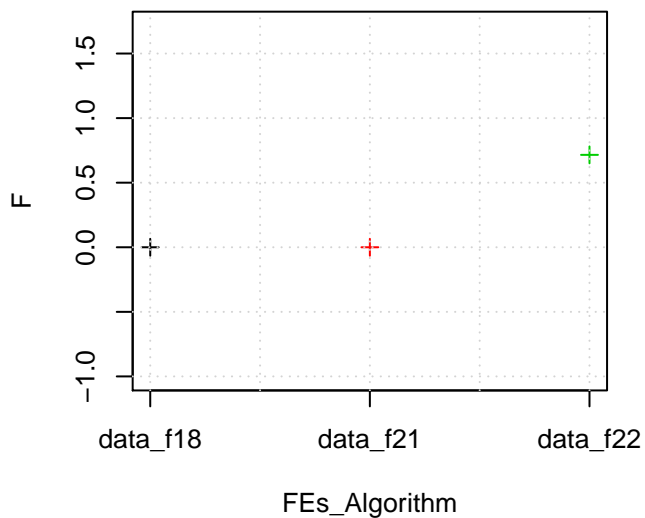
**DIM2-P-DCN-b**



**DIM2-P-DCN-c**

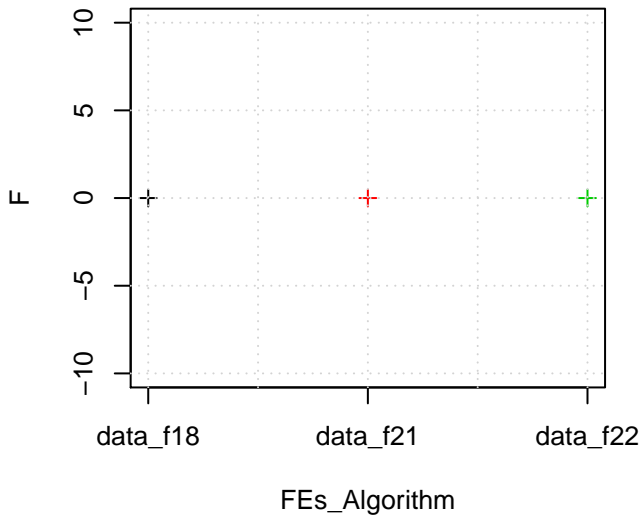


**DIM2-P-DCN-d**

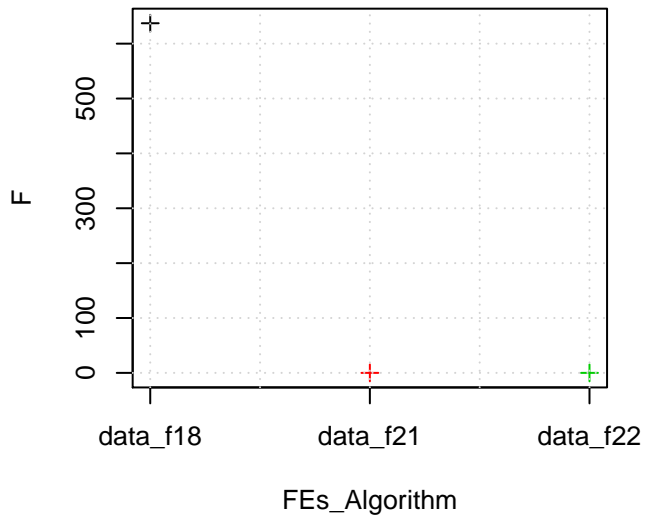




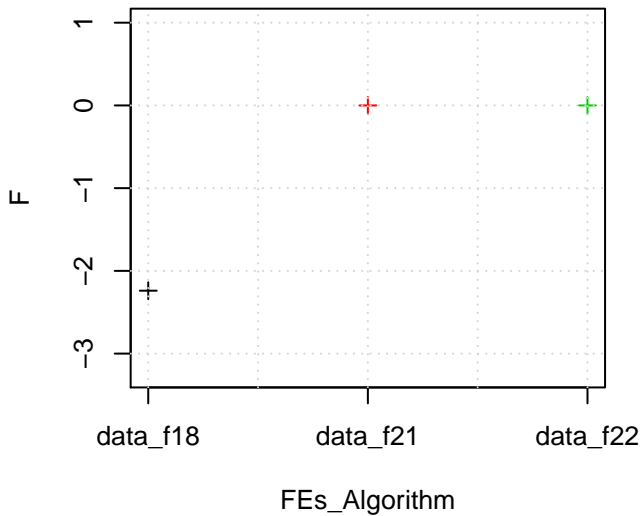
**DIM2-DE-a**



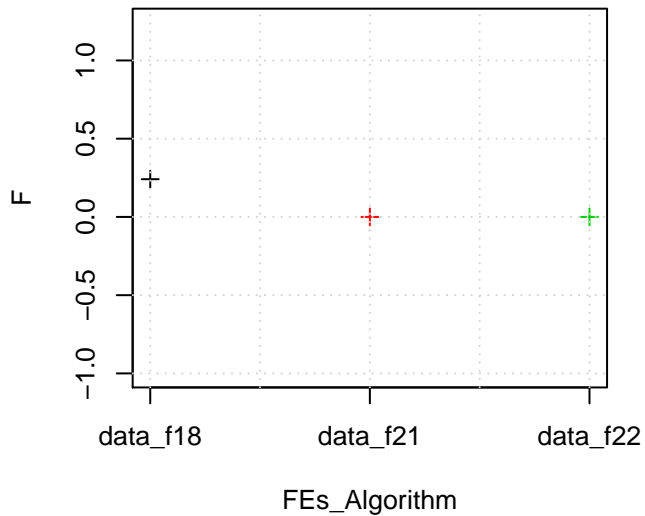
**DIM2-DE-b**



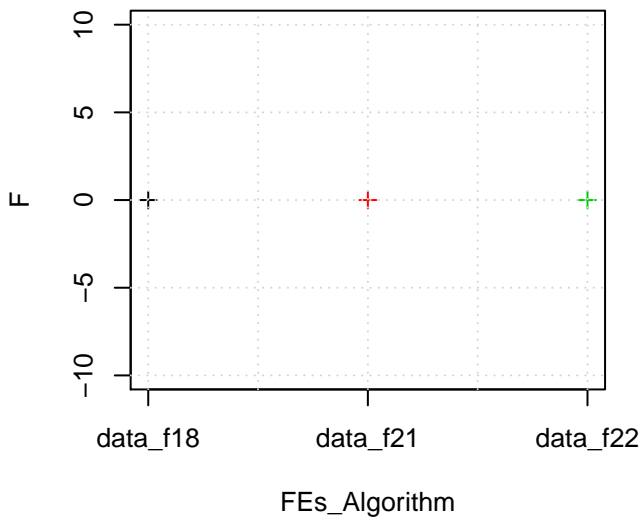
**DIM2-DE-c**



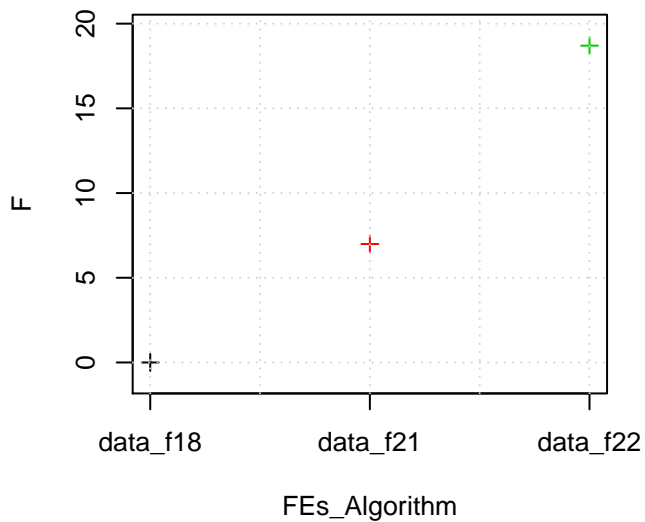
**DIM2-DE-d**



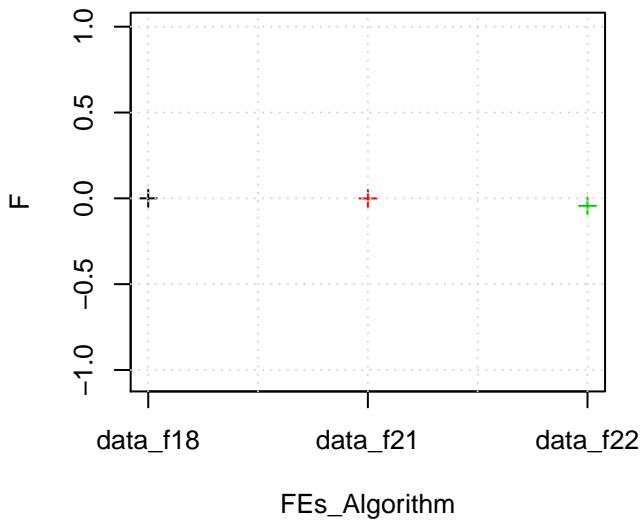
**DIM2-fmincon-a**



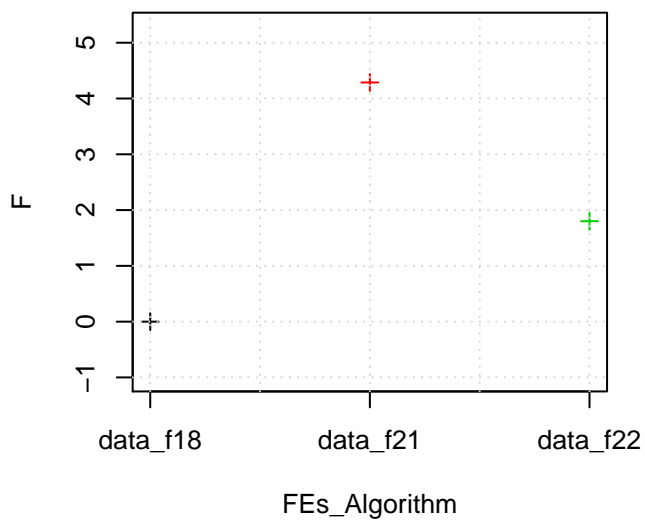
**DIM2-fmincon-b**



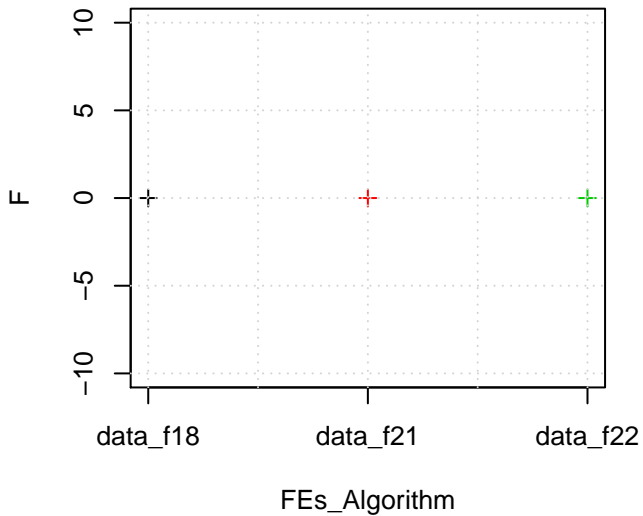
**DIM2-fmincon-c**



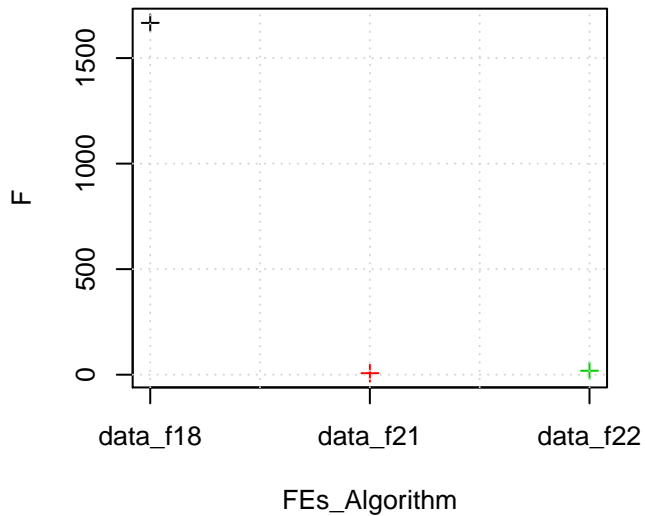
**DIM2-fmincon-d**



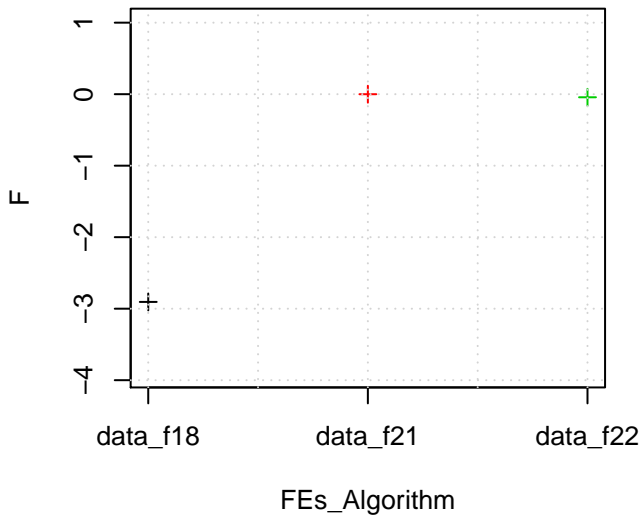
**DIM2-HMLSL-a**



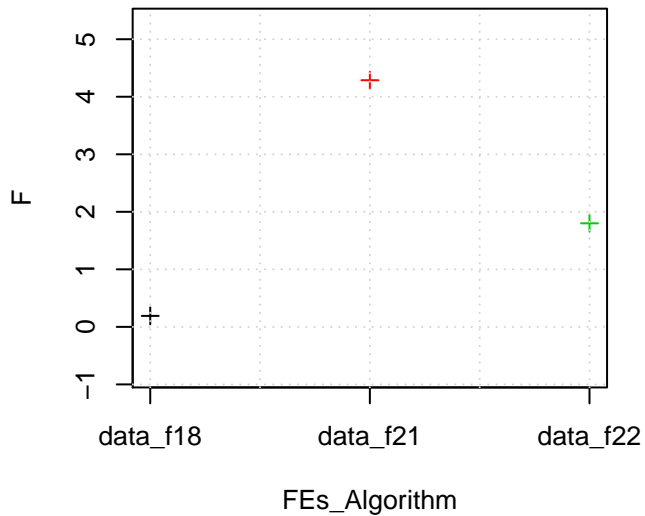
**DIM2-HMLSL-b**



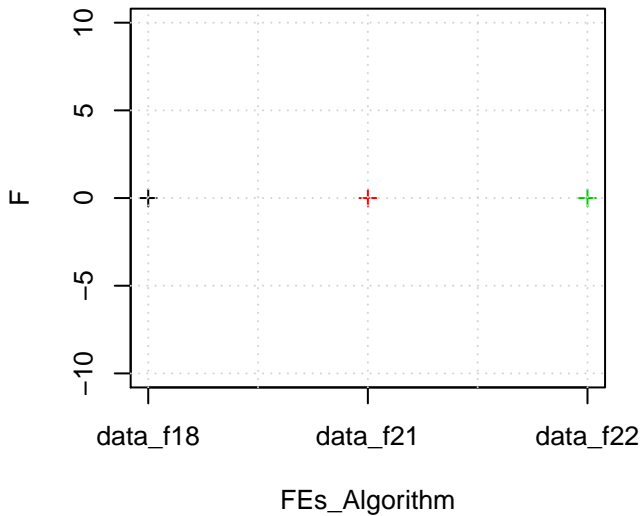
**DIM2-HMLSL-c**



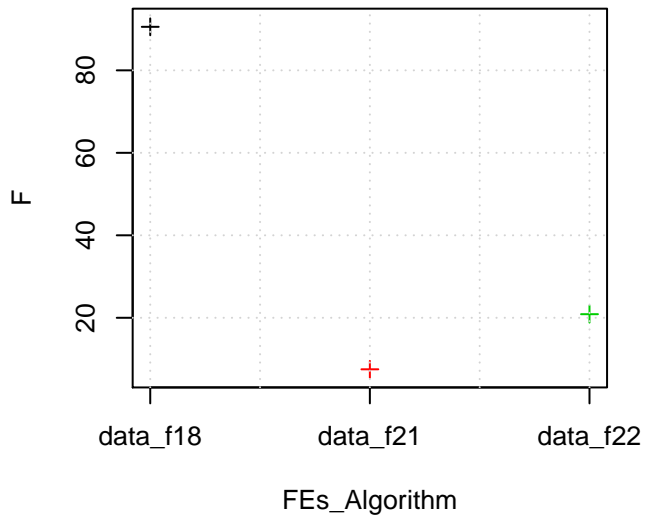
**DIM2-HMLSL-d**



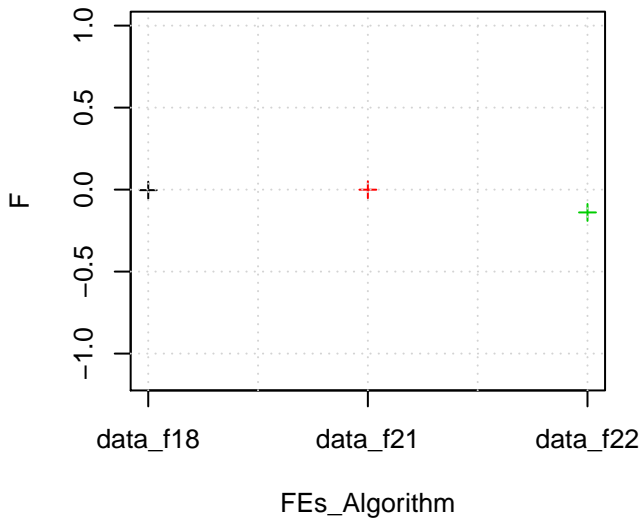
**DIM2-simplex-a**



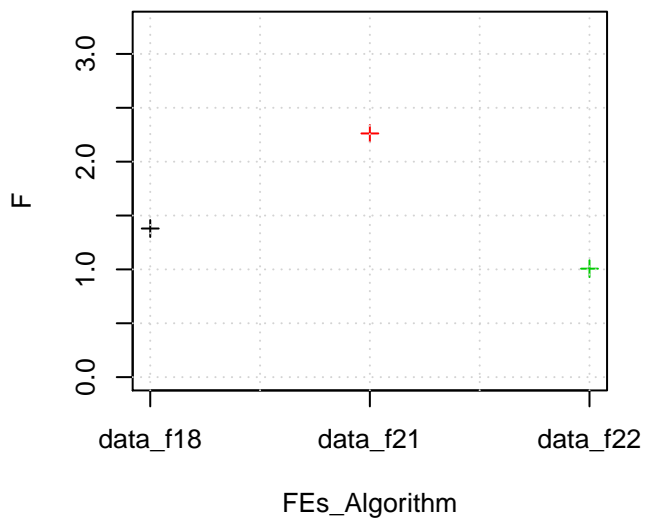
**DIM2-simplex-b**



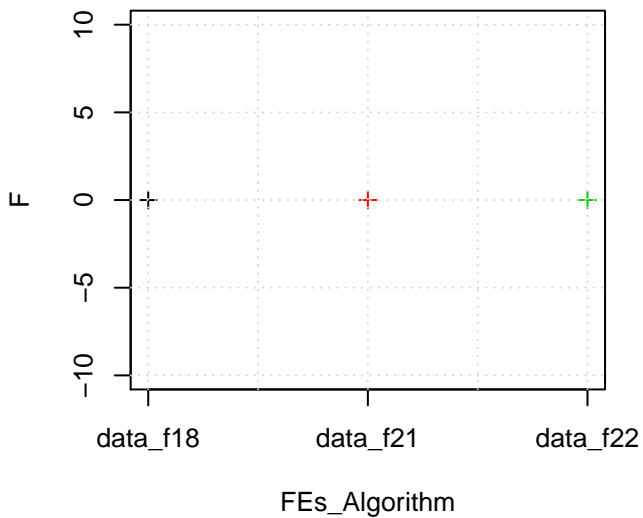
**DIM2-simplex-c**



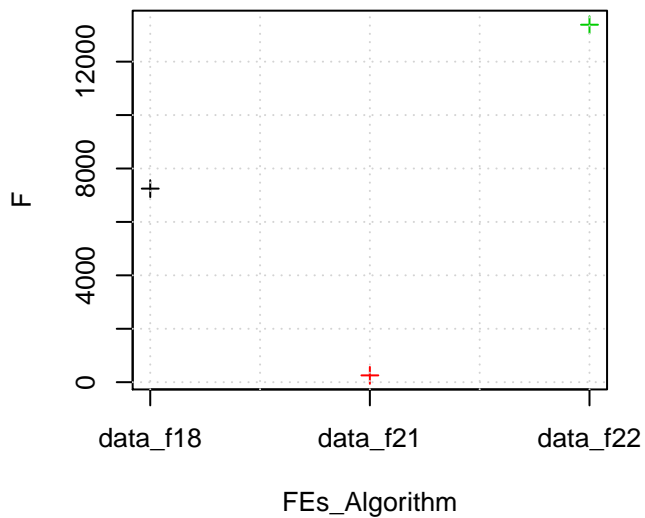
**DIM2-simplex-d**



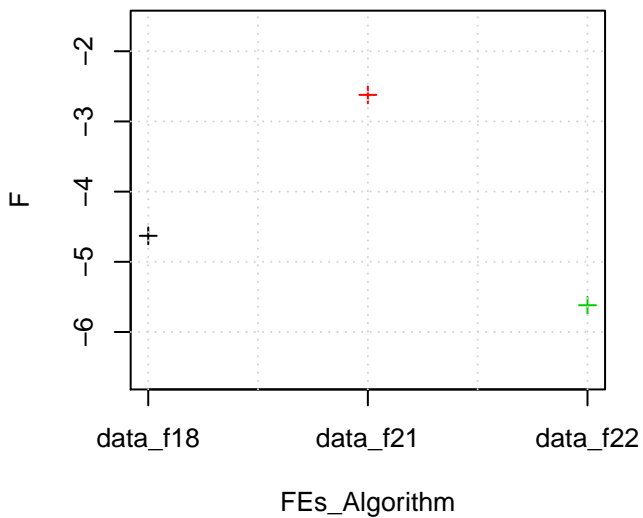
**DIM3-ga100-a**



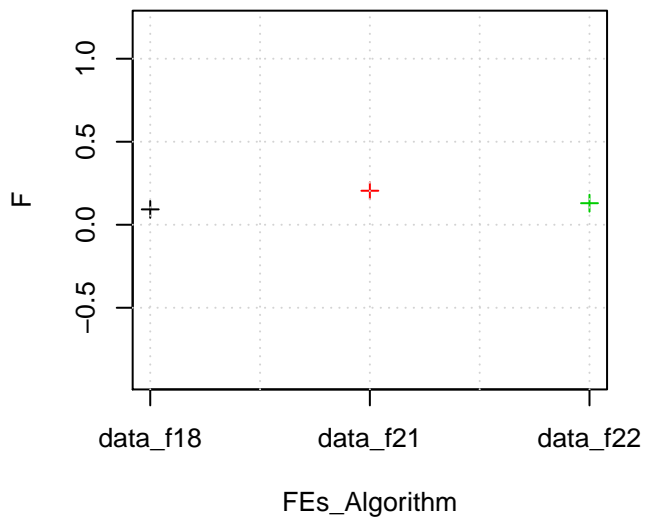
**DIM3-ga100-b**



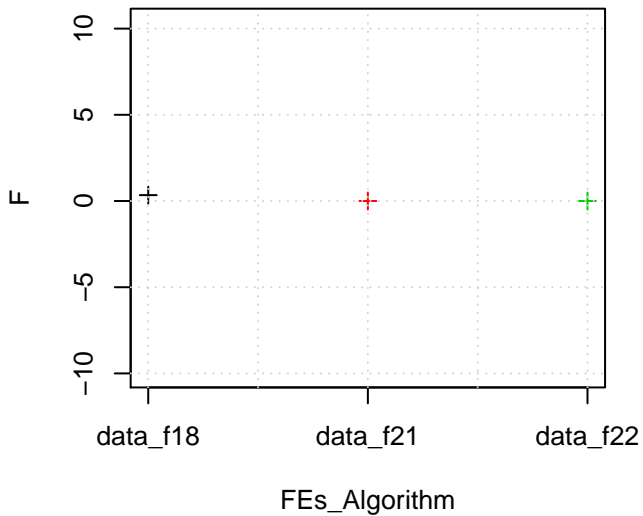
**DIM3-ga100-c**



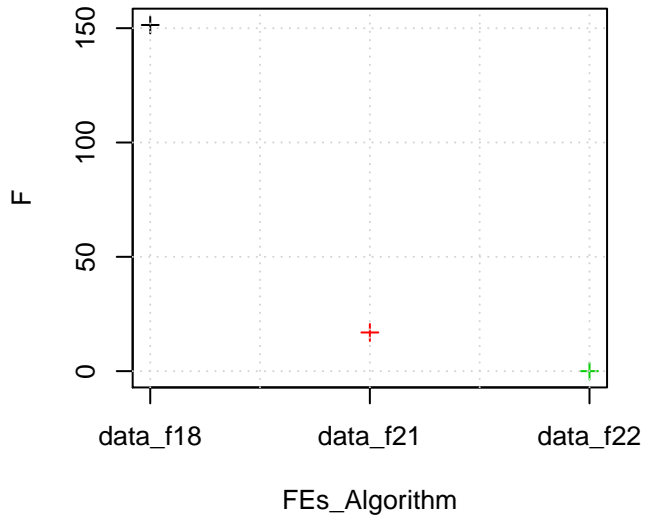
**DIM3-ga100-d**



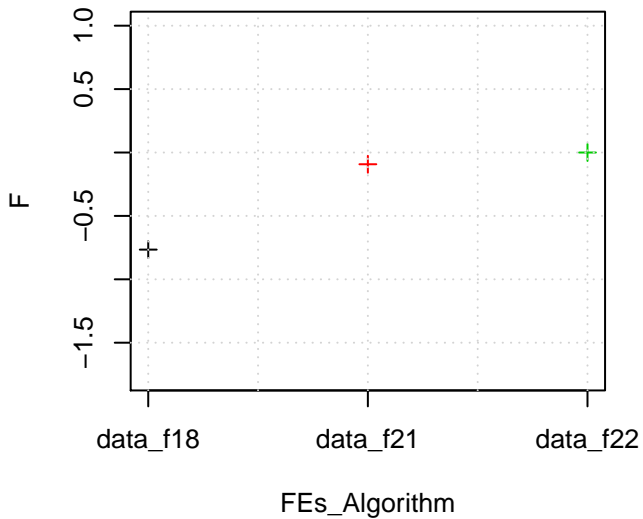
**DIM3-hill-a**



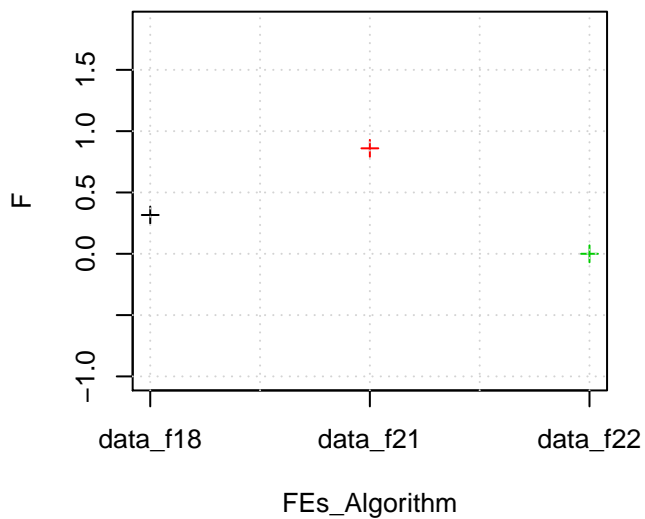
**DIM3-hill-b**



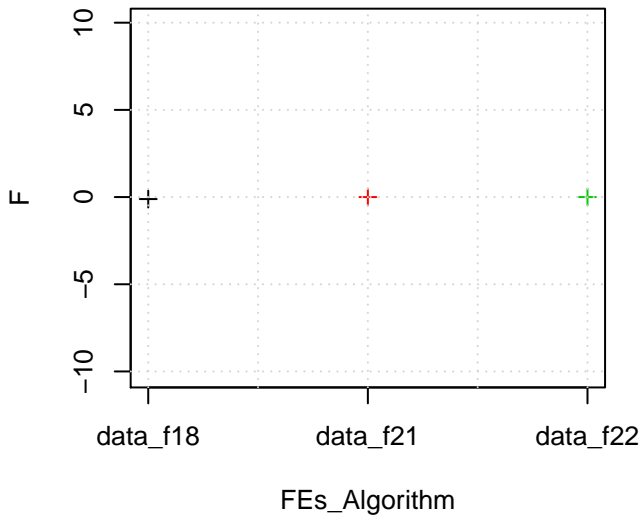
**DIM3-hill-c**



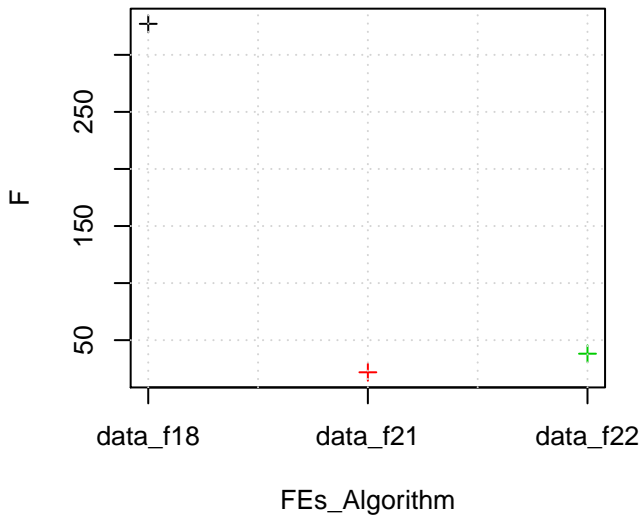
**DIM3-hill-d**



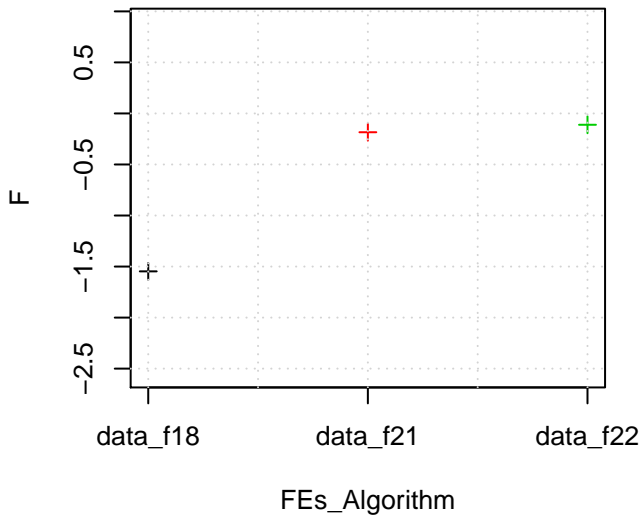
**DIM3-CMAES-a**



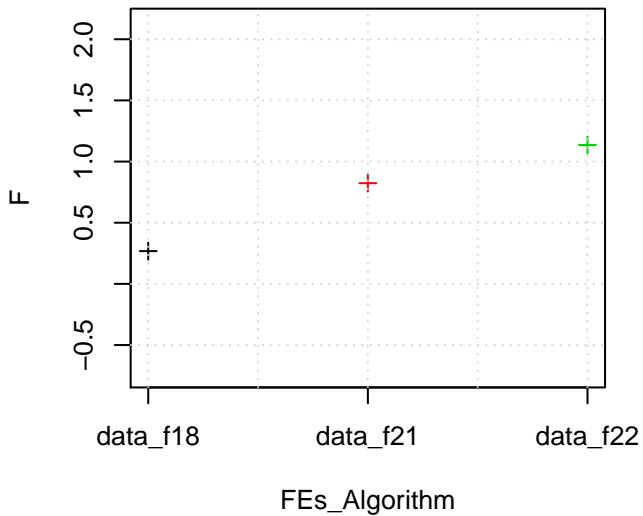
**DIM3-CMAES-b**



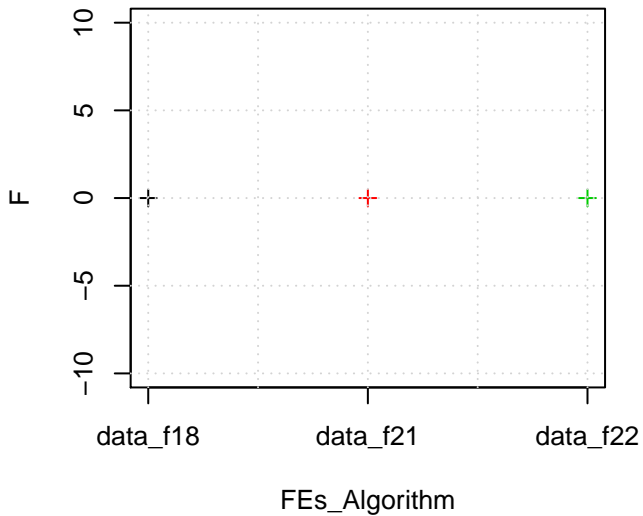
**DIM3-CMAES-c**



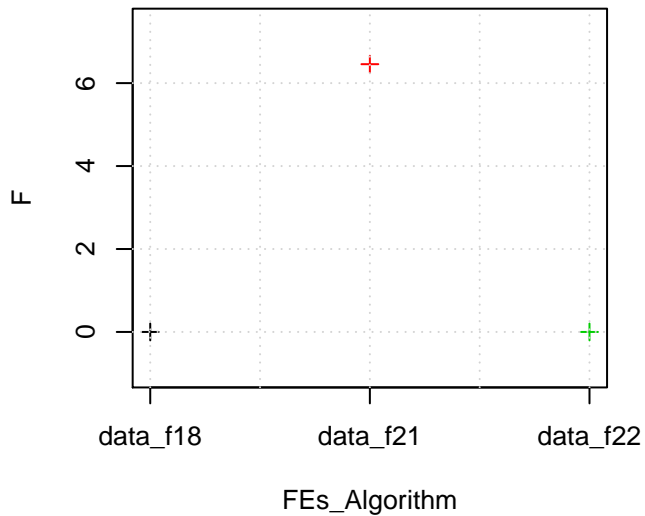
**DIM3-CMAES-d**



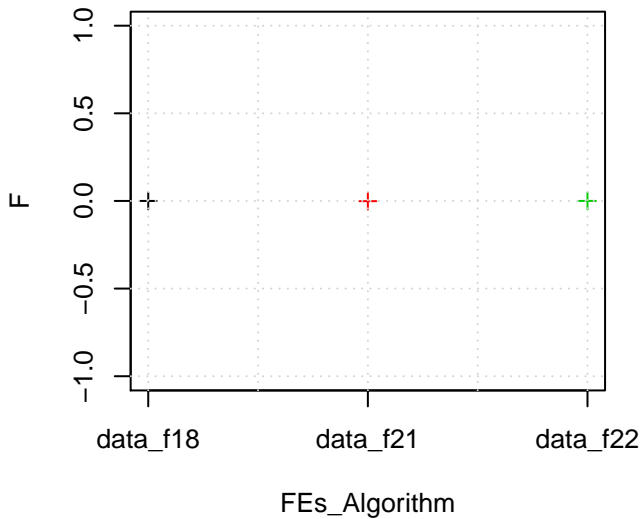
**DIM3-IPOP-a**



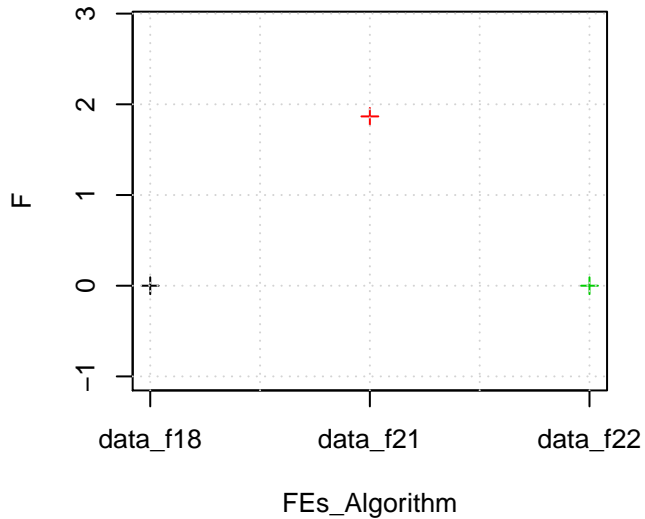
**DIM3-IPOP-b**



**DIM3-IPOP-c**

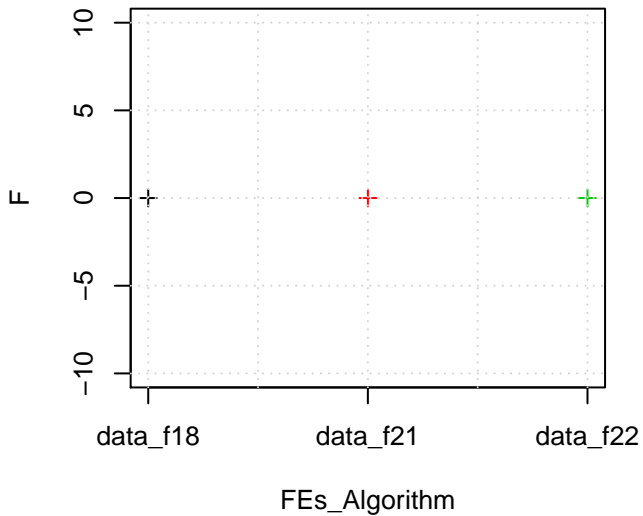


**DIM3-IPOP-d**

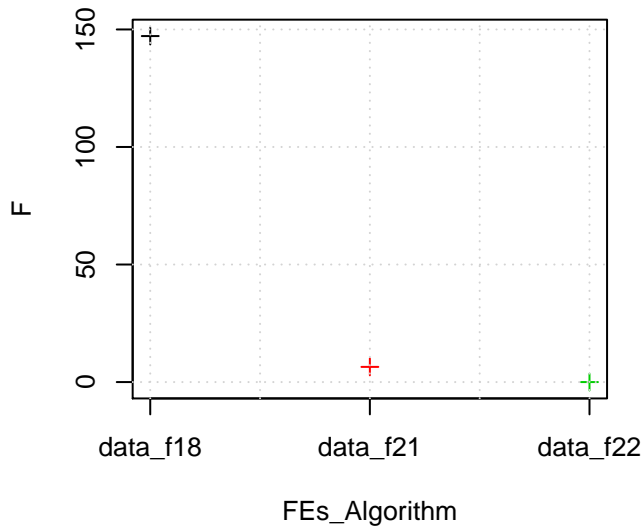




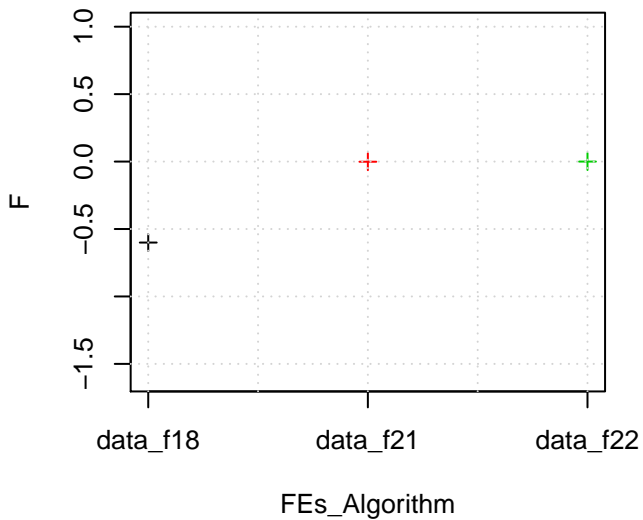
**DIM3-IPOP-500-a**



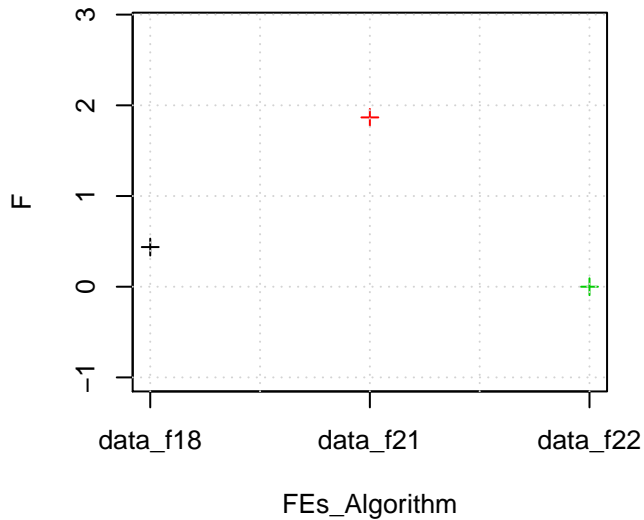
**DIM3-IPOP-500-b**



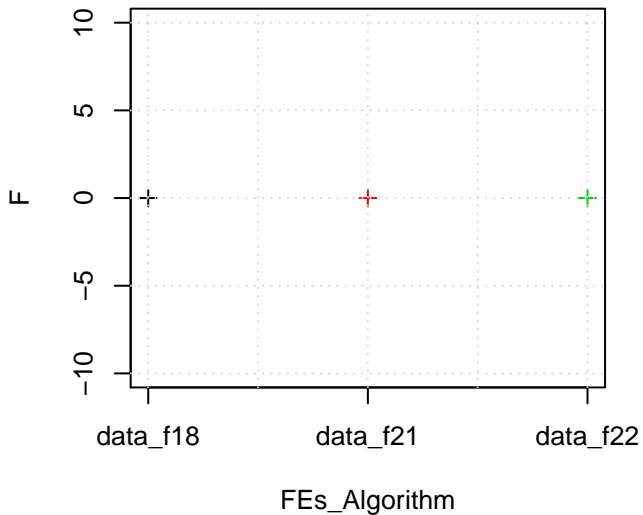
**DIM3-IPOP-500-c**



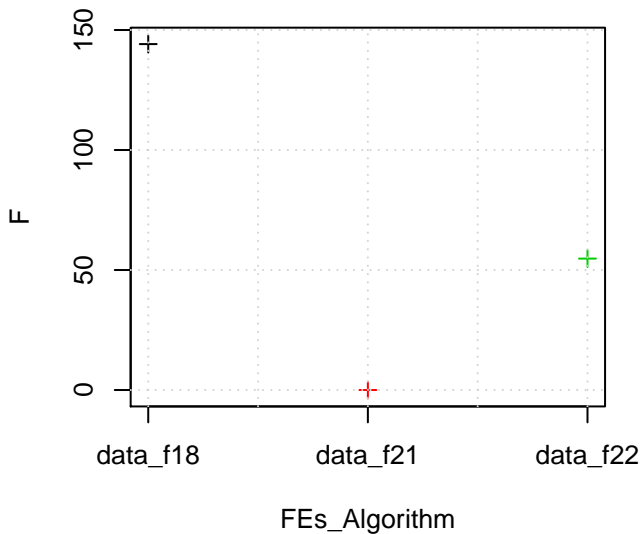
**DIM3-IPOP-500-d**



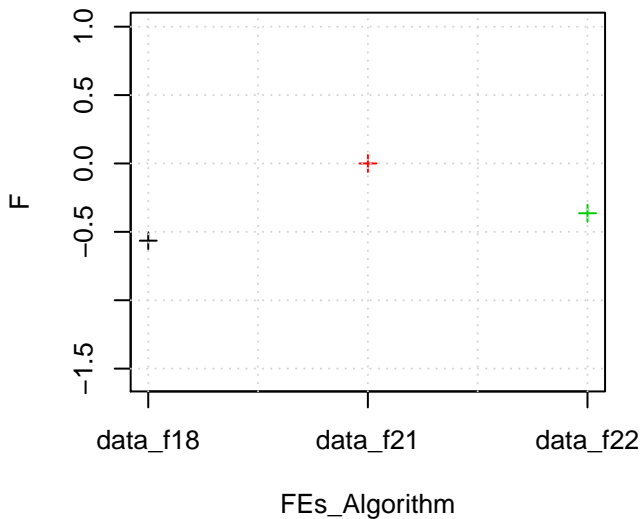
**DIM3-IPOP-tany-a**



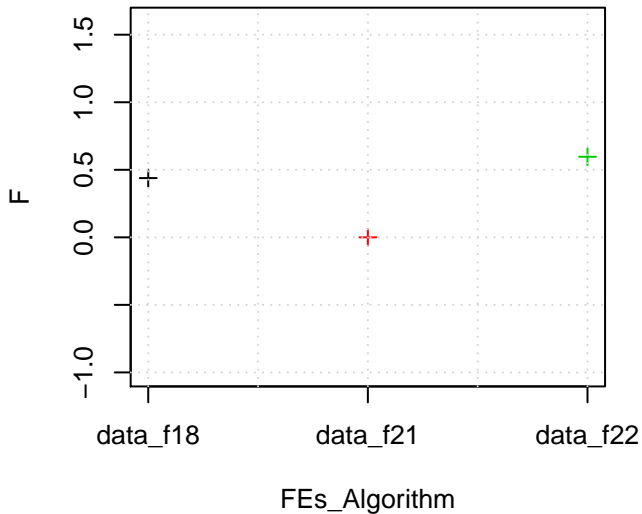
**DIM3-IPOP-tany-b**



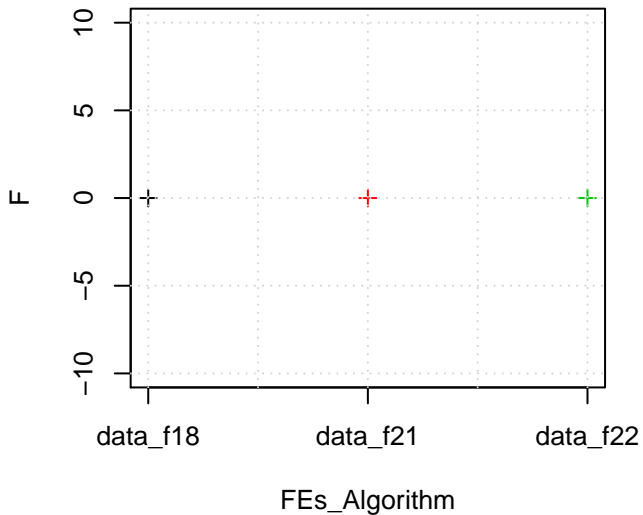
**DIM3-IPOP-tany-c**



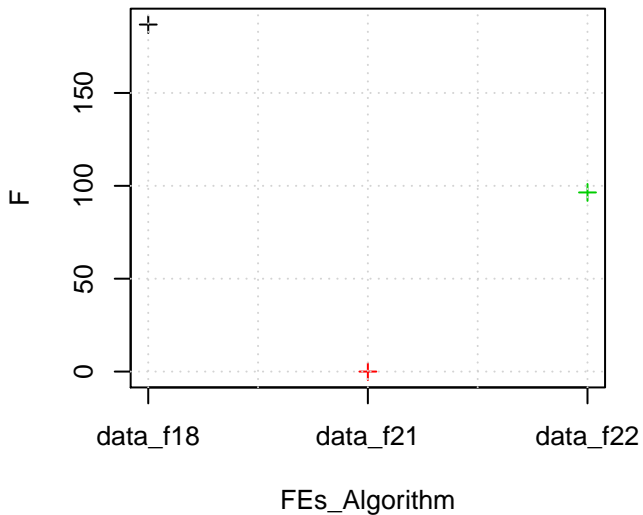
**DIM3-IPOP-tany-d**



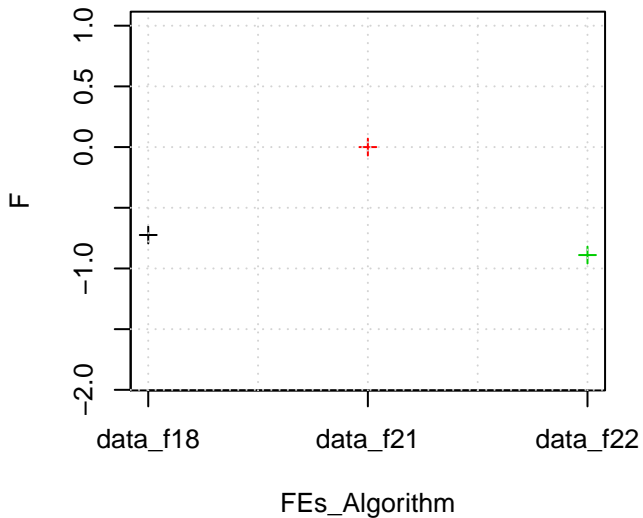
**DIM3-IPOP-texp-a**



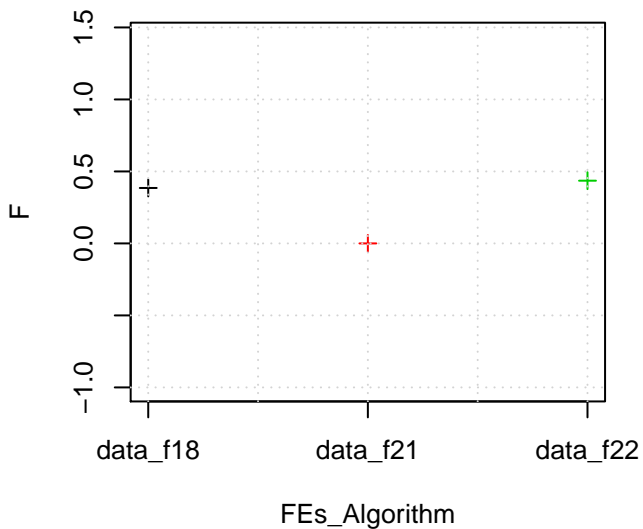
**DIM3-IPOP-texp-b**



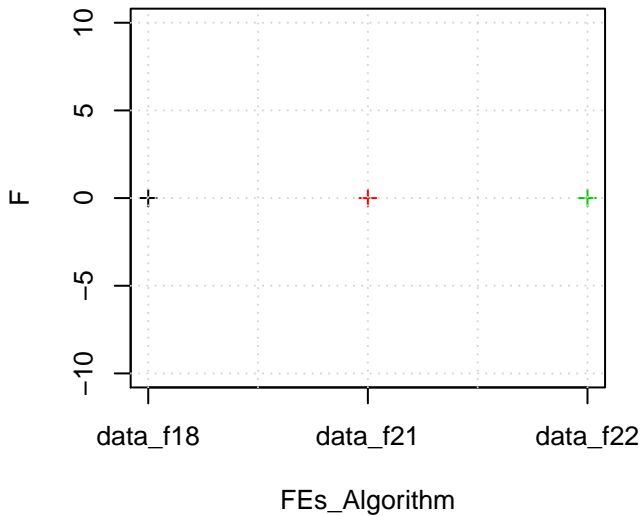
**DIM3-IPOP-texp-c**



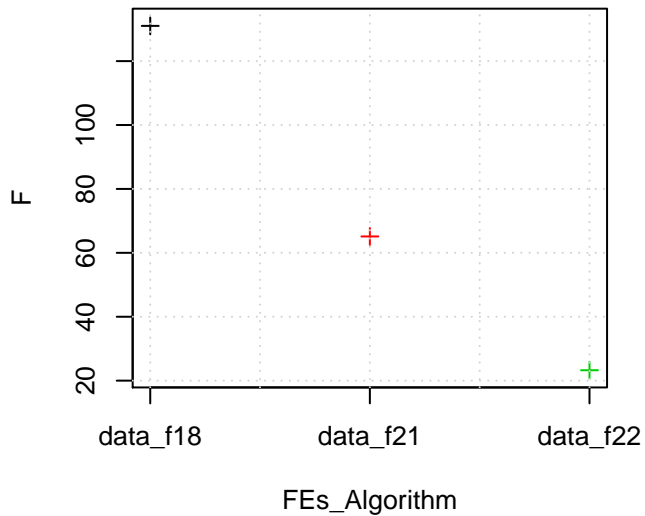
**DIM3-IPOP-texp-d**



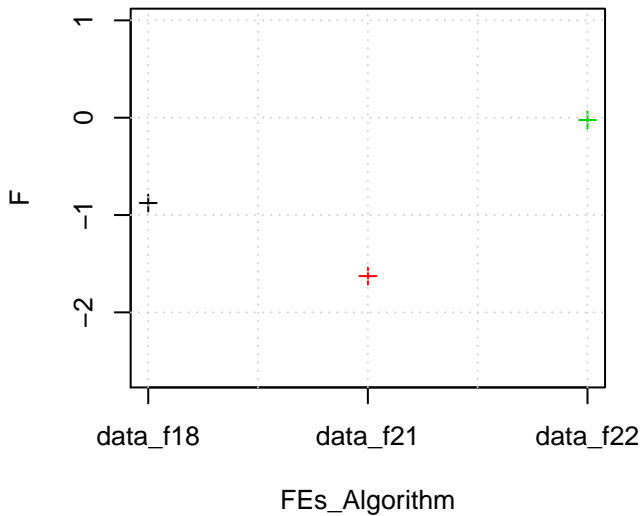
**DIM3-P-DCN-a**



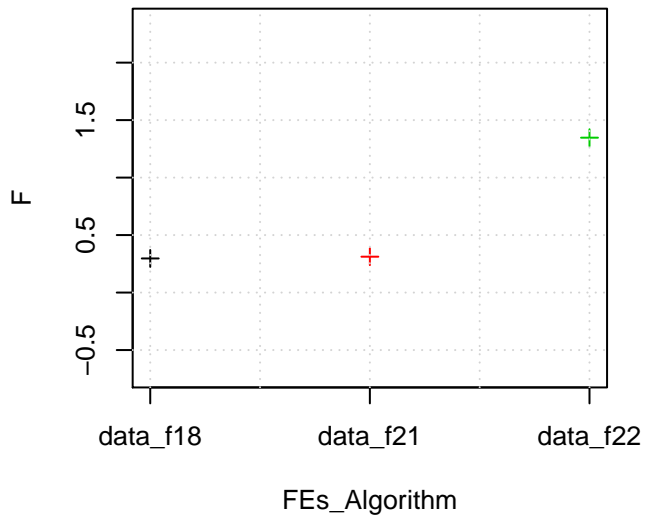
**DIM3-P-DCN-b**



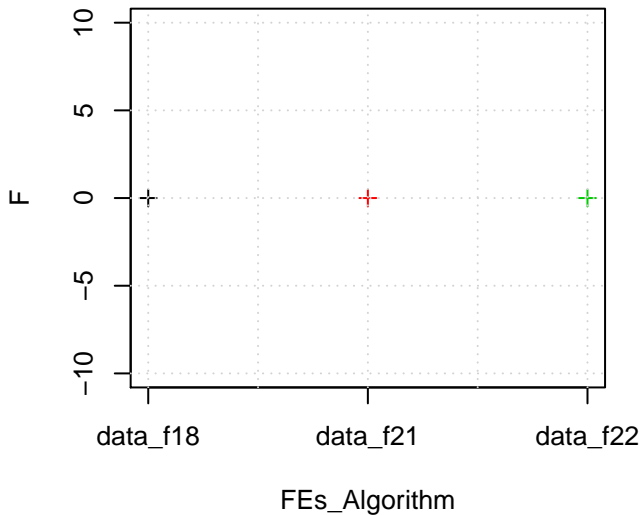
**DIM3-P-DCN-c**



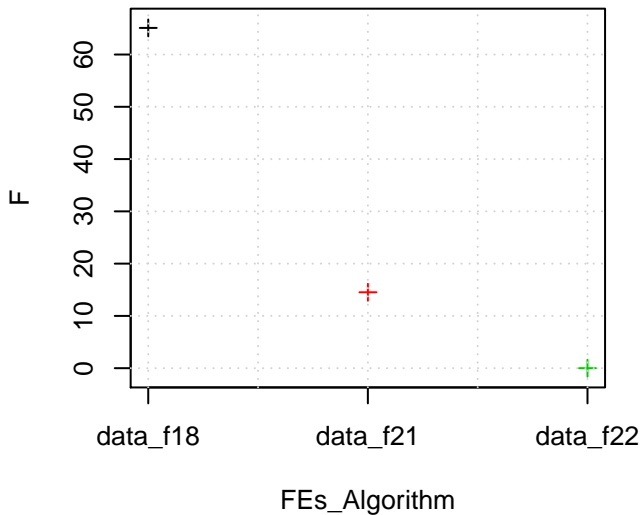
**DIM3-P-DCN-d**



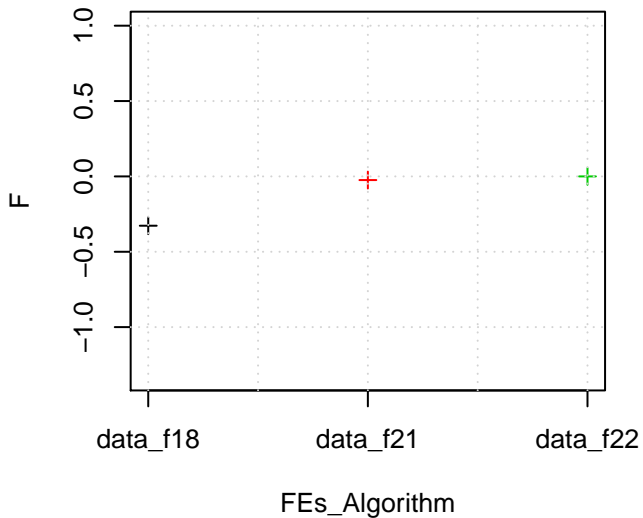
**DIM3-DE-a**



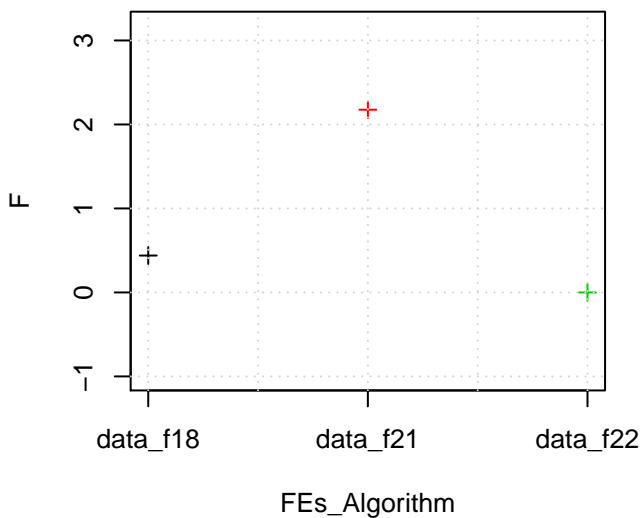
**DIM3-DE-b**



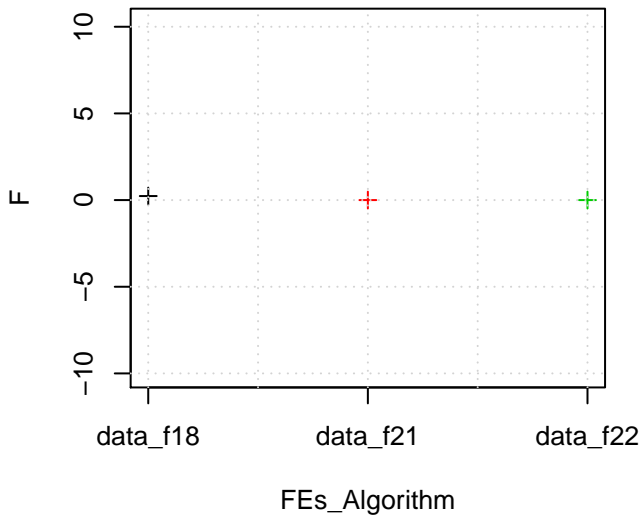
**DIM3-DE-c**



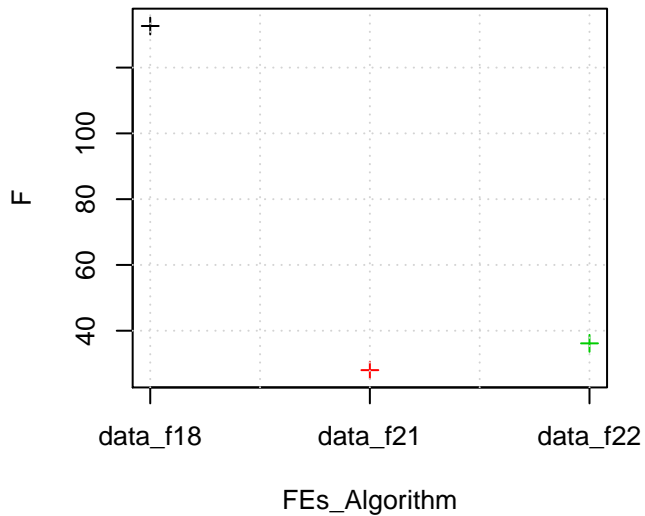
**DIM3-DE-d**



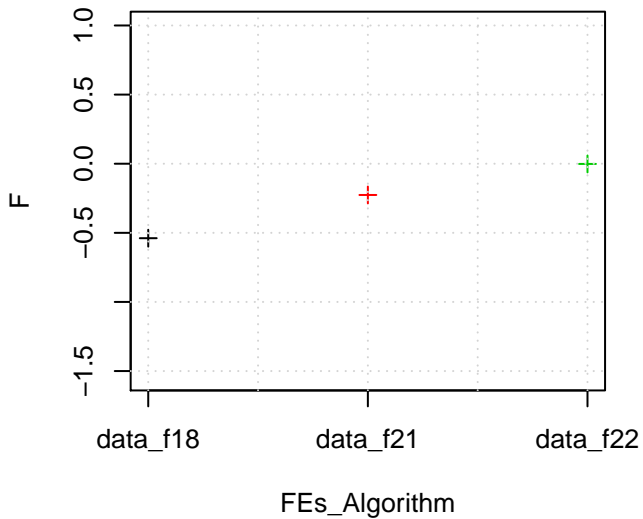
**DIM3-fmincon-a**



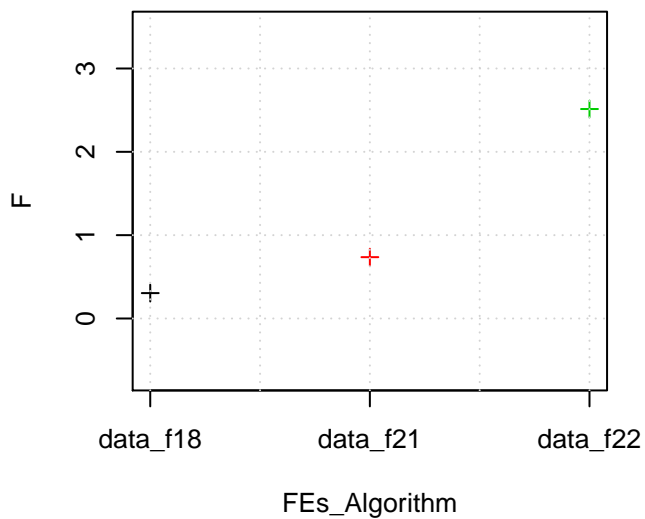
**DIM3-fmincon-b**



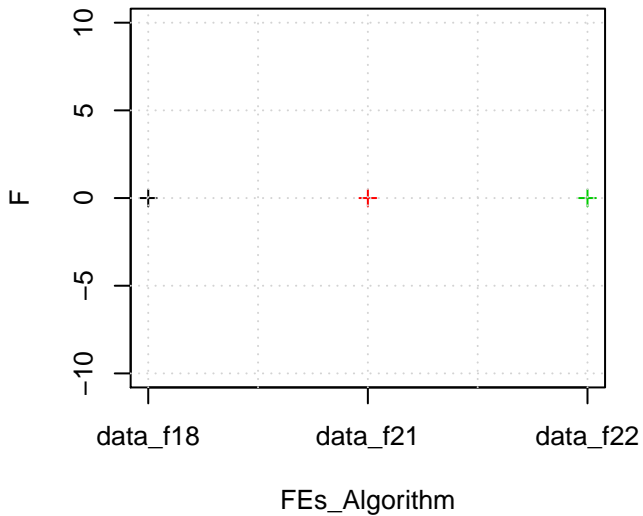
**DIM3-fmincon-c**



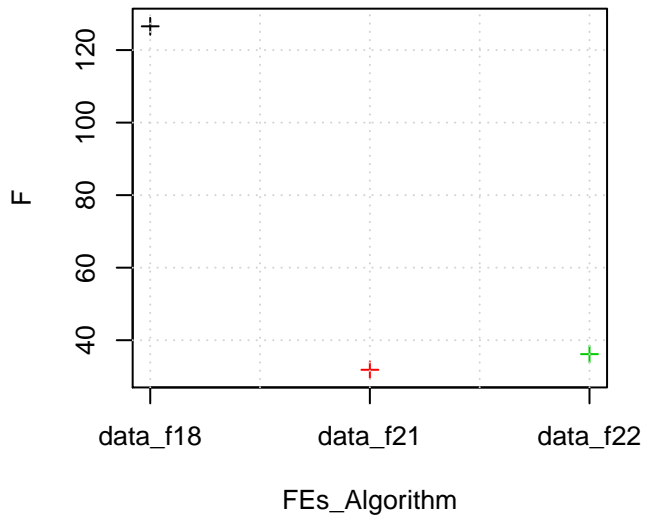
**DIM3-fmincon-d**



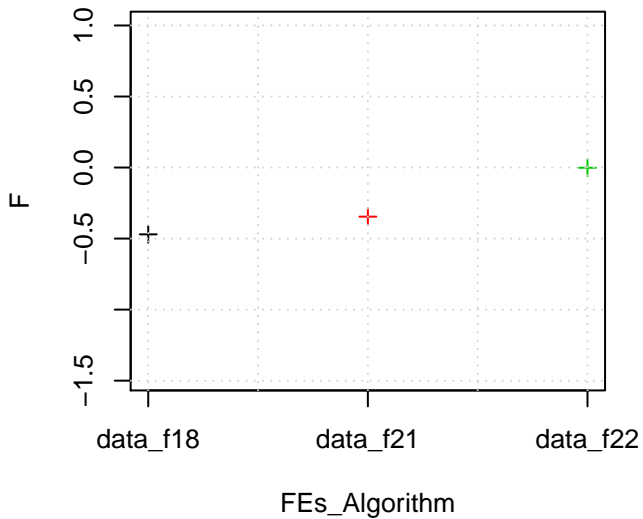
**DIM3-HMLSL-a**



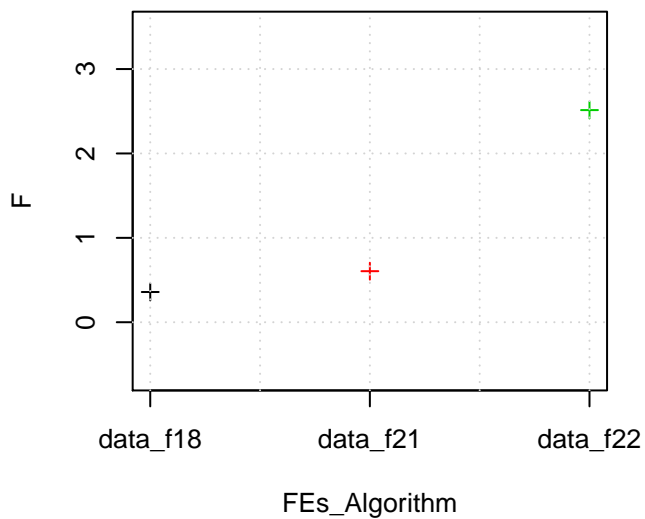
**DIM3-HMLSL-b**



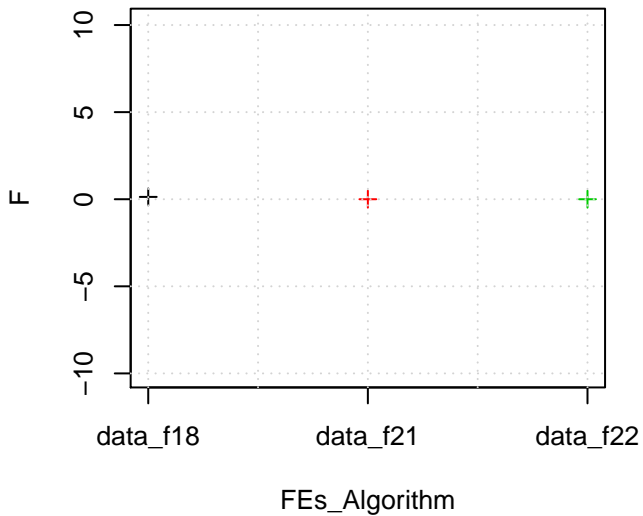
**DIM3-HMLSL-c**



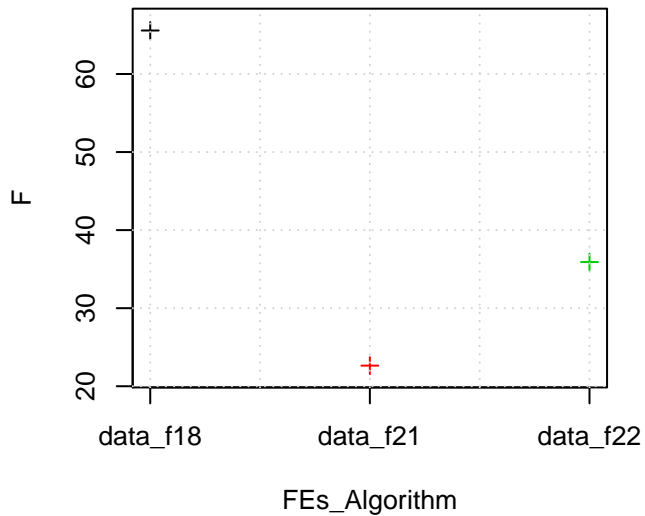
**DIM3-HMLSL-d**



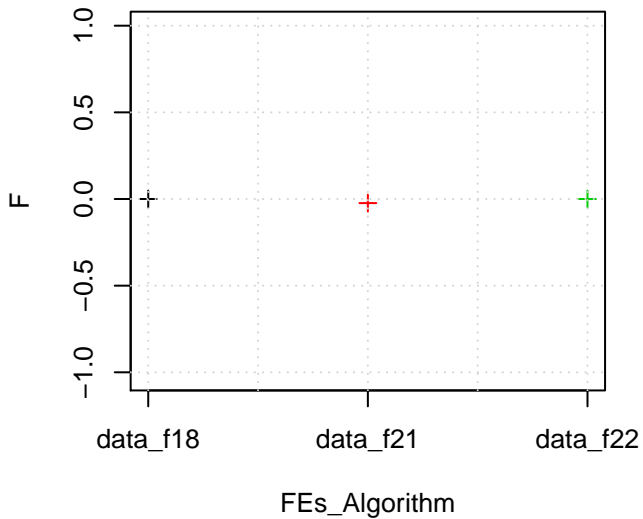
**DIM3-simplex-a**



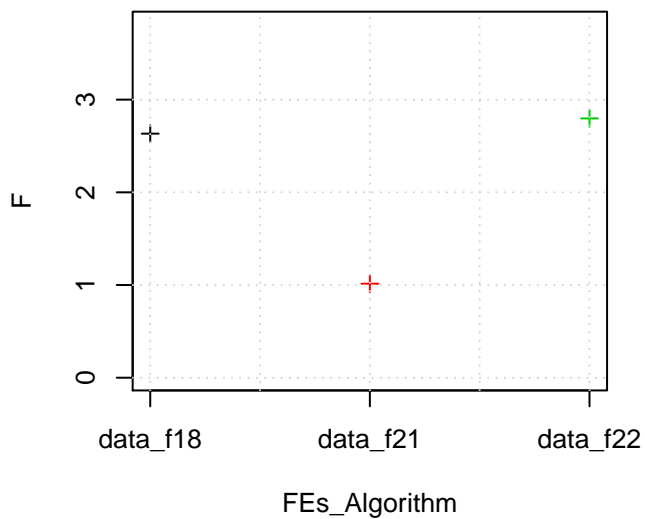
**DIM3-simplex-b**



**DIM3-simplex-c**

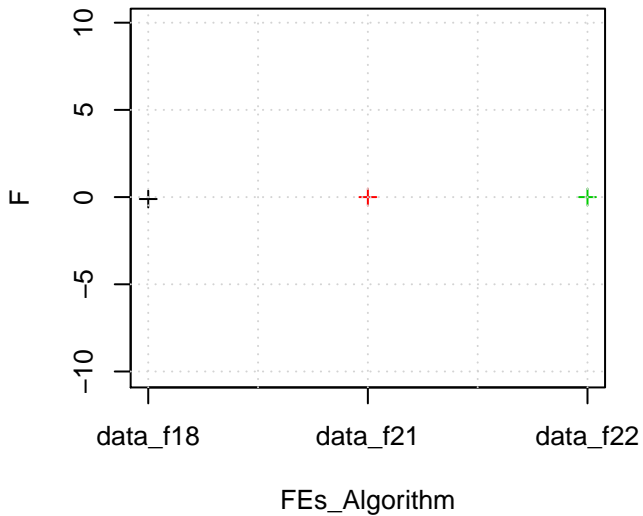


**DIM3-simplex-d**

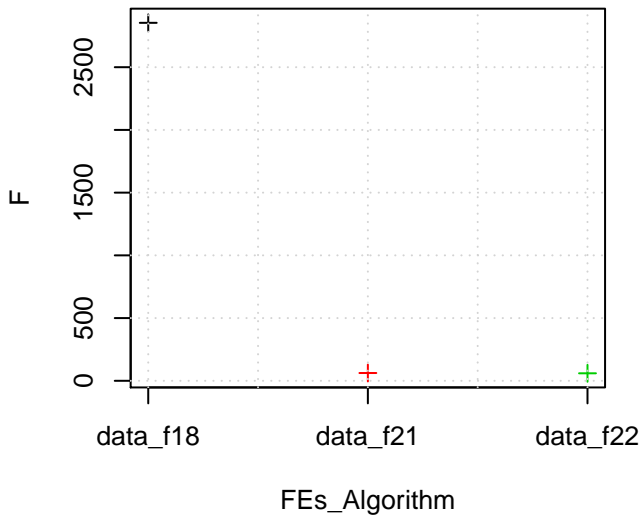




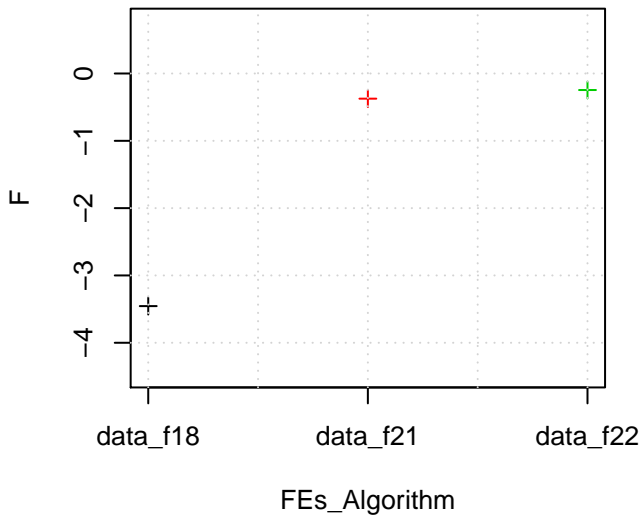
**DIM5-ga100-a**



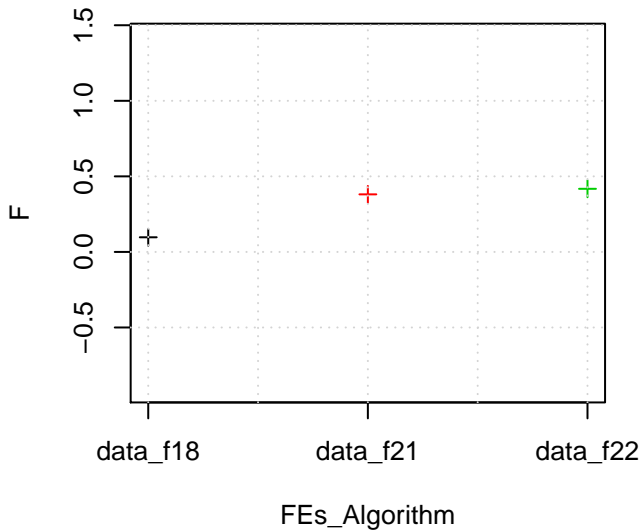
**DIM5-ga100-b**



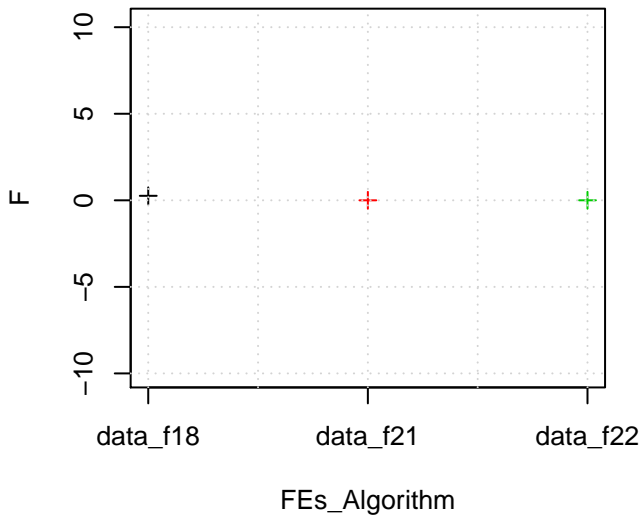
**DIM5-ga100-c**



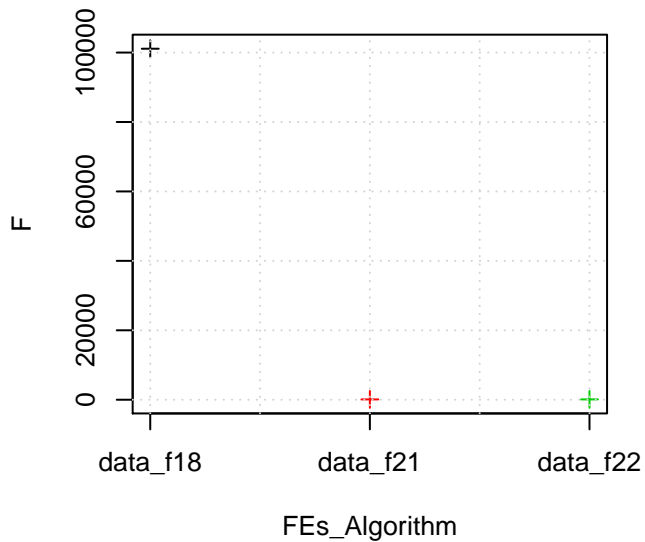
**DIM5-ga100-d**



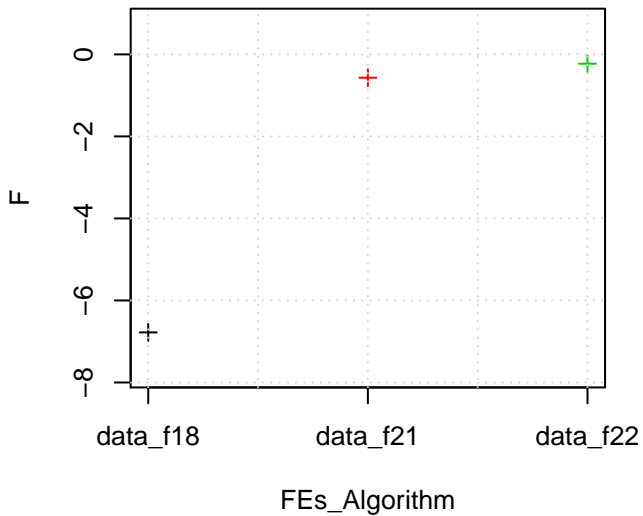
**DIM5-hill-a**



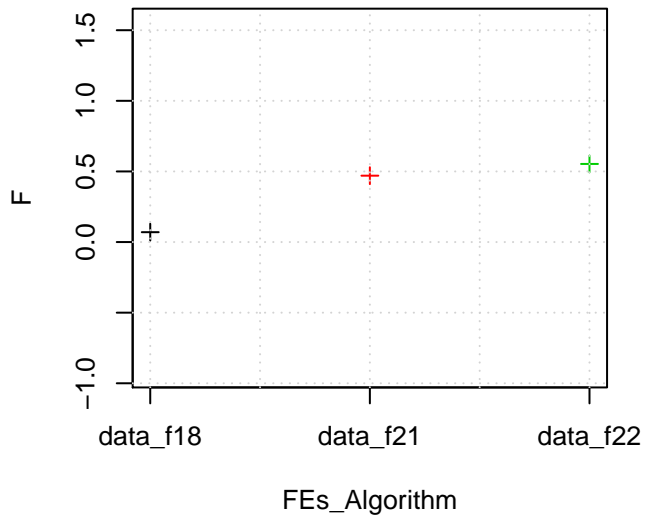
**DIM5-hill-b**



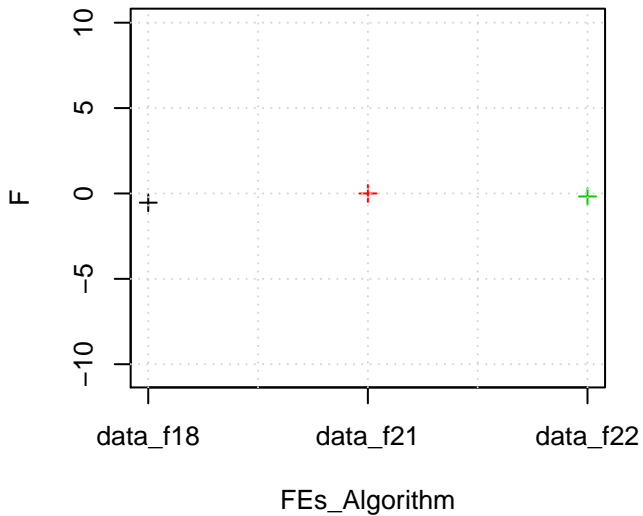
**DIM5-hill-c**



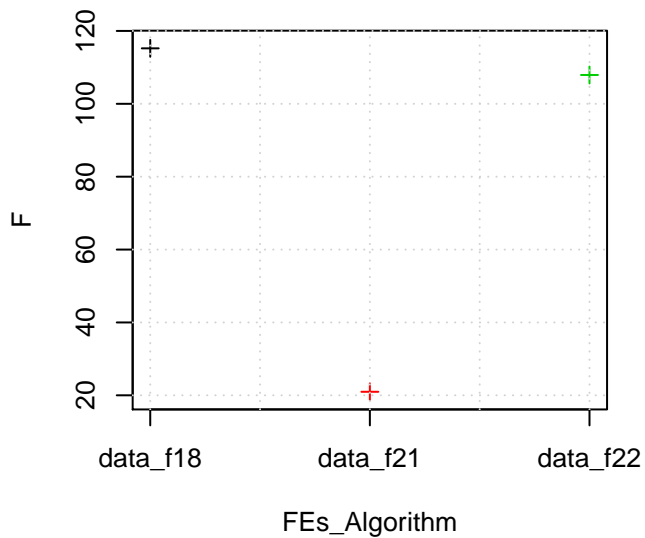
**DIM5-hill-d**



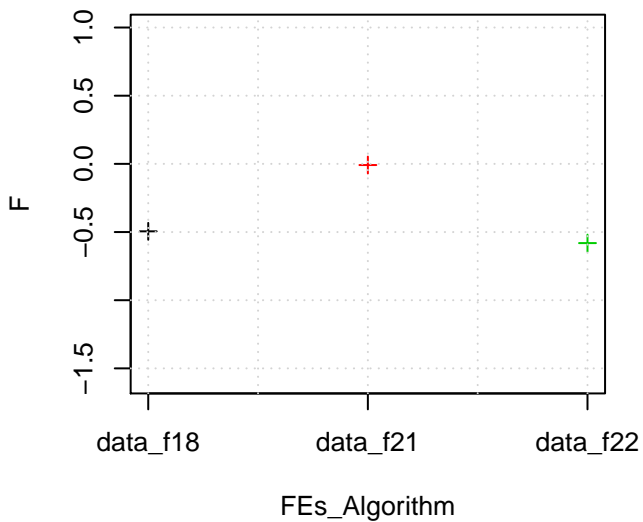
**DIM5-CMAES-a**



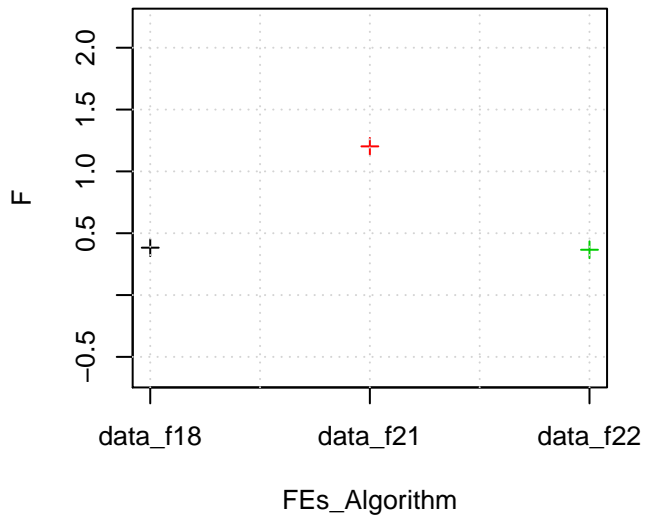
**DIM5-CMAES-b**



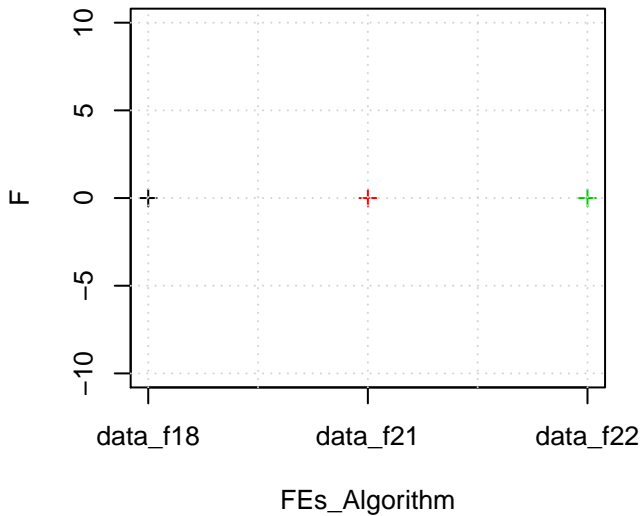
**DIM5-CMAES-c**



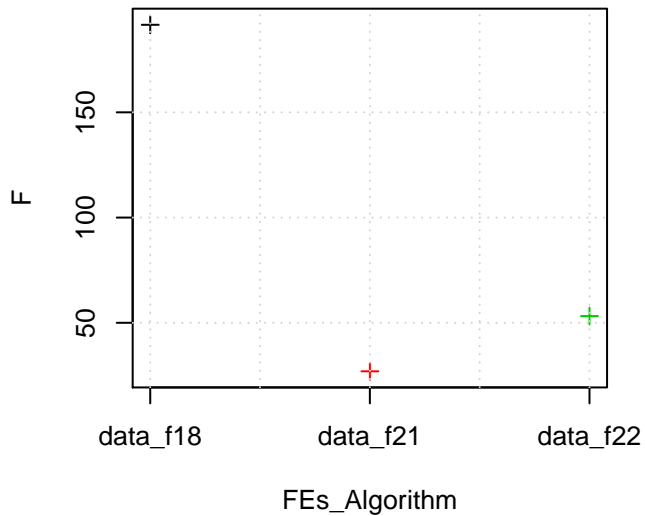
**DIM5-CMAES-d**



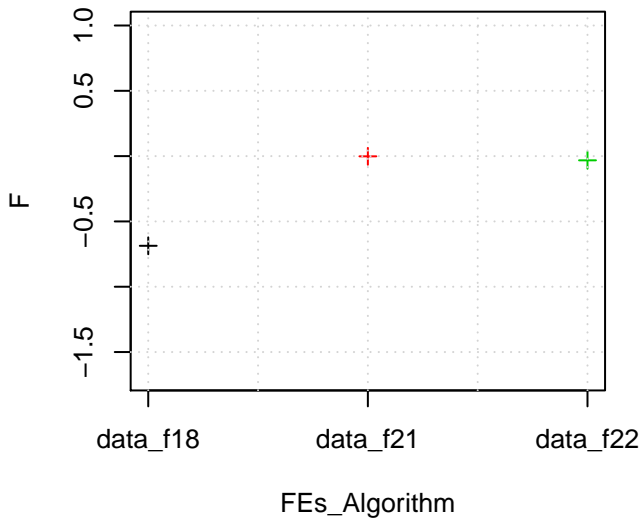
**DIM5-IPOP-a**



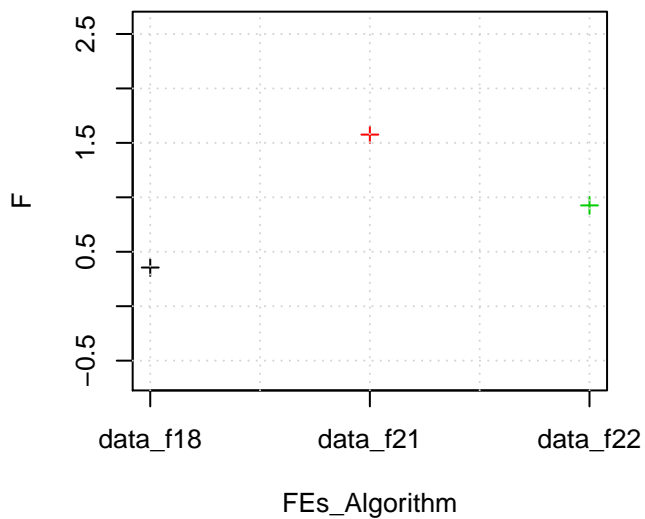
**DIM5-IPOP-b**



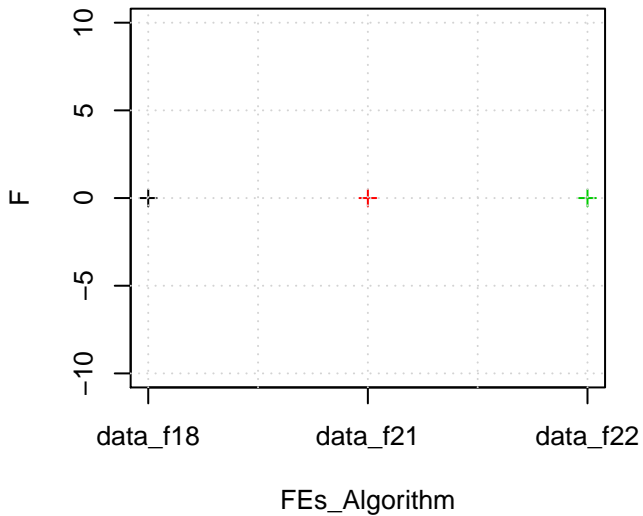
**DIM5-IPOP-c**



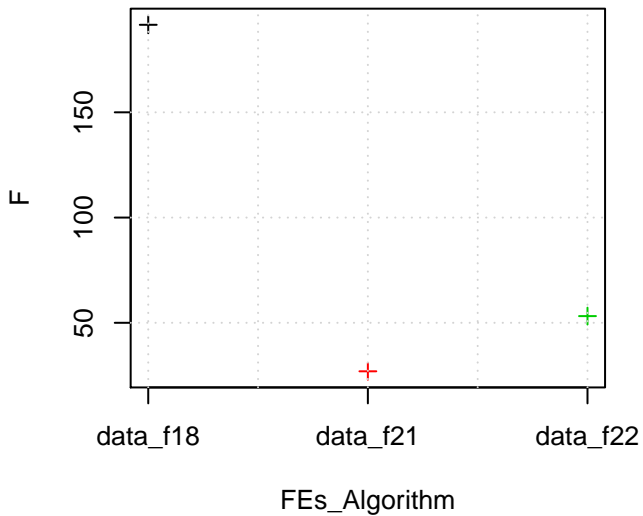
**DIM5-IPOP-d**



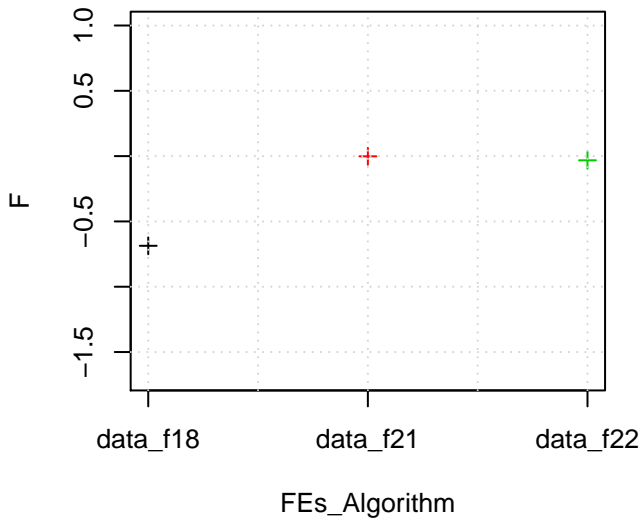
**DIM5-IPOP-500-a**



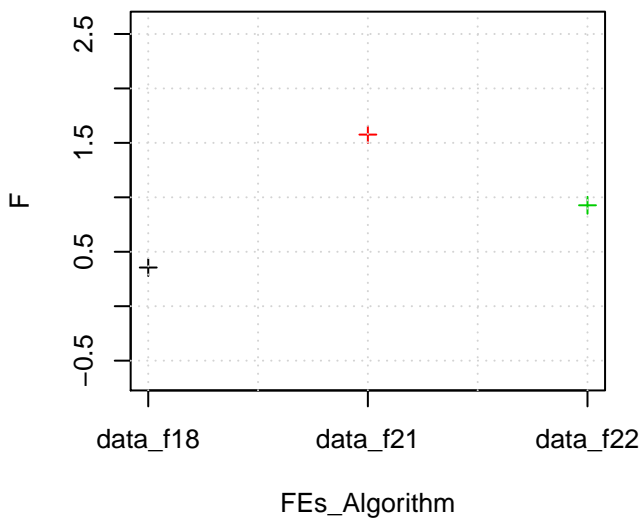
**DIM5-IPOP-500-b**



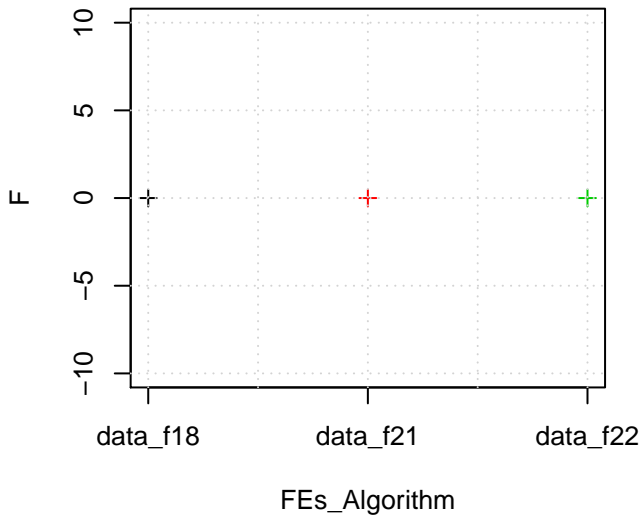
**DIM5-IPOP-500-c**



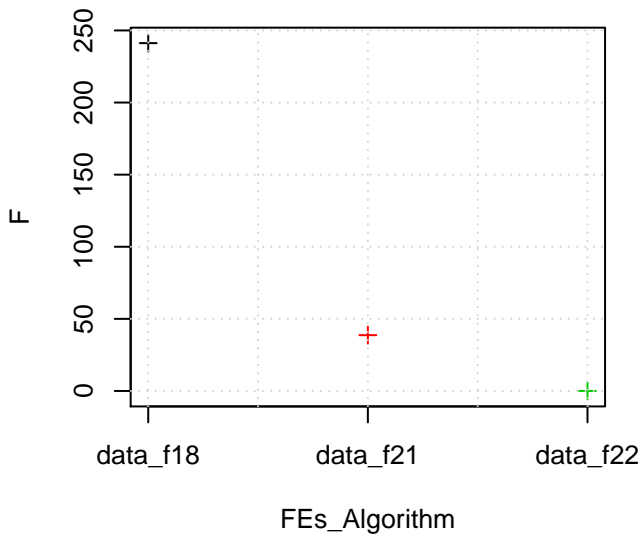
**DIM5-IPOP-500-d**



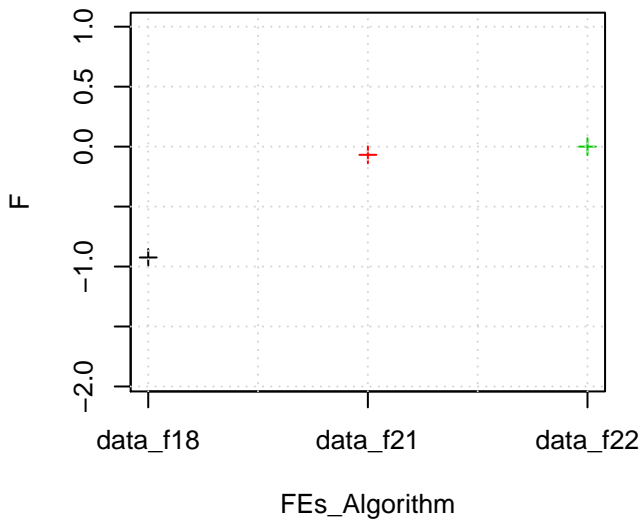
**DIM5-IPOP-tany-a**



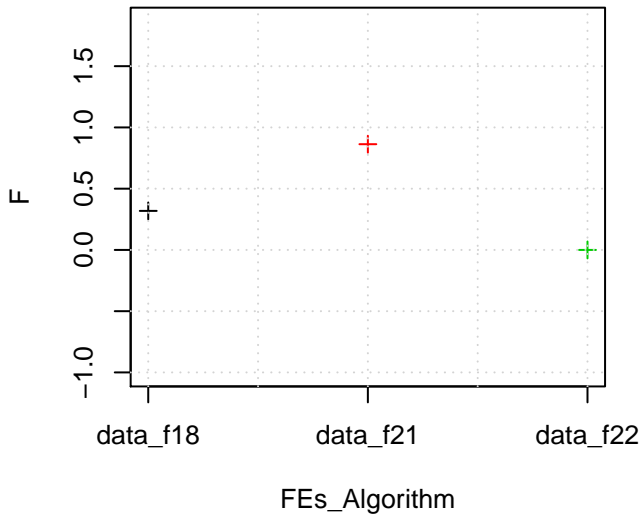
**DIM5-IPOP-tany-b**



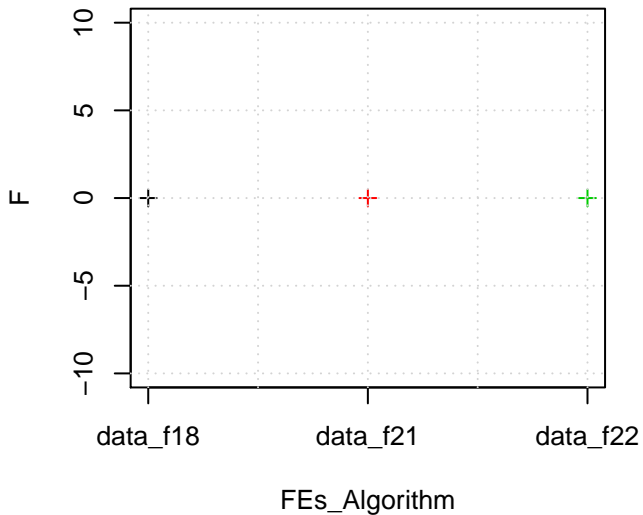
**DIM5-IPOP-tany-c**



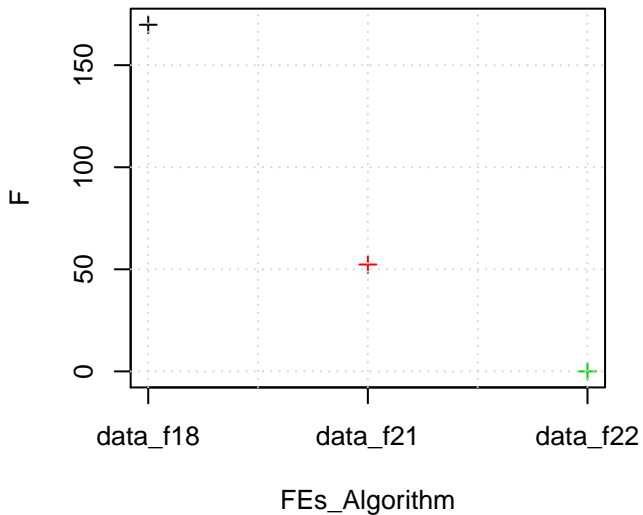
**DIM5-IPOP-tany-d**



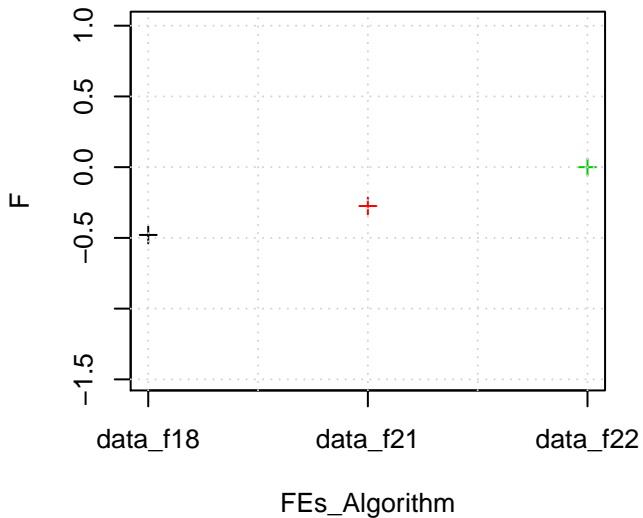
**DIM5-IPOP-texp-a**



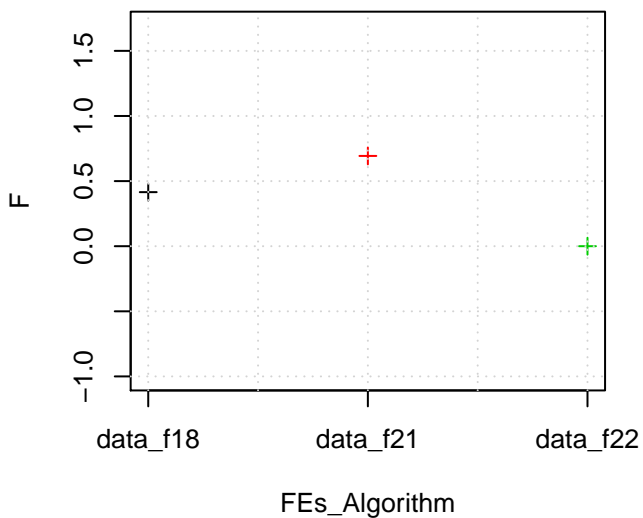
**DIM5-IPOP-texp-b**



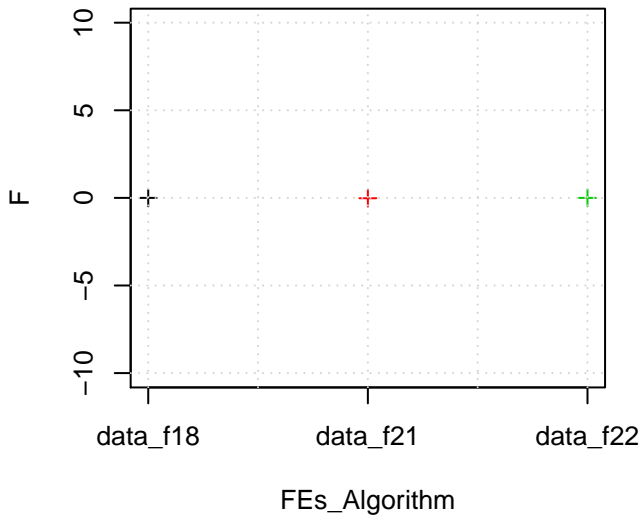
**DIM5-IPOP-texp-c**



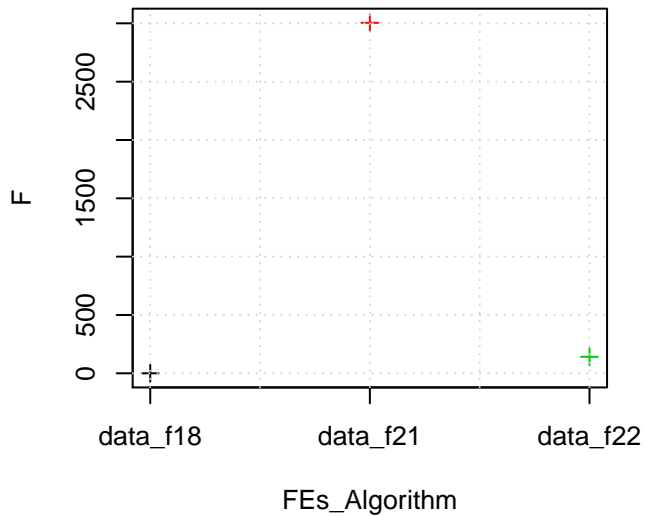
**DIM5-IPOP-texp-d**



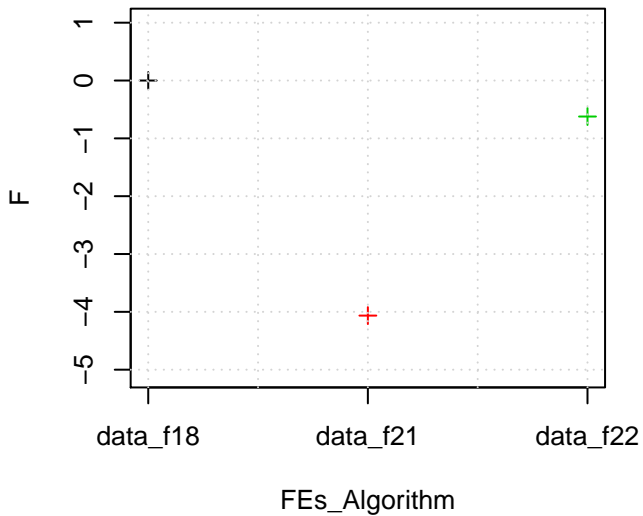
**DIM5-P-DCN-a**



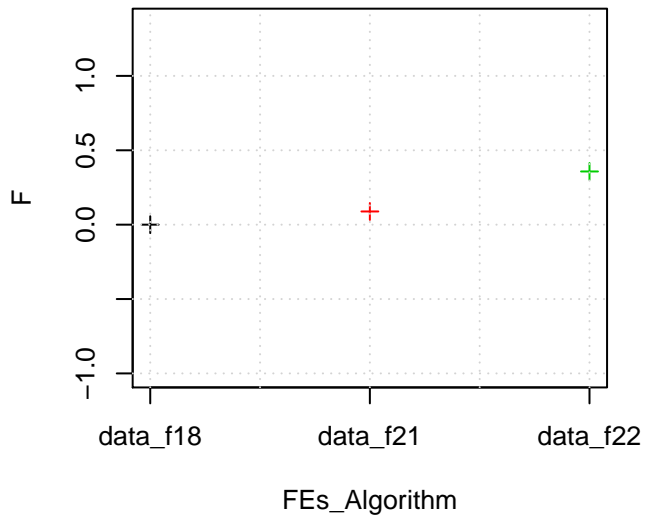
**DIM5-P-DCN-b**



**DIM5-P-DCN-c**

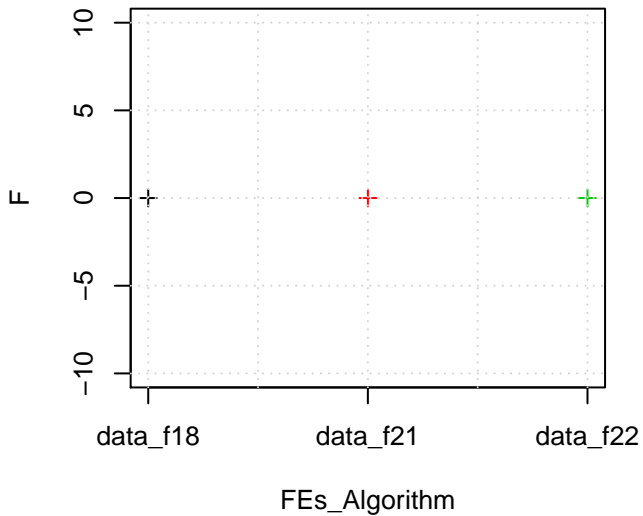


**DIM5-P-DCN-d**

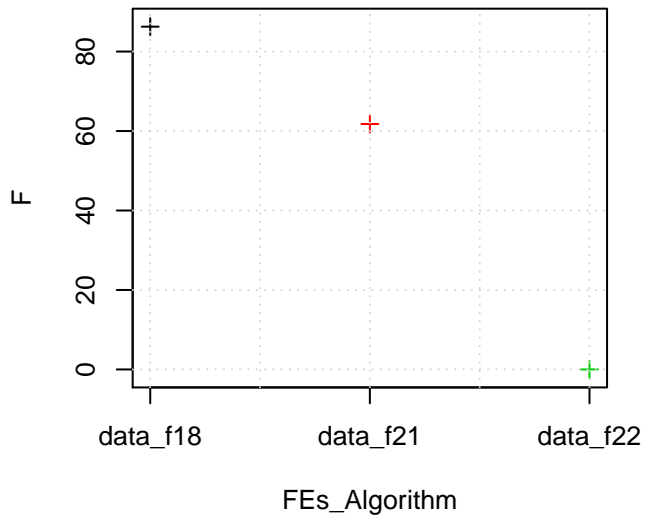




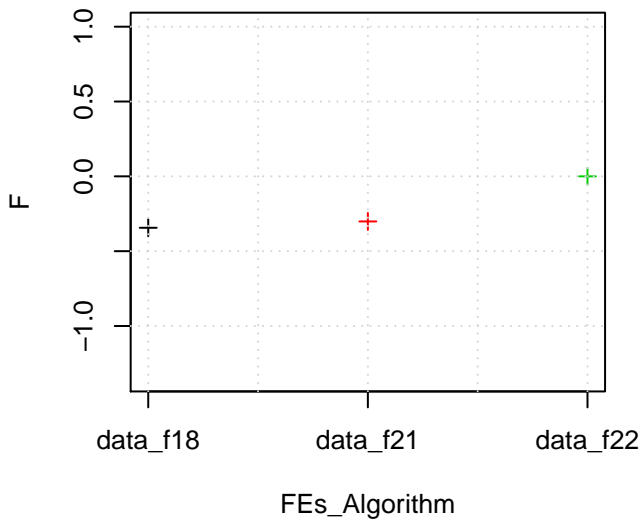
**DIM5-DE-a**



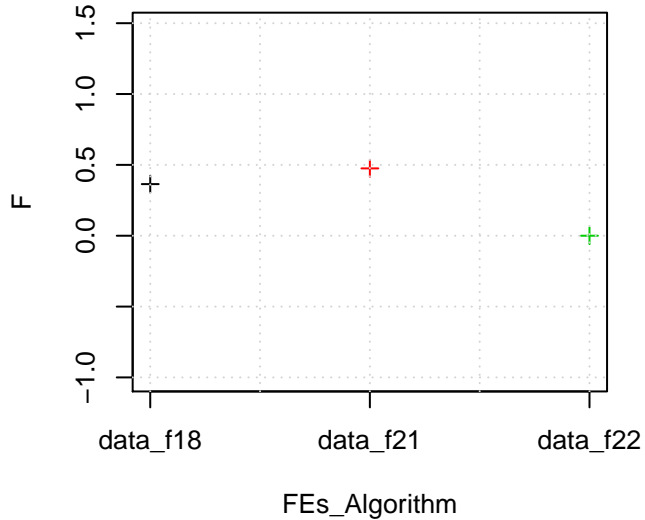
**DIM5-DE-b**



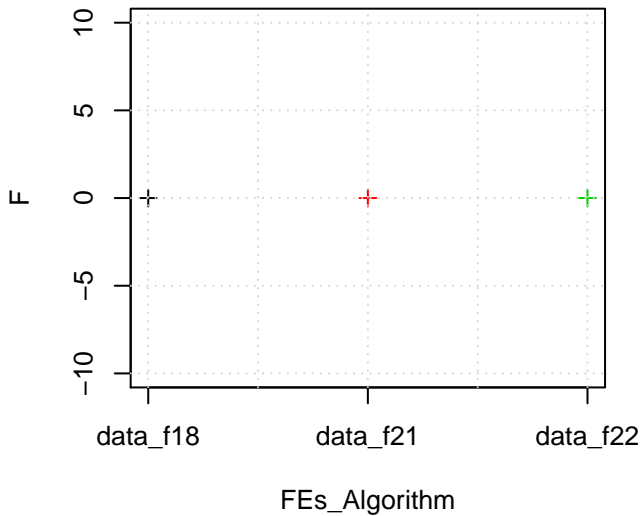
**DIM5-DE-c**



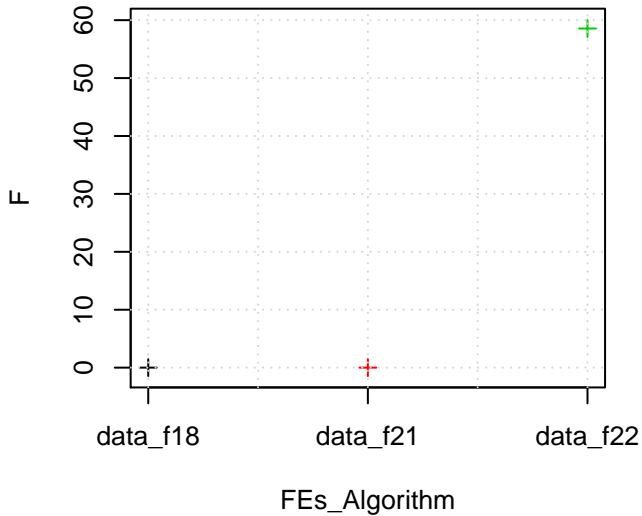
**DIM5-DE-d**



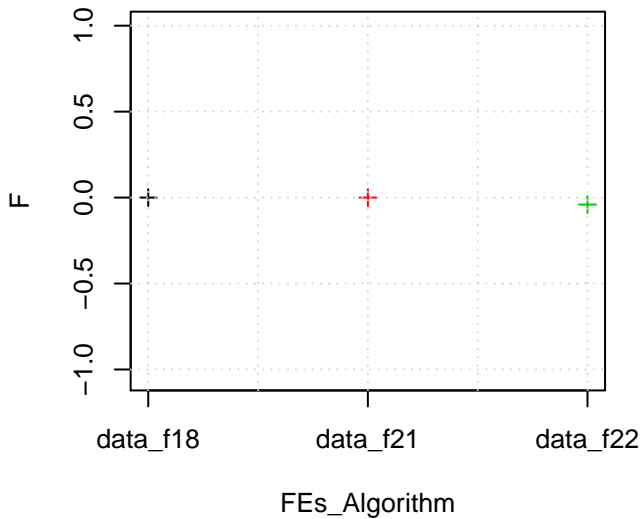
**DIM5-fmincon-a**



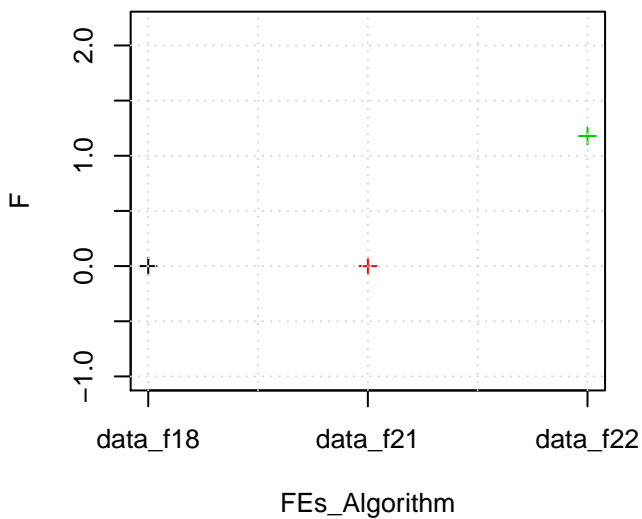
**DIM5-fmincon-b**



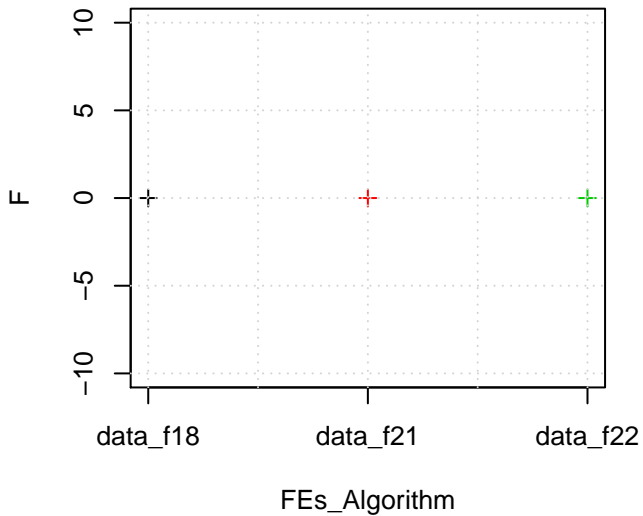
**DIM5-fmincon-c**



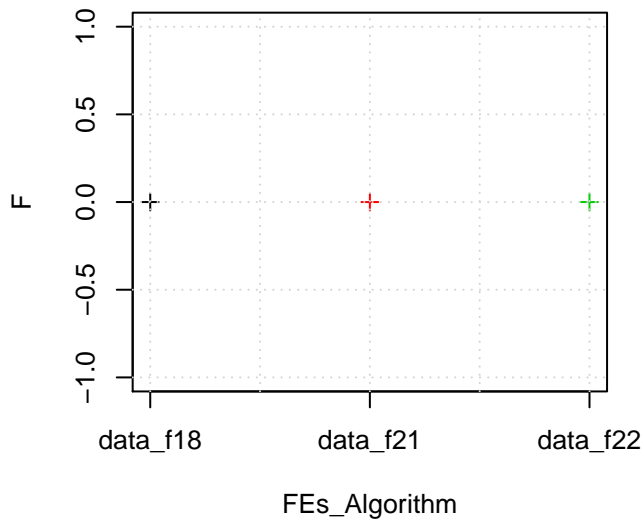
**DIM5-fmincon-d**



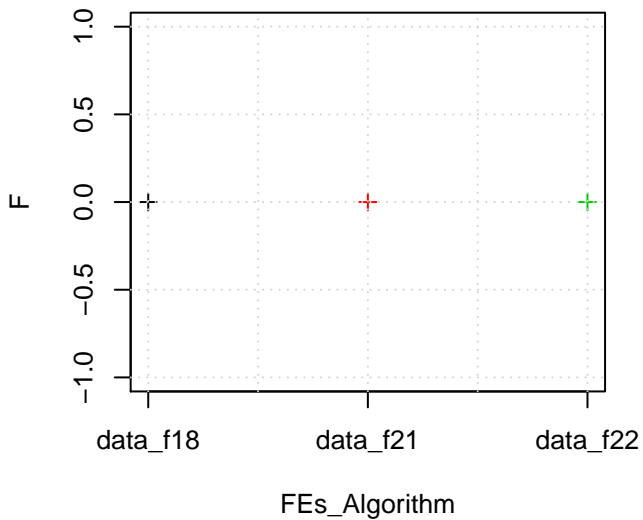
**DIM5-HMLSL-a**



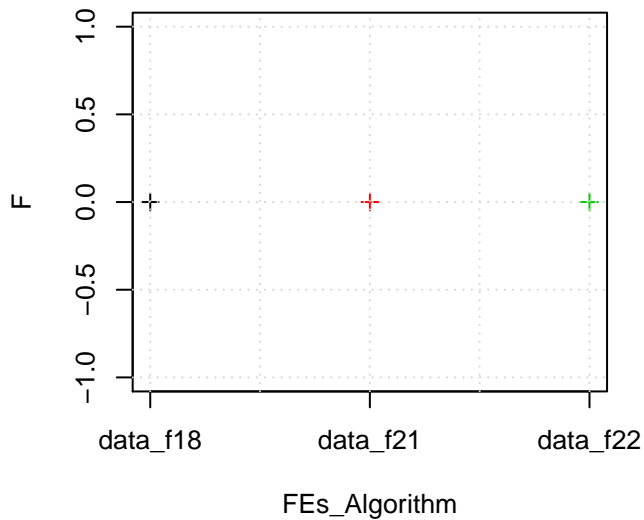
**DIM5-HMLSL-b**



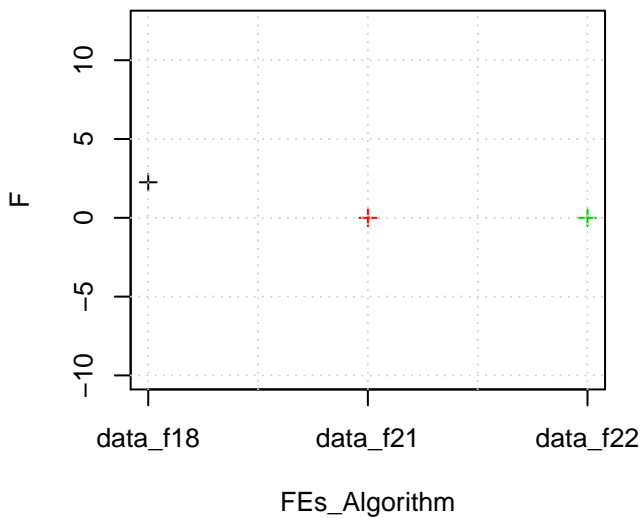
**DIM5-HMLSL-c**



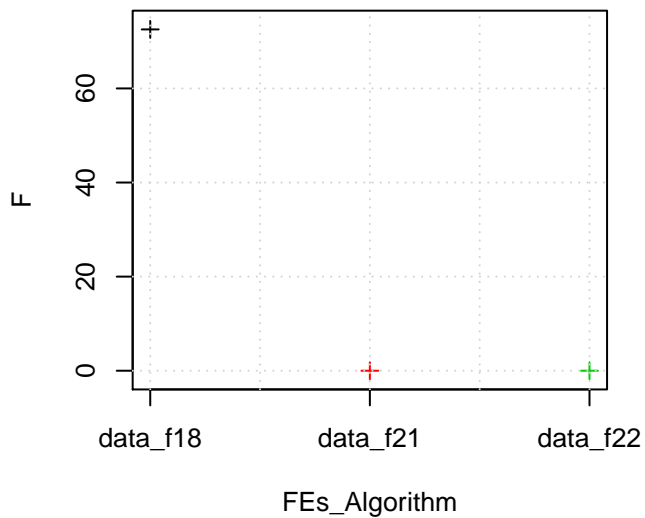
**DIM5-HMLSL-d**



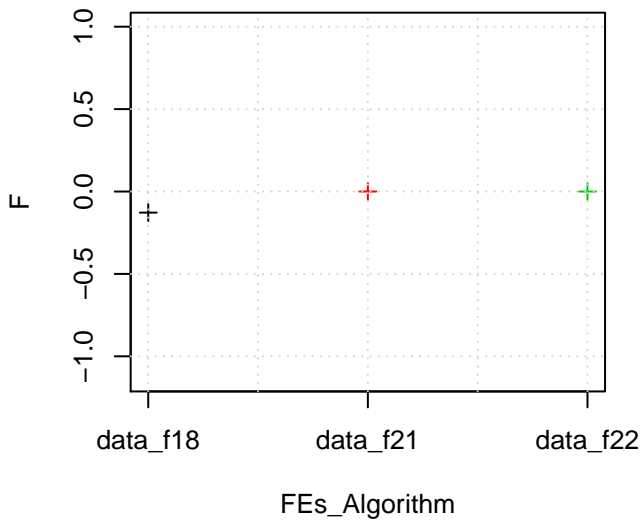
**DIM5-simplex-a**



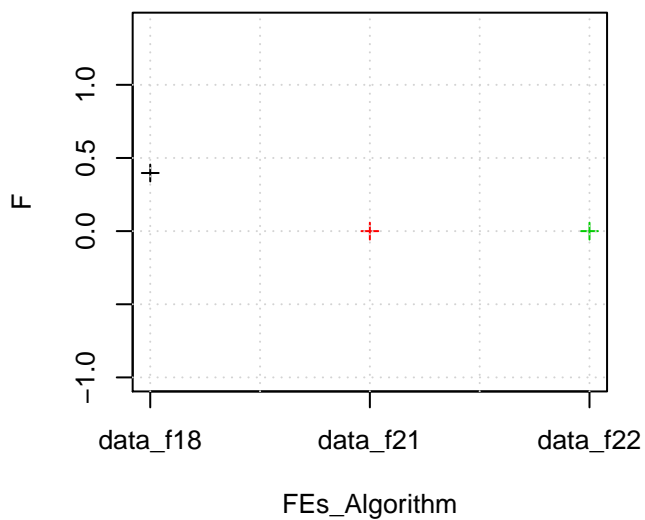
**DIM5-simplex-b**



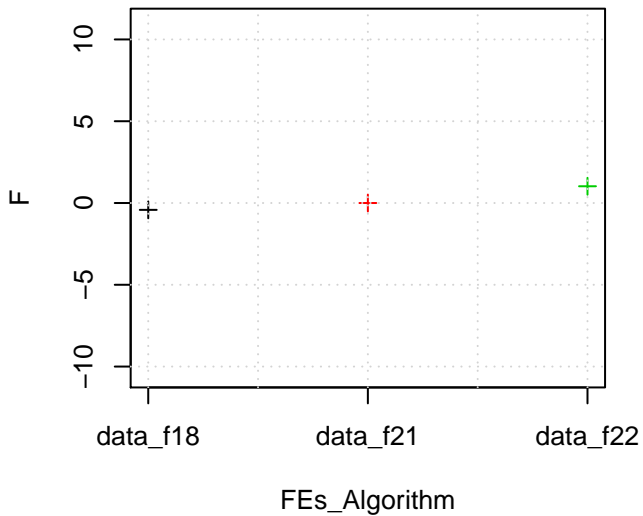
**DIM5-simplex-c**



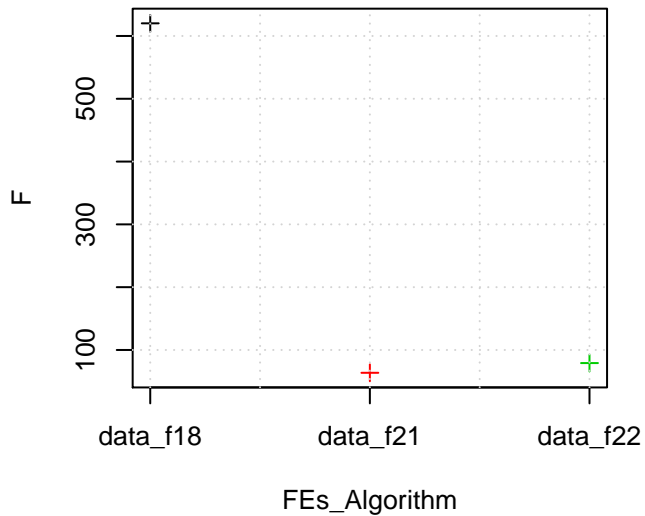
**DIM5-simplex-d**



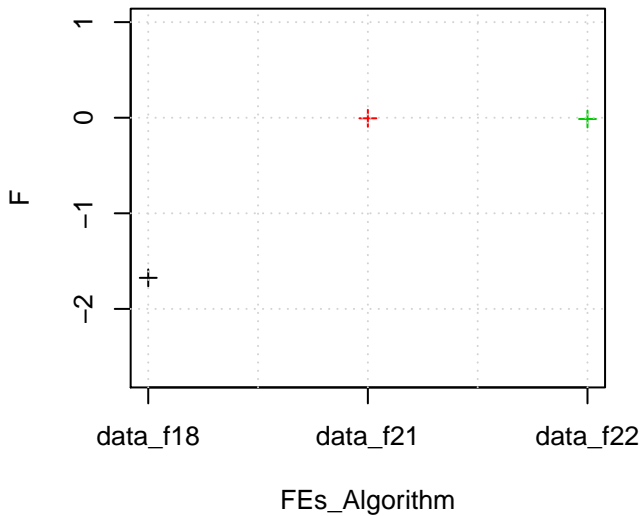
**DIM10-ga100-a**



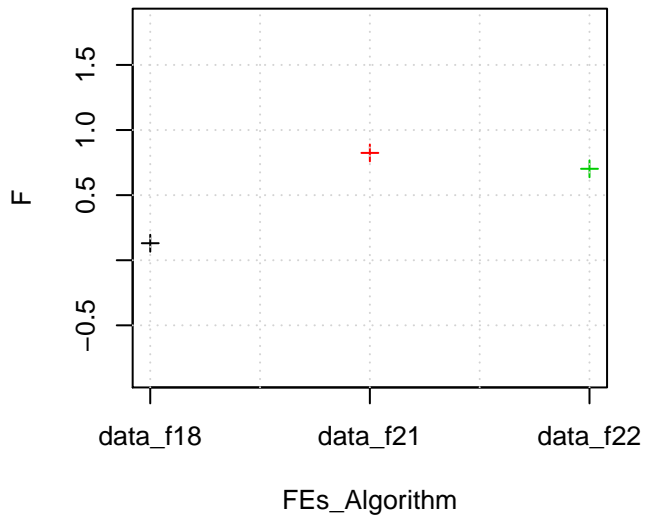
**DIM10-ga100-b**



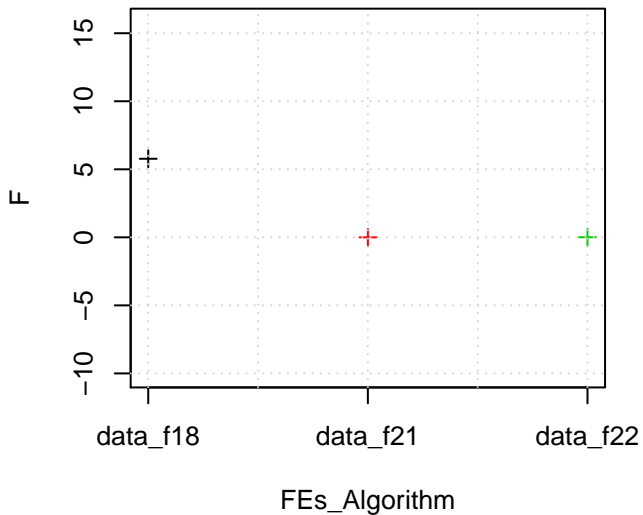
**DIM10-ga100-c**



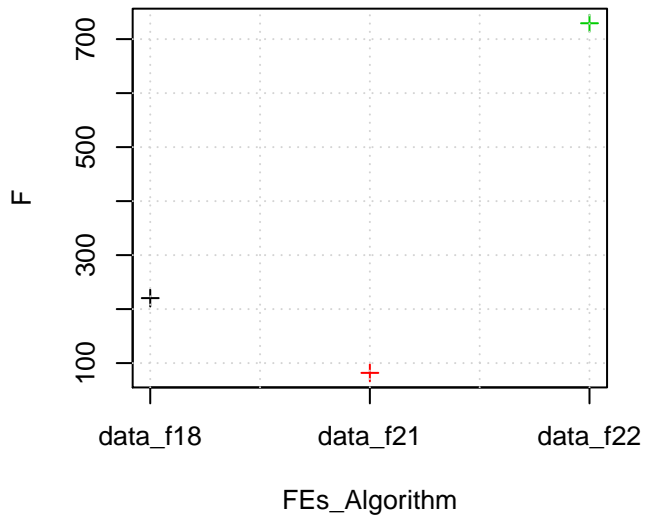
**DIM10-ga100-d**



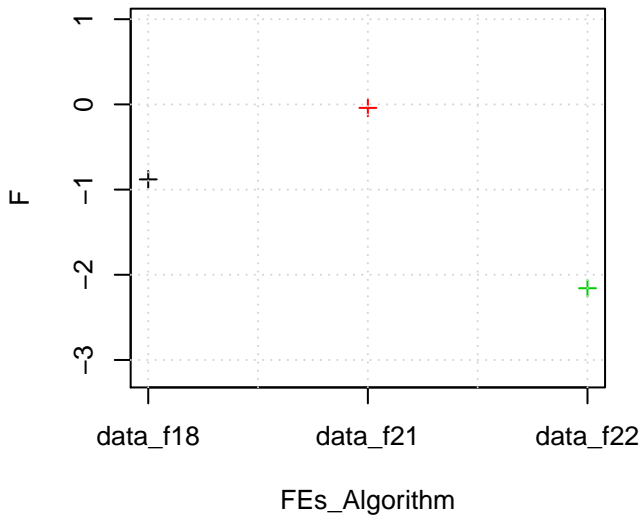
**DIM10-hill-a**



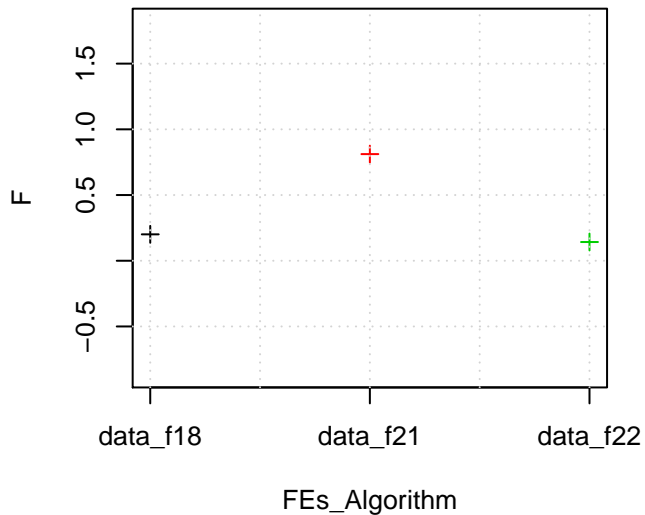
**DIM10-hill-b**



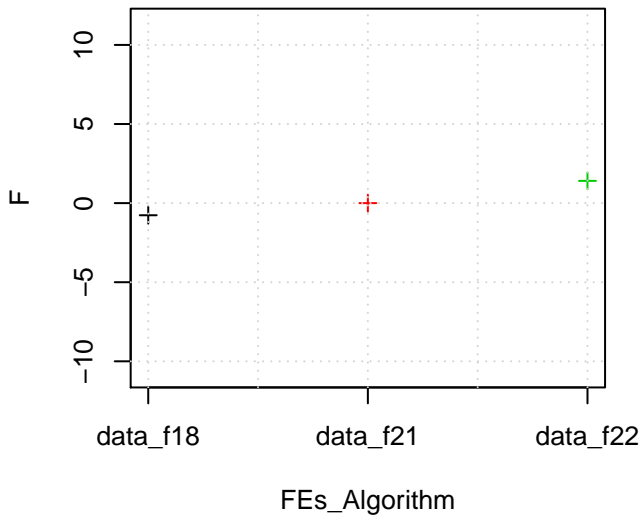
**DIM10-hill-c**



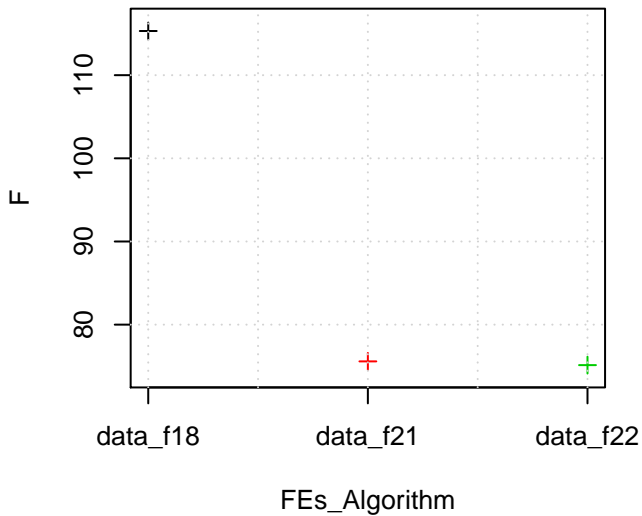
**DIM10-hill-d**



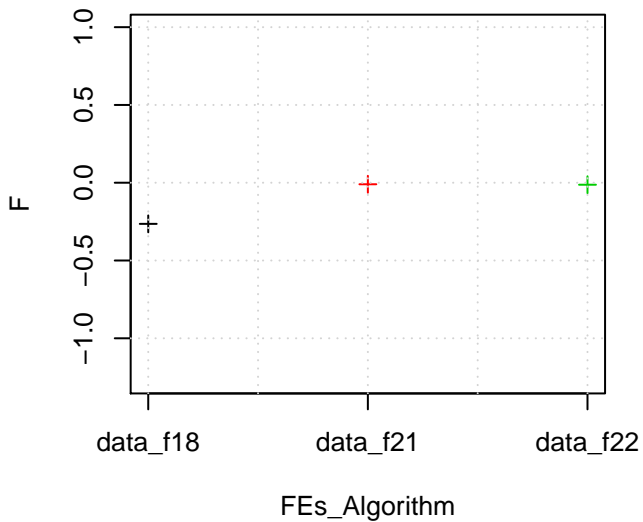
**DIM10-CMAES-a**



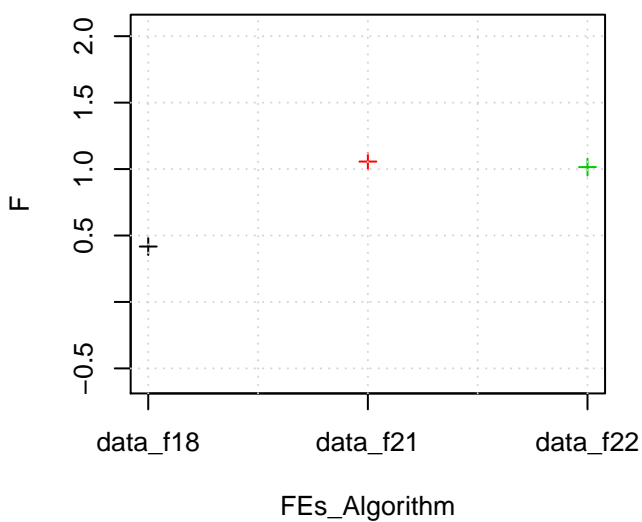
**DIM10-CMAES-b**



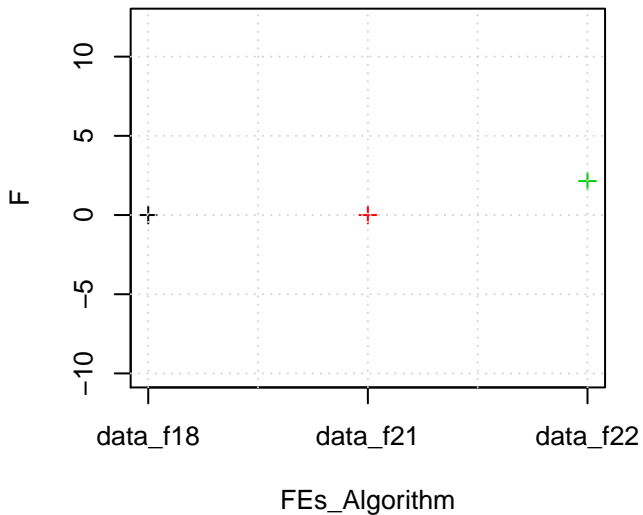
**DIM10-CMAES-c**



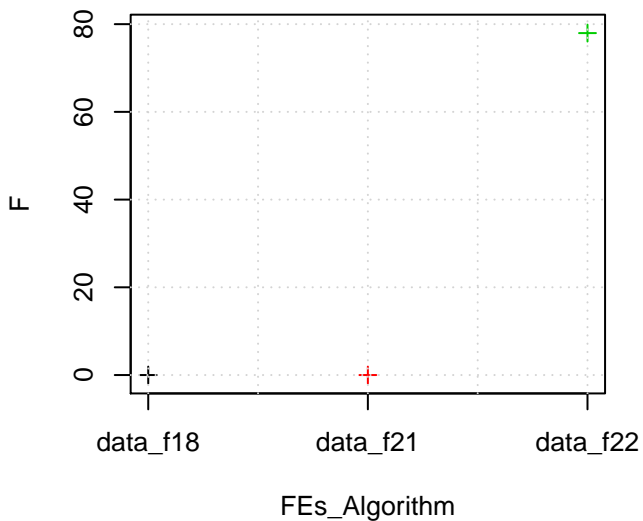
**DIM10-CMAES-d**



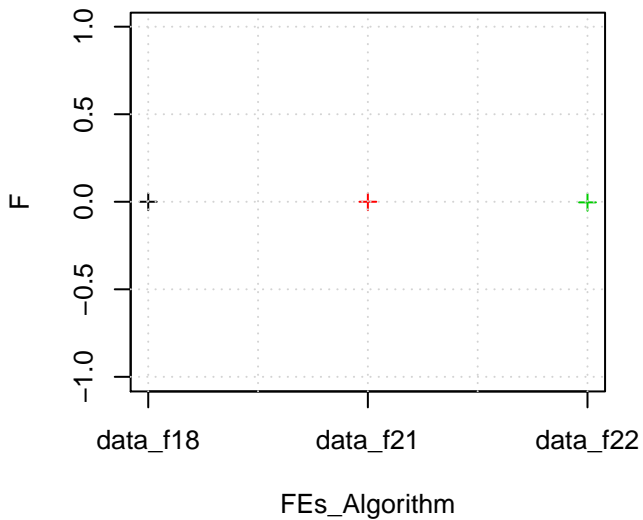
**DIM10-IPOP-a**



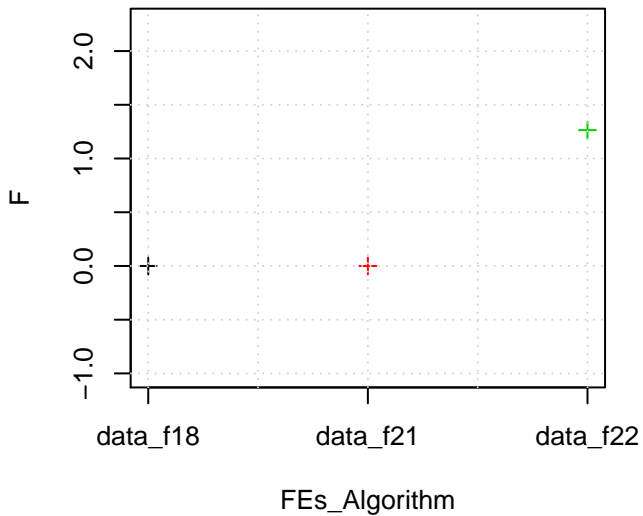
**DIM10-IPOP-b**



**DIM10-IPOP-c**

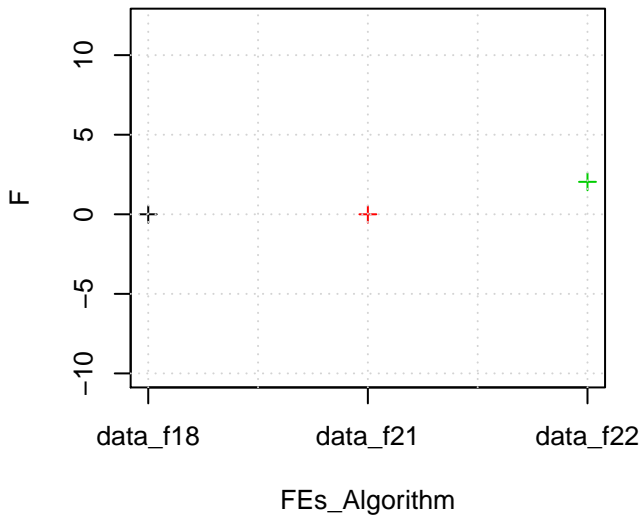


**DIM10-IPOP-d**

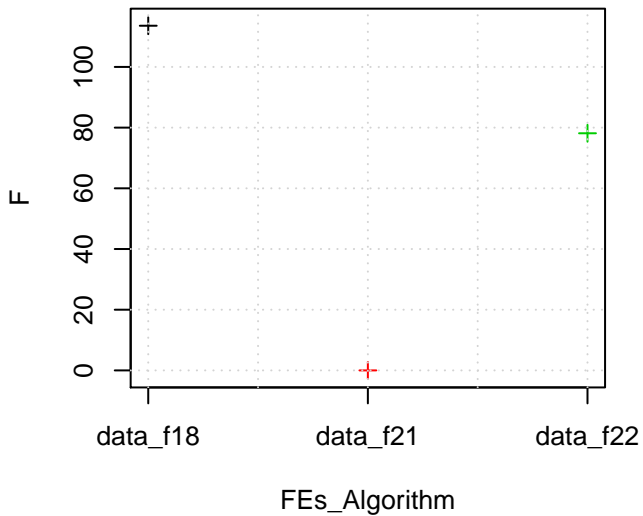




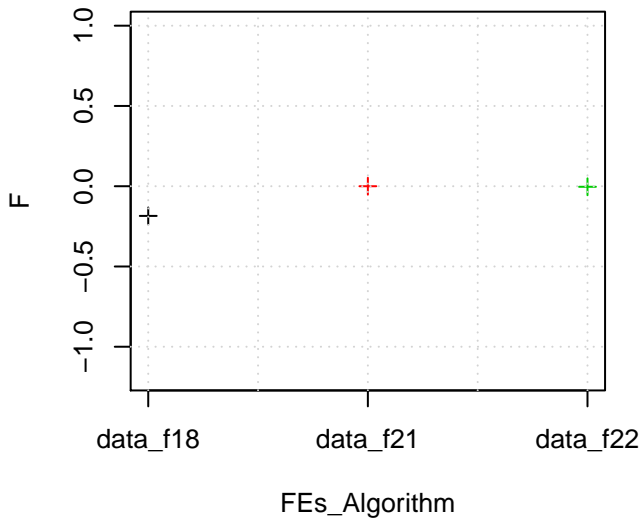
**DIM10-IPOP-500-a**



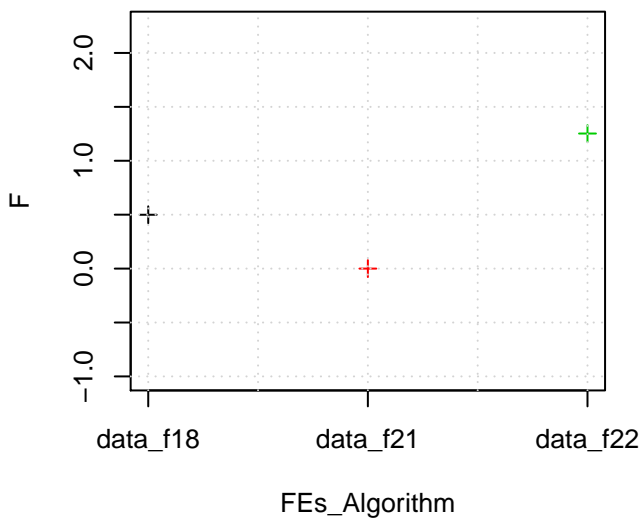
**DIM10-IPOP-500-b**



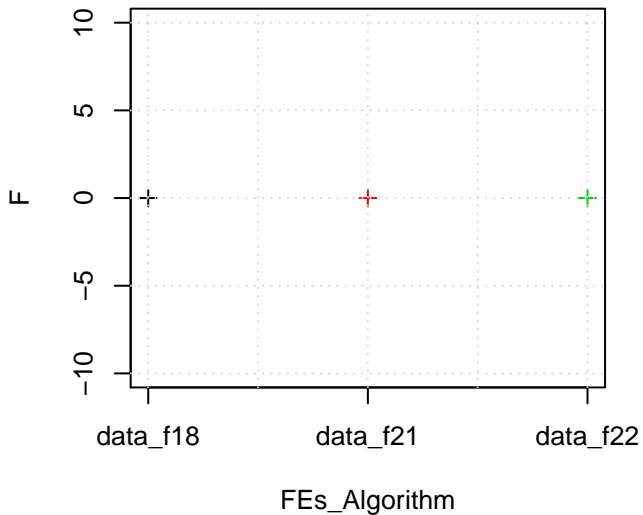
**DIM10-IPOP-500-c**



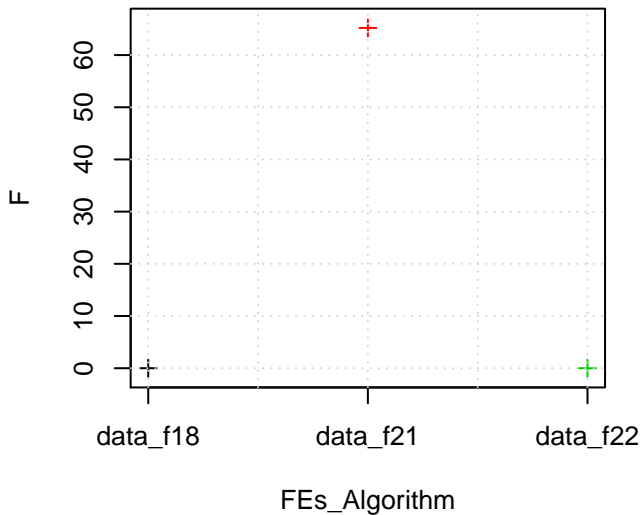
**DIM10-IPOP-500-d**



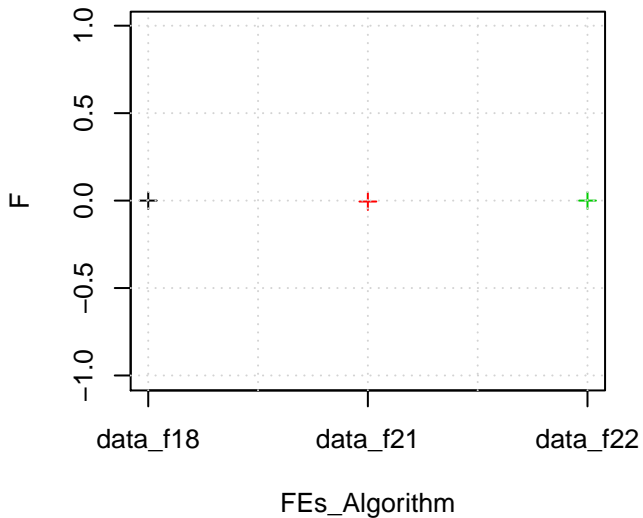
**DIM10-IPOP-tany-a**



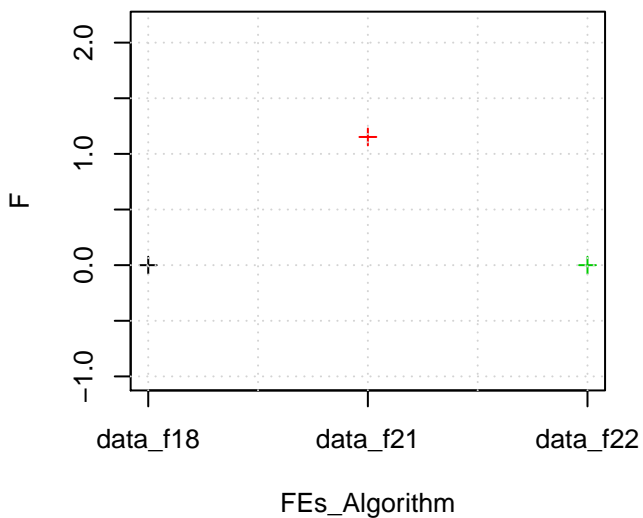
**DIM10-IPOP-tany-b**



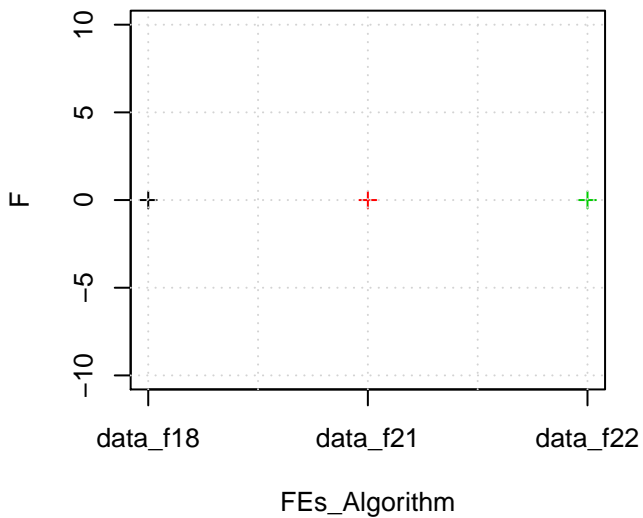
**DIM10-IPOP-tany-c**



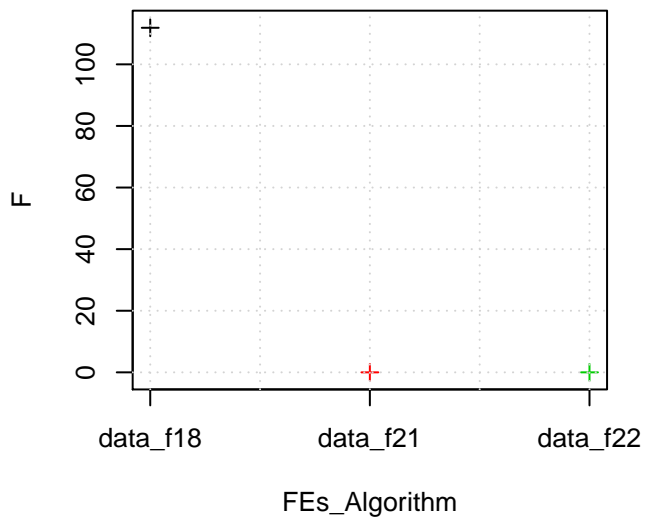
**DIM10-IPOP-tany-d**



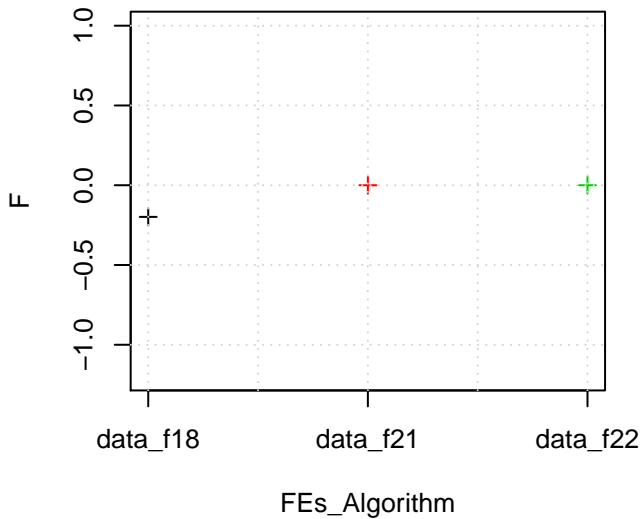
**DIM10-IPOP-texp-a**



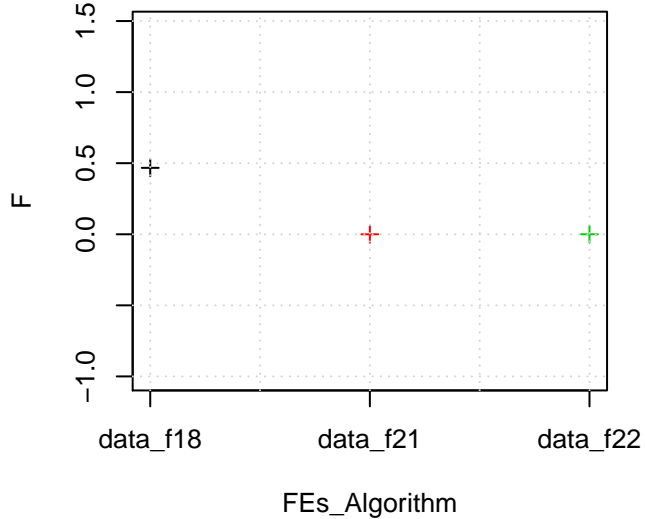
**DIM10-IPOP-texp-b**



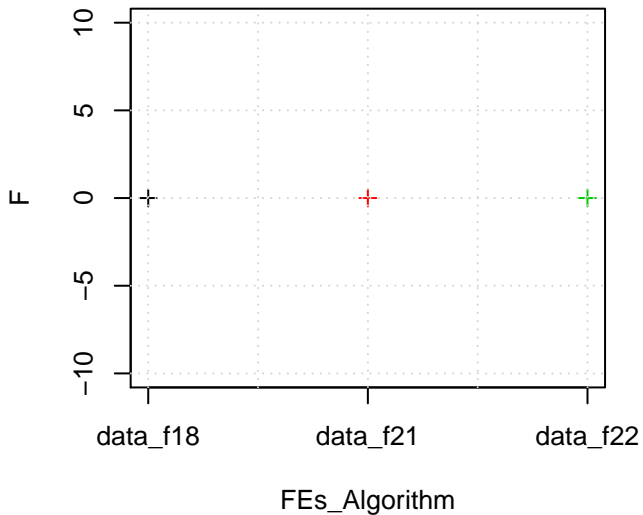
**DIM10-IPOP-texp-c**



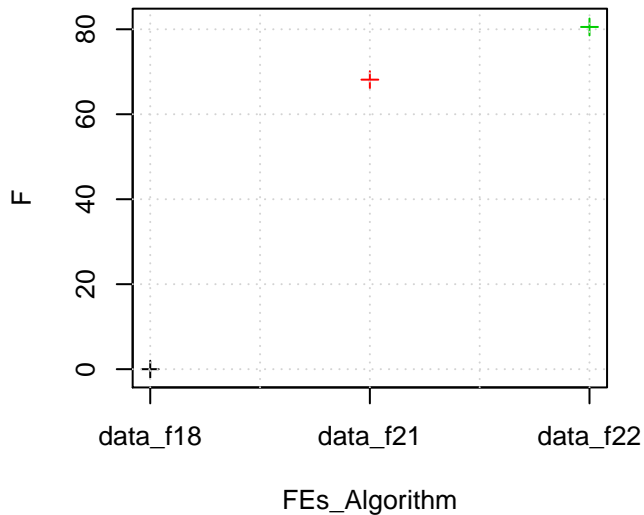
**DIM10-IPOP-texp-d**



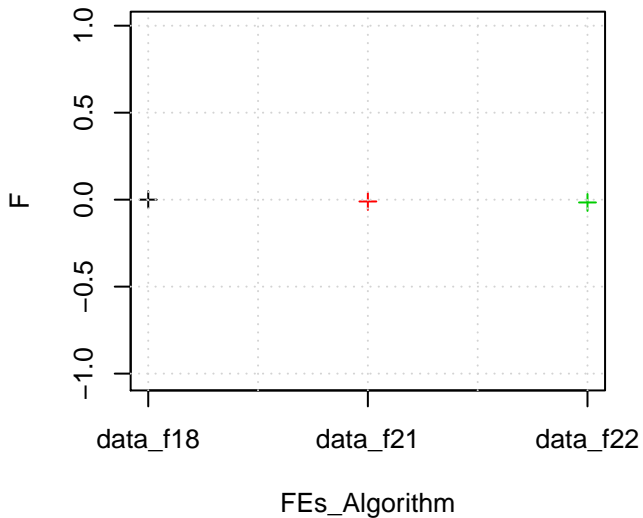
**DIM10-P-DCN-a**



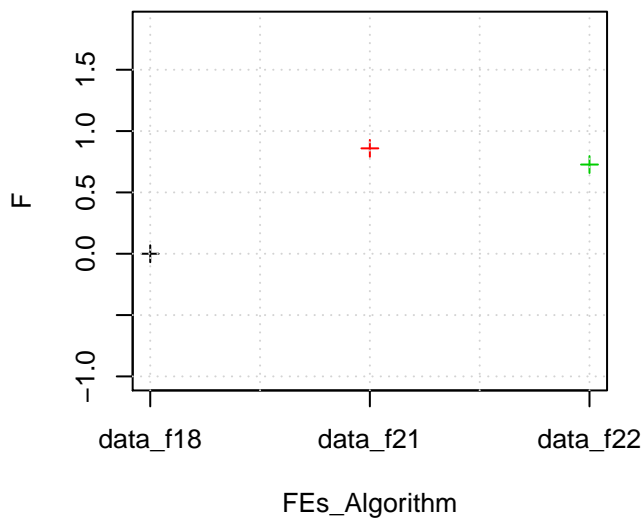
**DIM10-P-DCN-b**



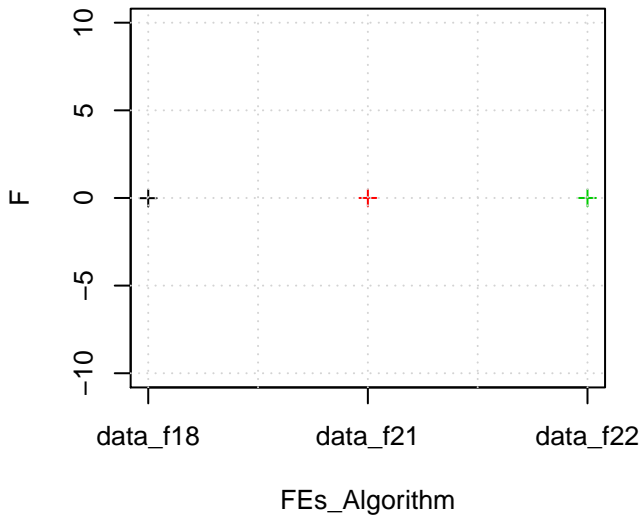
**DIM10-P-DCN-c**



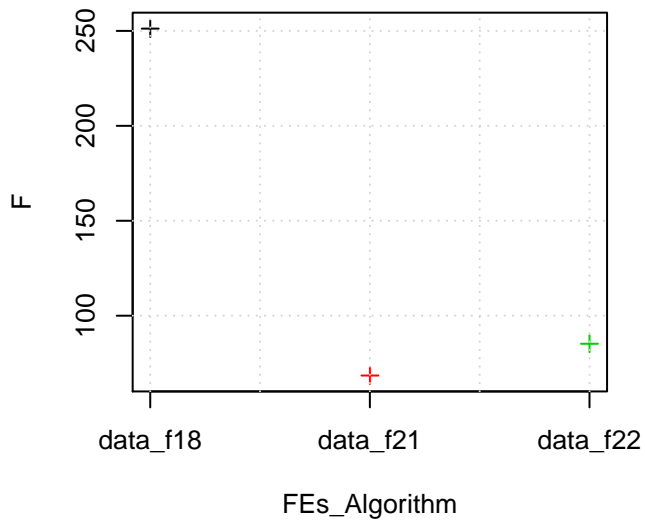
**DIM10-P-DCN-d**



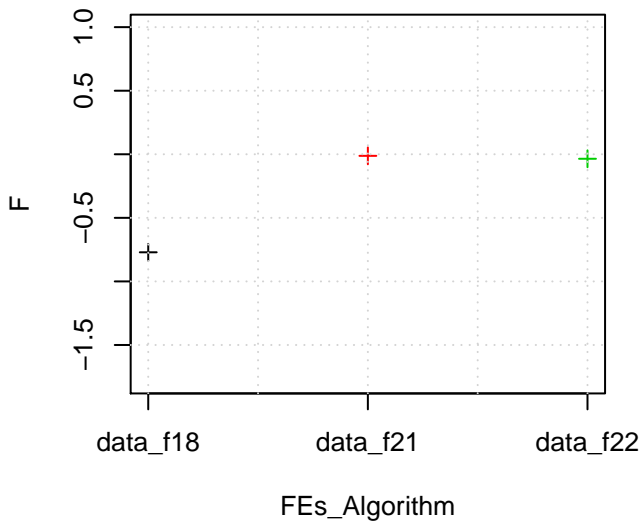
**DIM10-DE-a**



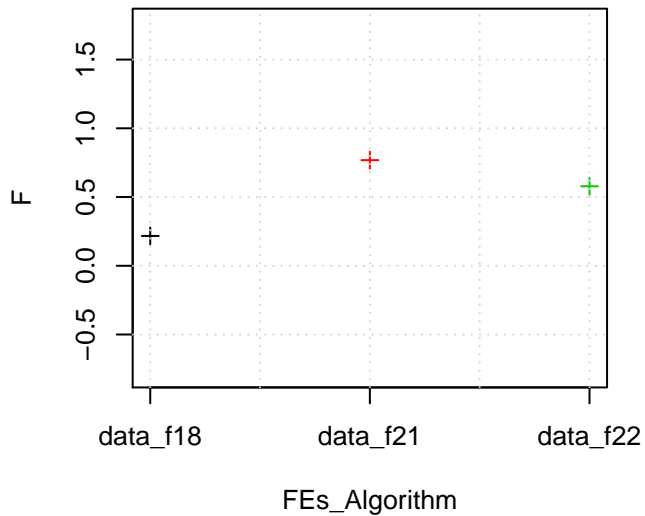
**DIM10-DE-b**



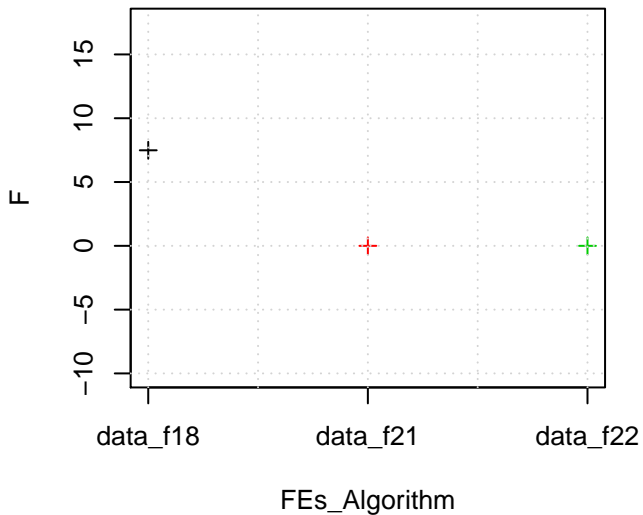
**DIM10-DE-c**



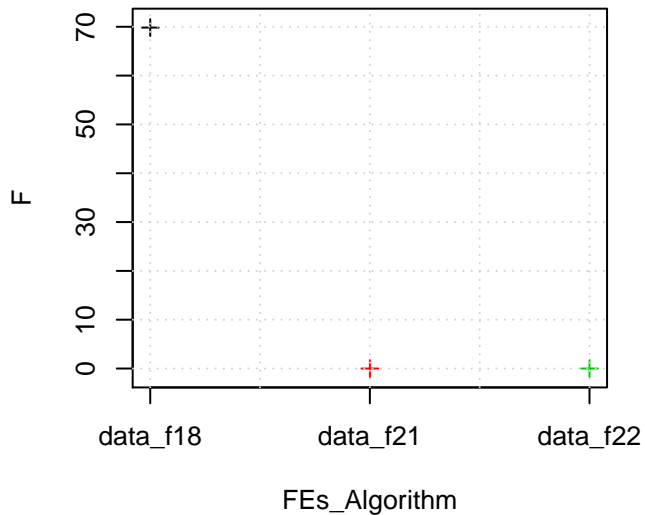
**DIM10-DE-d**



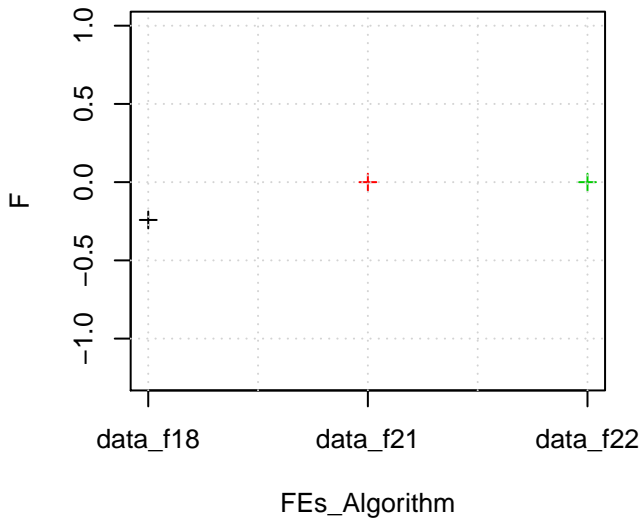
**DIM10-fmincon-a**



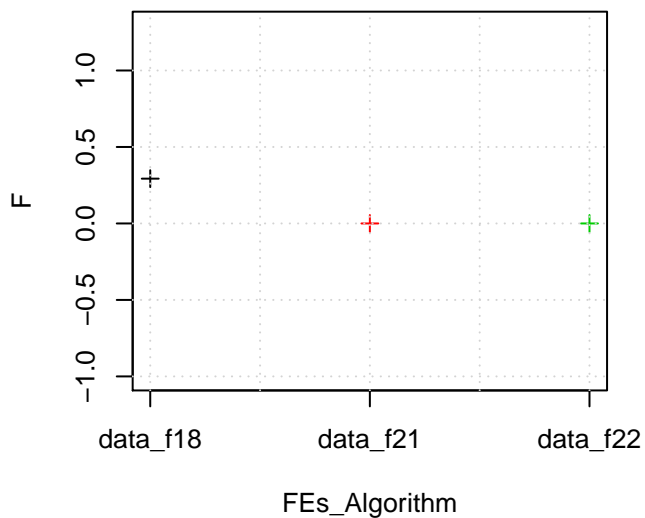
**DIM10-fmincon-b**



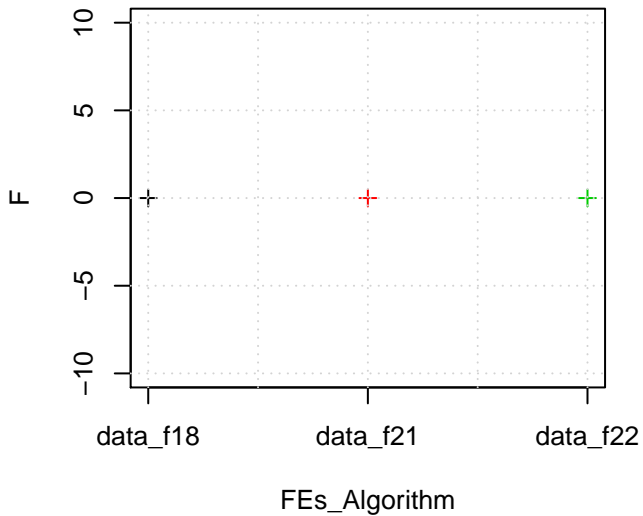
**DIM10-fmincon-c**



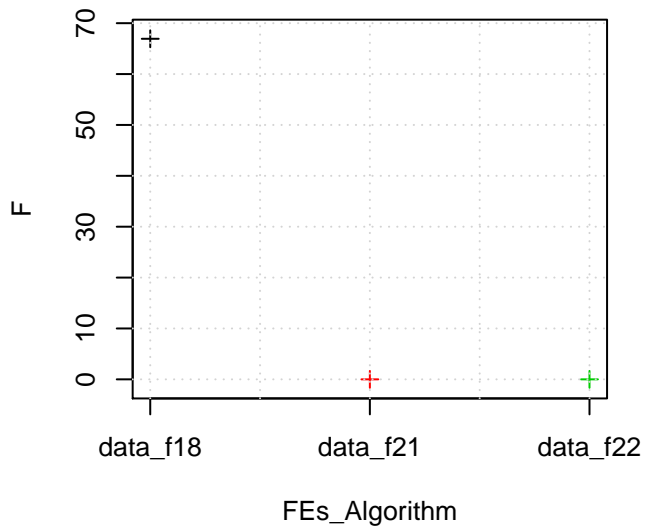
**DIM10-fmincon-d**



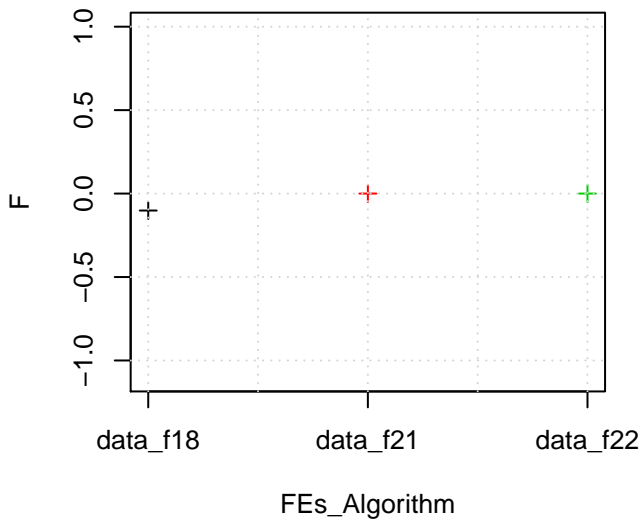
**DIM10-HMLSL-a**



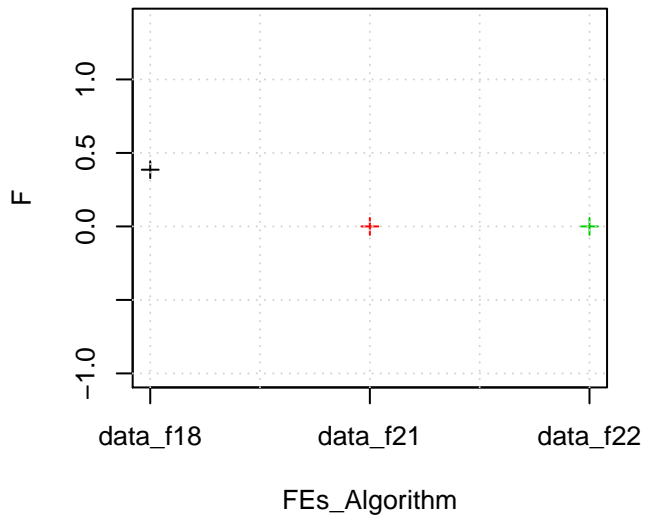
**DIM10-HMLSL-b**



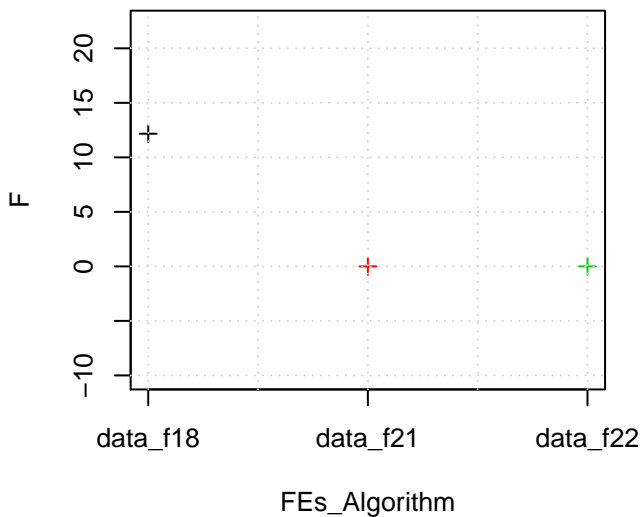
**DIM10-HMLSL-c**



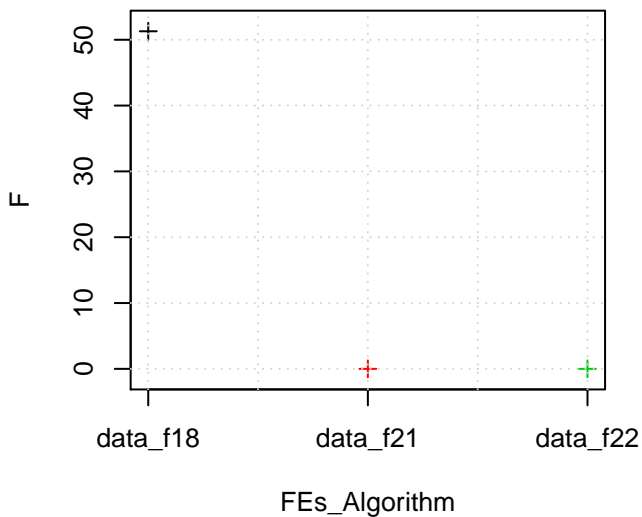
**DIM10-HMLSL-d**



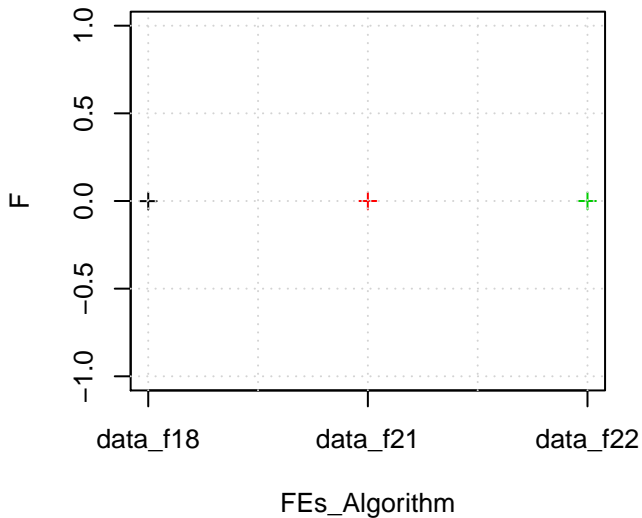
**DIM10-simplex-a**



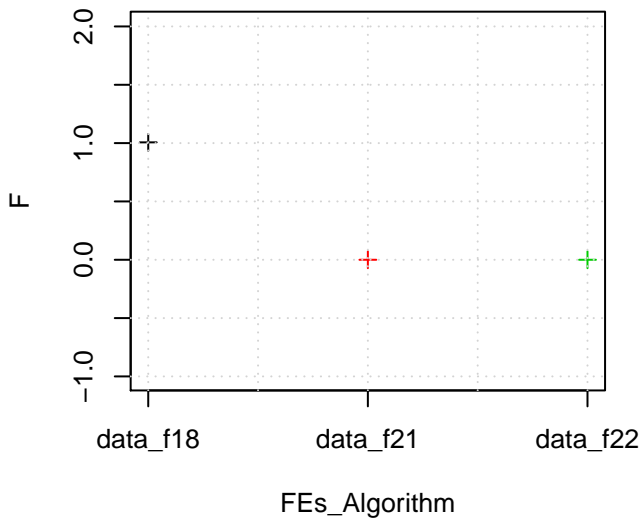
**DIM10-simplex-b**



**DIM10-simplex-c**

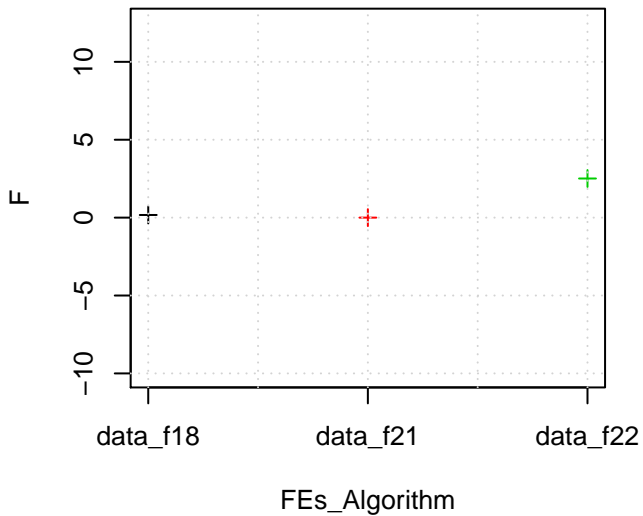


**DIM10-simplex-d**

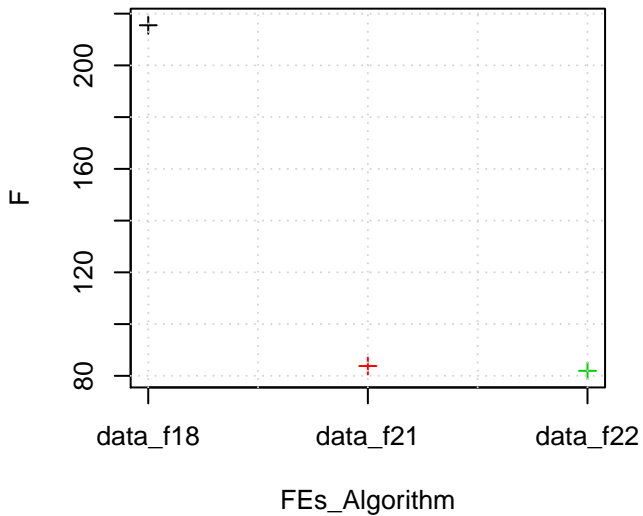




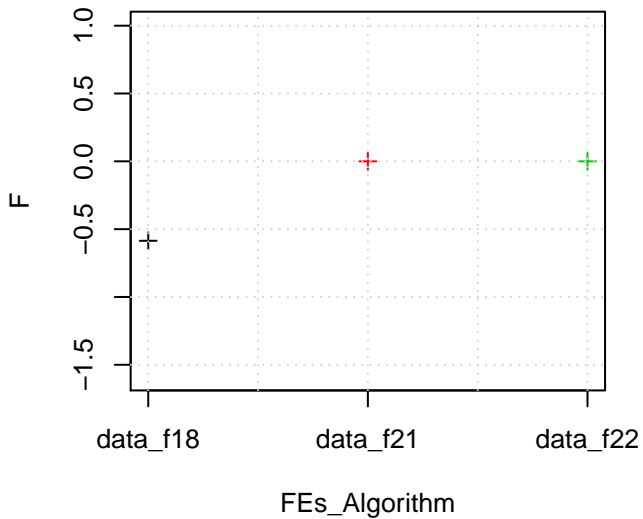
**DIM20-ga100-a**



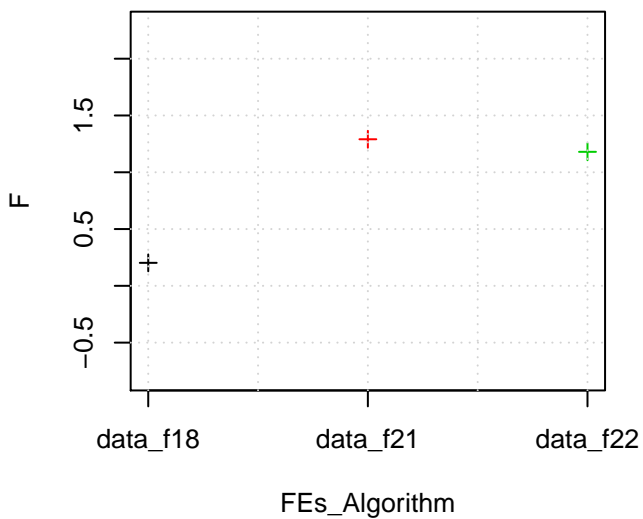
**DIM20-ga100-b**



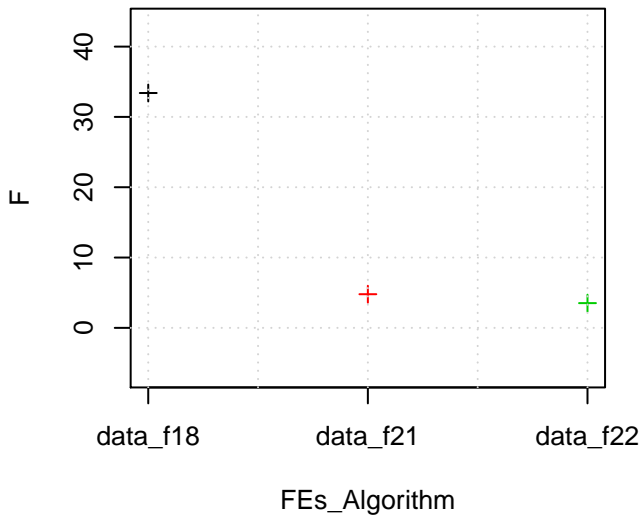
**DIM20-ga100-c**



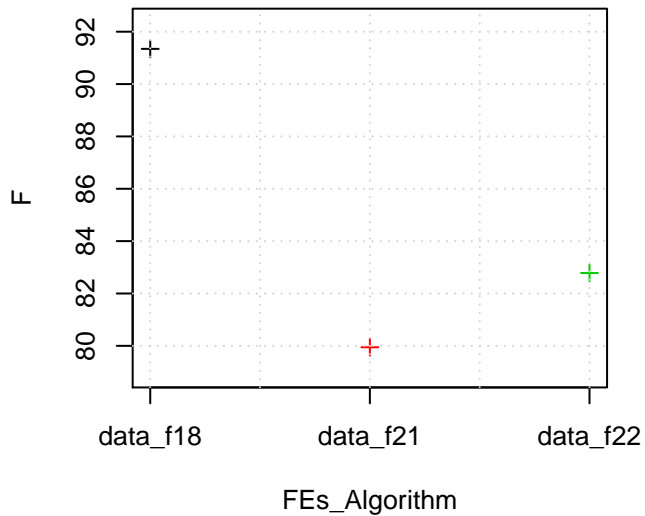
**DIM20-ga100-d**



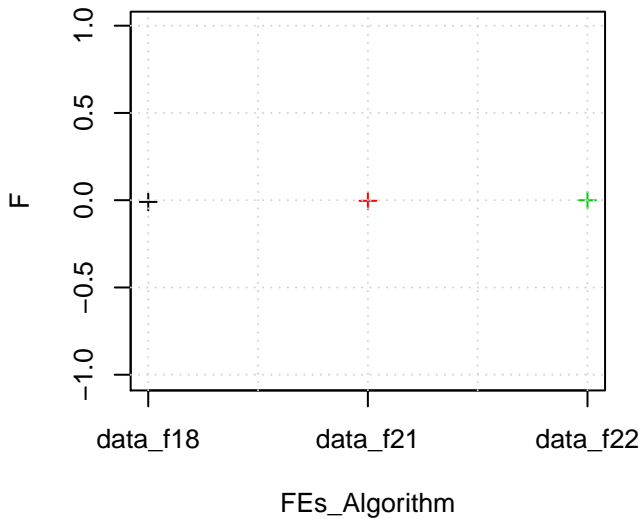
**DIM20-hill-a**



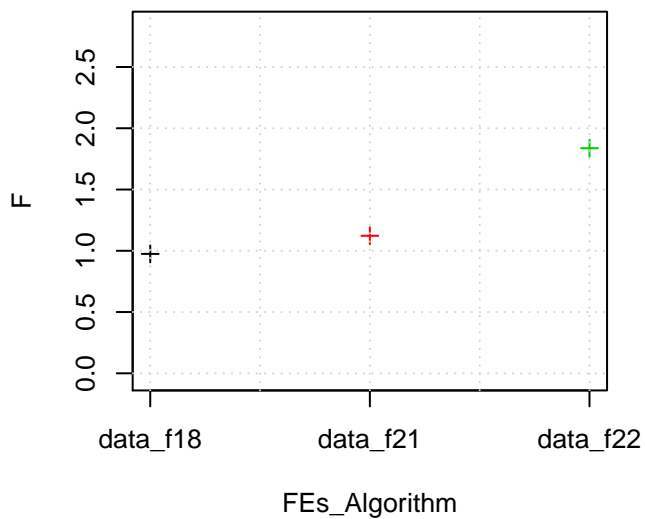
**DIM20-hill-b**



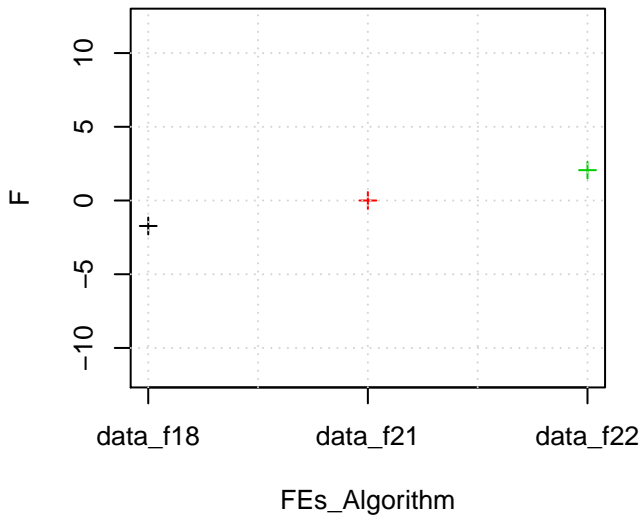
**DIM20-hill-c**



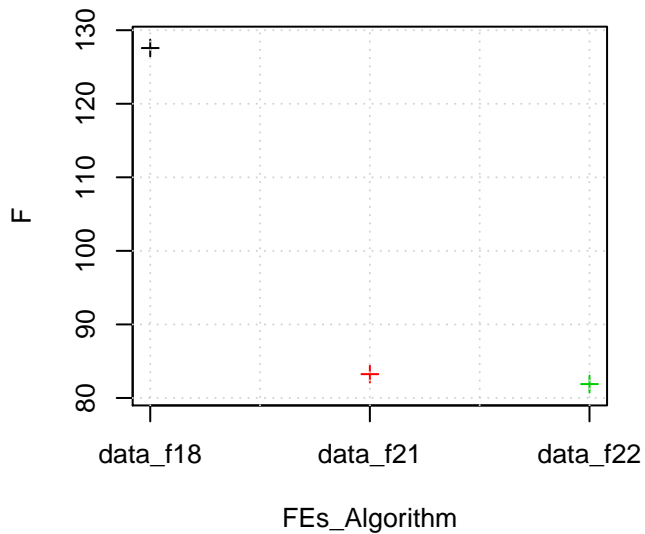
**DIM20-hill-d**



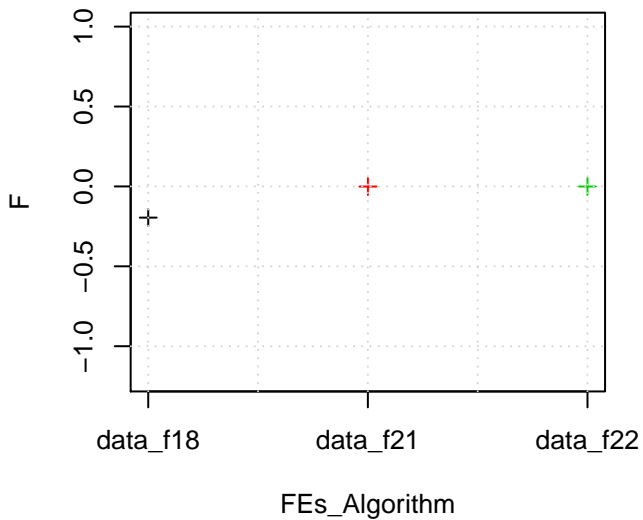
**DIM20-CMAES-a**



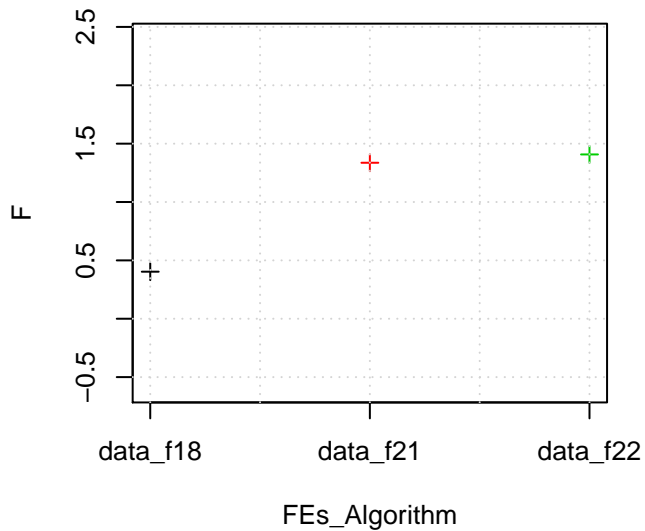
**DIM20-CMAES-b**



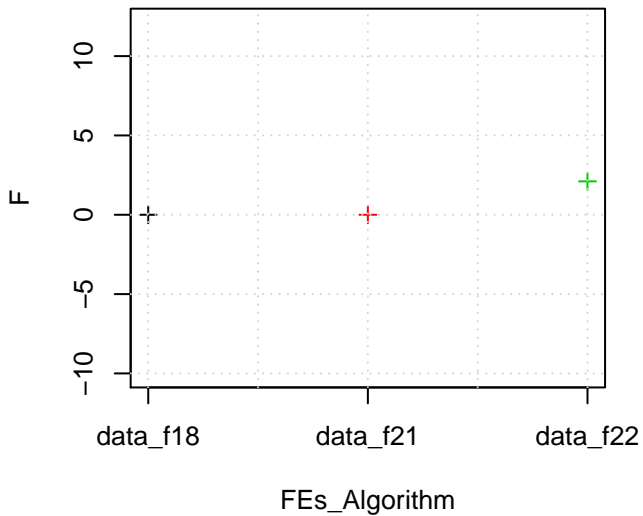
**DIM20-CMAES-c**



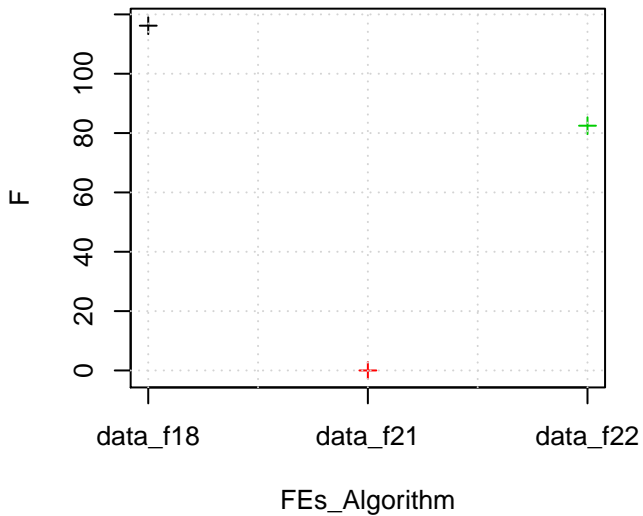
**DIM20-CMAES-d**



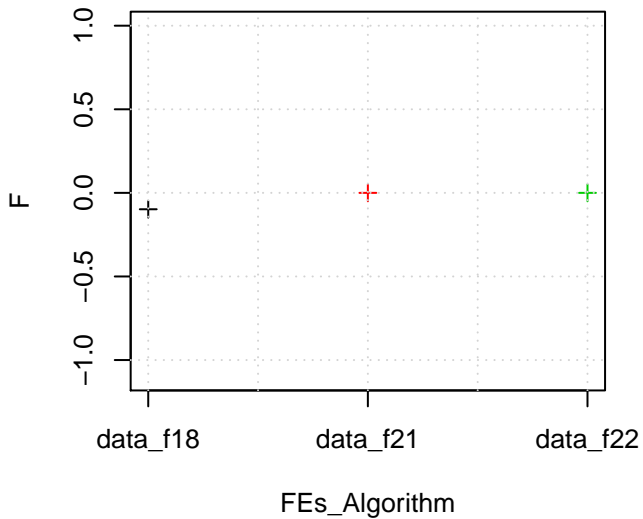
**DIM20-IPOP-a**



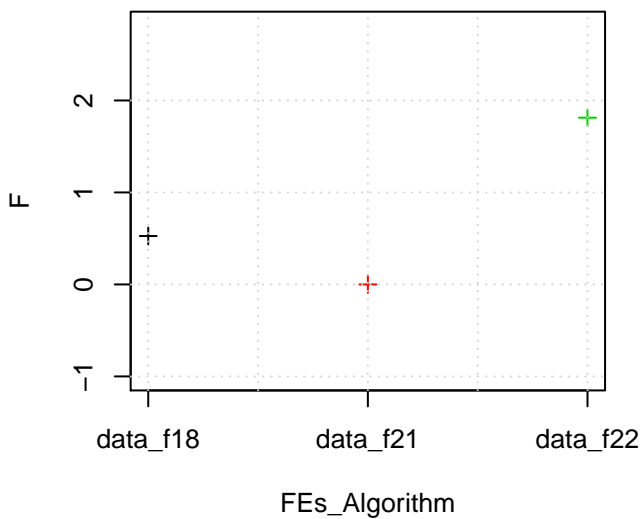
**DIM20-IPOP-b**



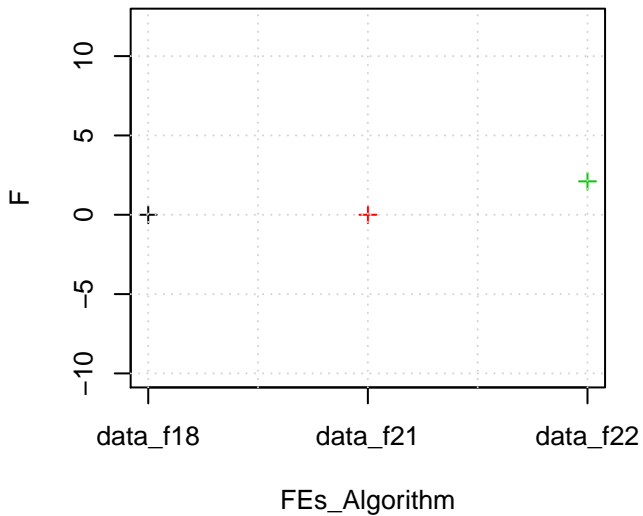
**DIM20-IPOP-c**



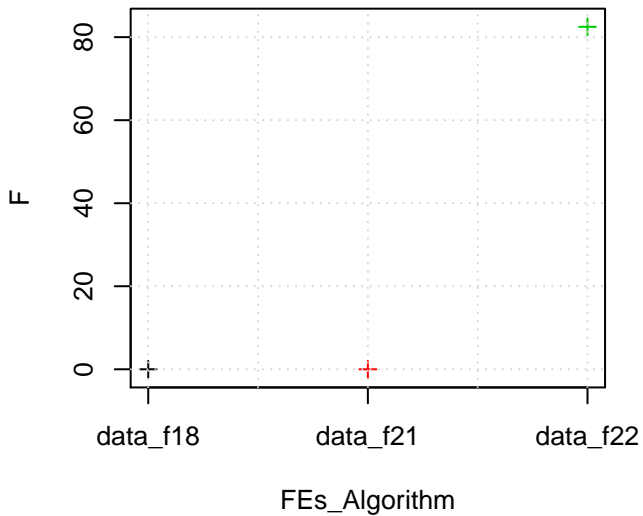
**DIM20-IPOP-d**



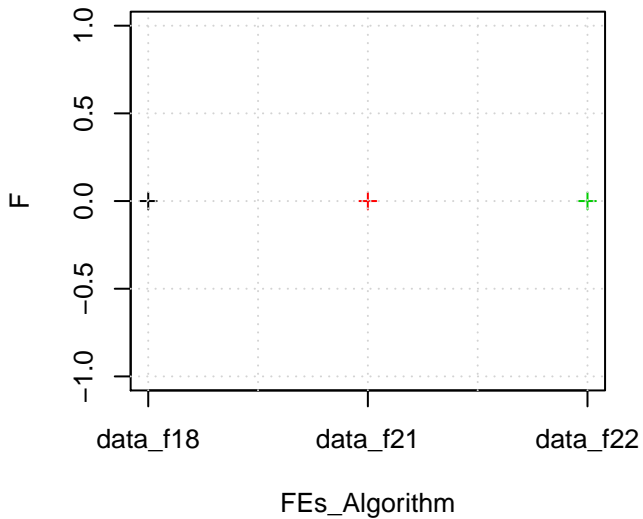
**DIM20-IPOP-500-a**



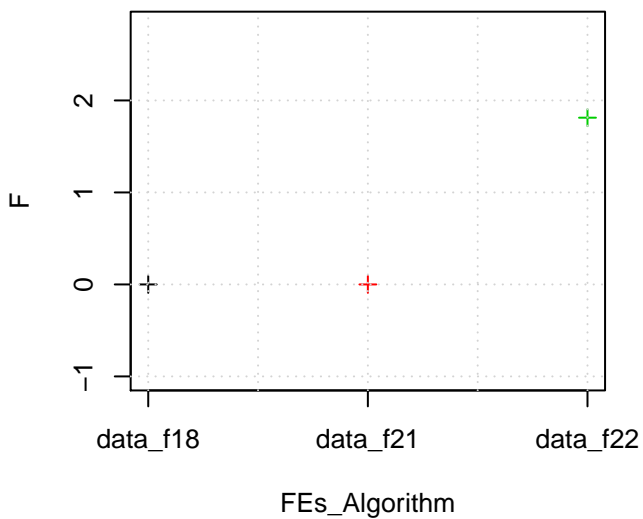
**DIM20-IPOP-500-b**



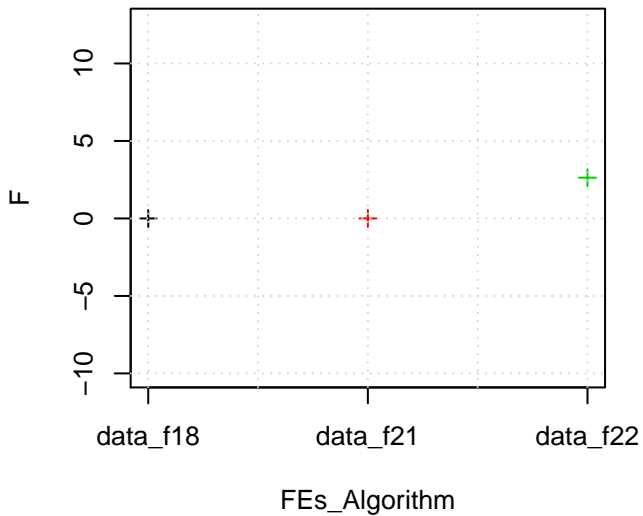
**DIM20-IPOP-500-c**



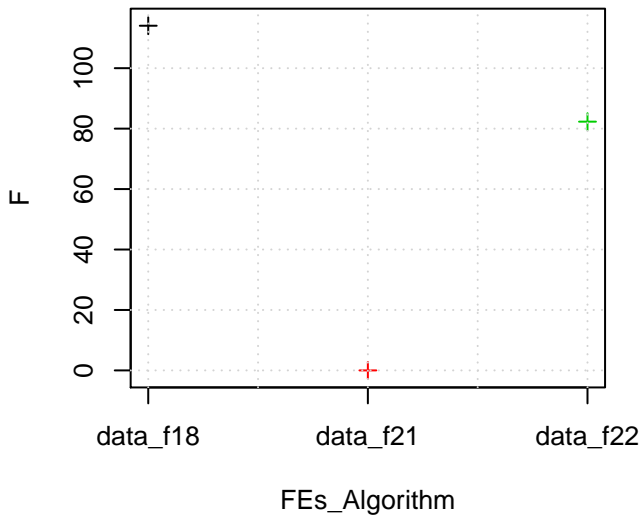
**DIM20-IPOP-500-d**



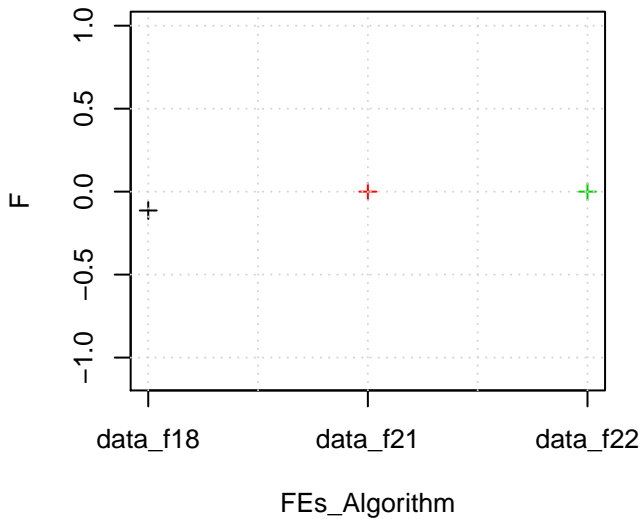
**DIM20-IPOP-tany-a**



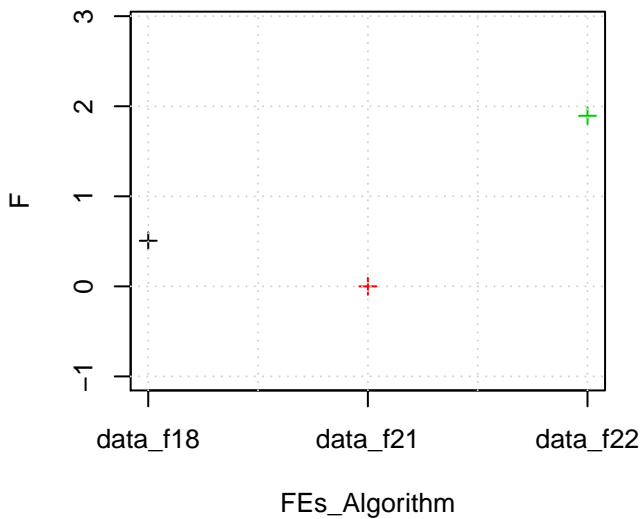
**DIM20-IPOP-tany-b**



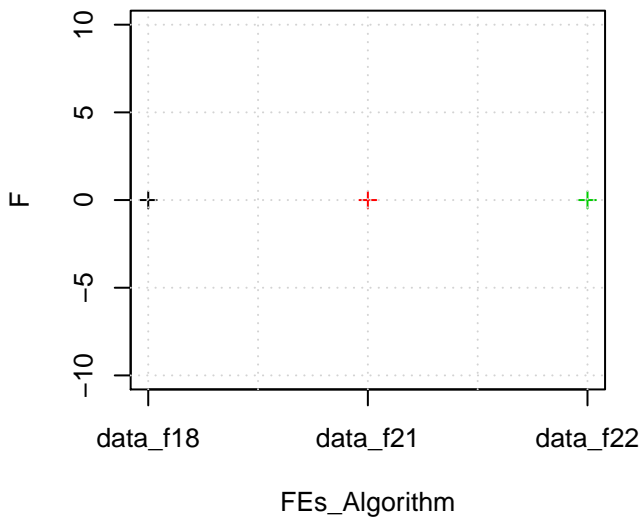
**DIM20-IPOP-tany-c**



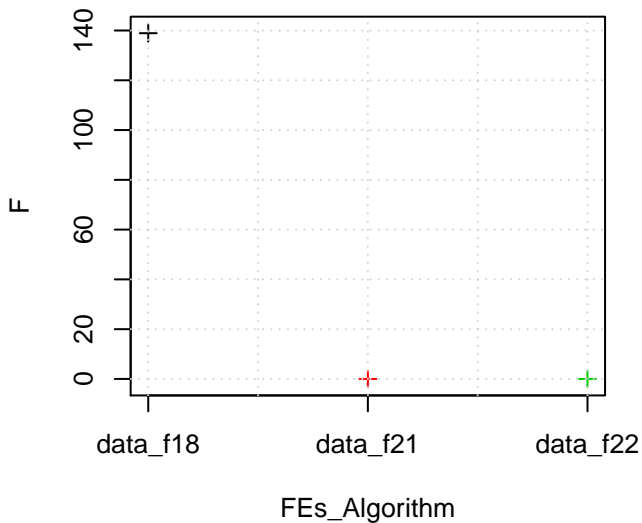
**DIM20-IPOP-tany-d**



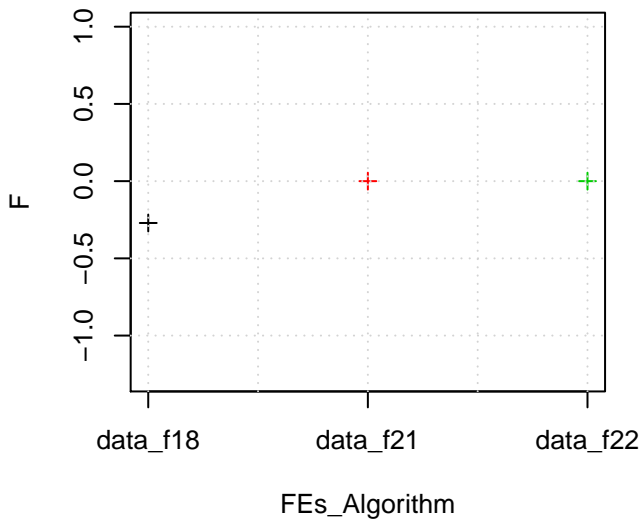
**DIM20-IPOP-texp-a**



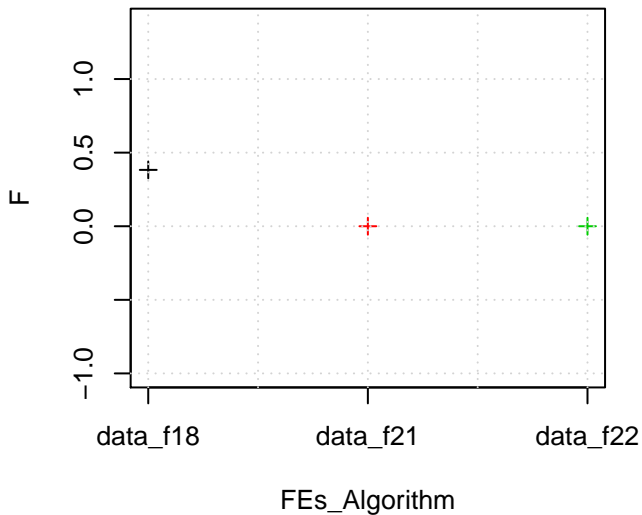
**DIM20-IPOP-texp-b**



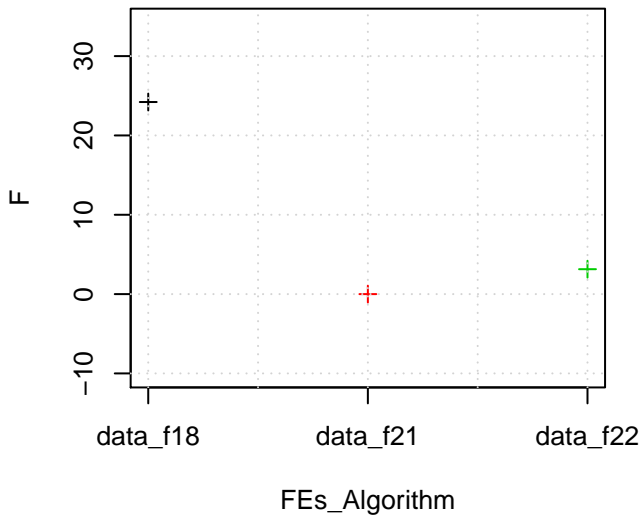
**DIM20-IPOP-texp-c**



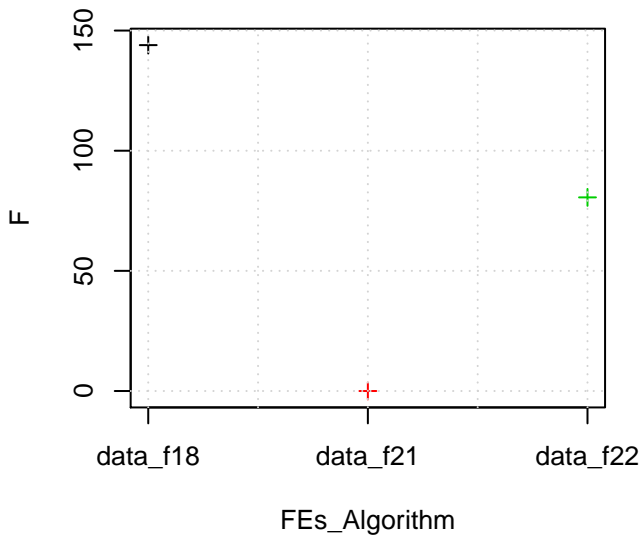
**DIM20-IPOP-texp-d**



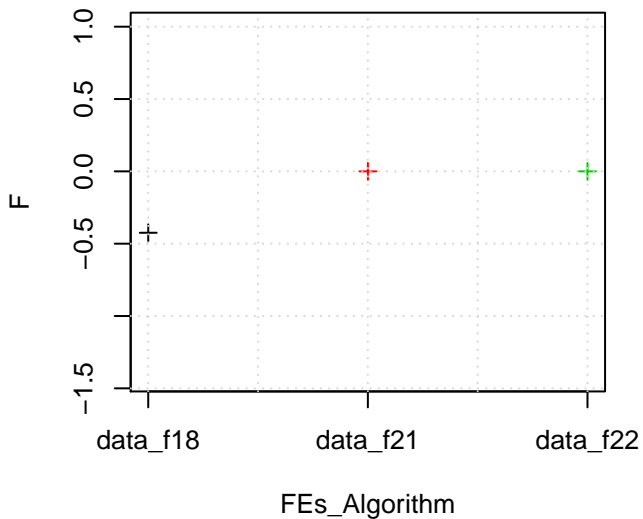
**DIM20-P-DCN-a**



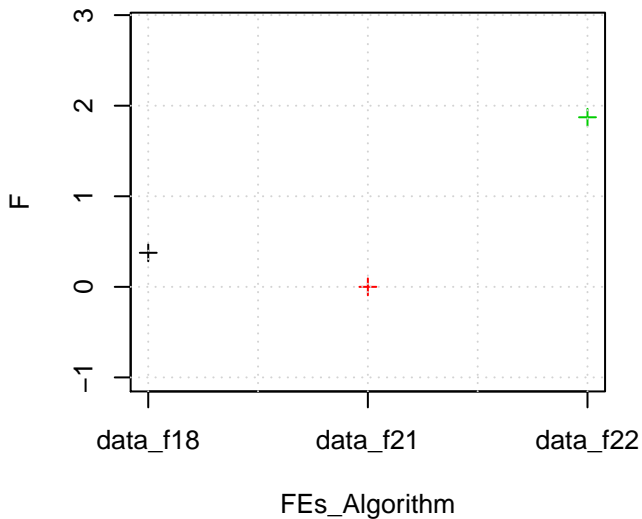
**DIM20-P-DCN-b**



**DIM20-P-DCN-c**

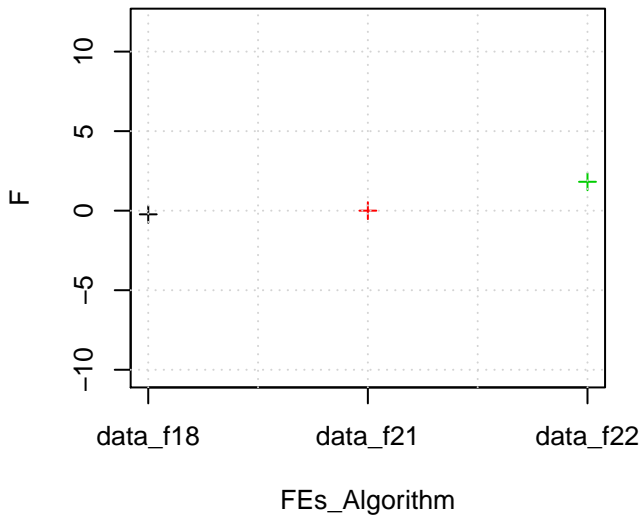


**DIM20-P-DCN-d**

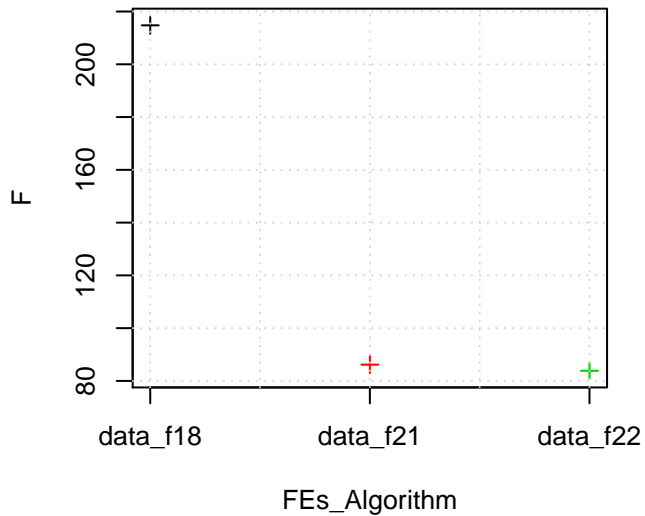




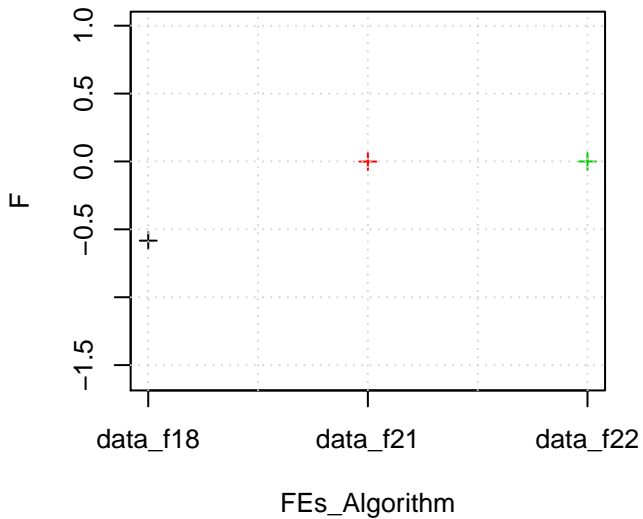
**DIM20-DE-a**



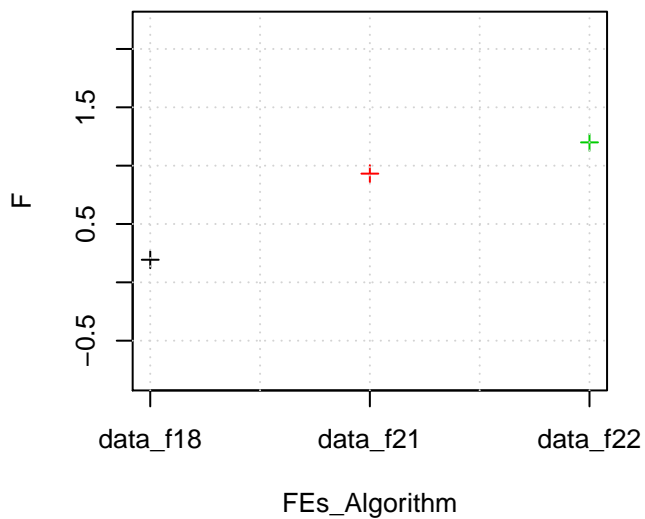
**DIM20-DE-b**



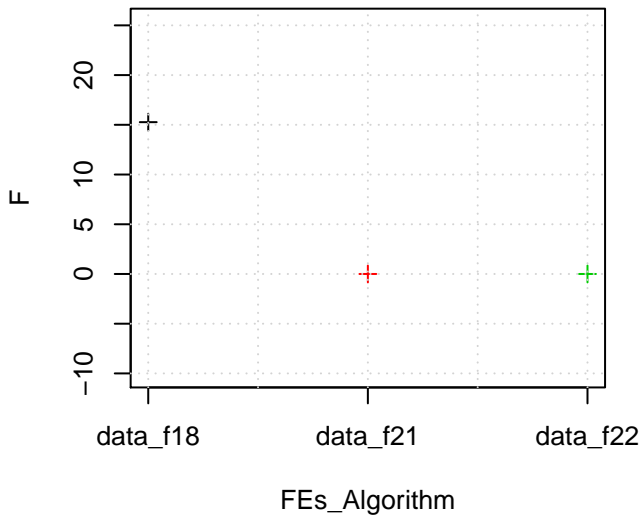
**DIM20-DE-c**



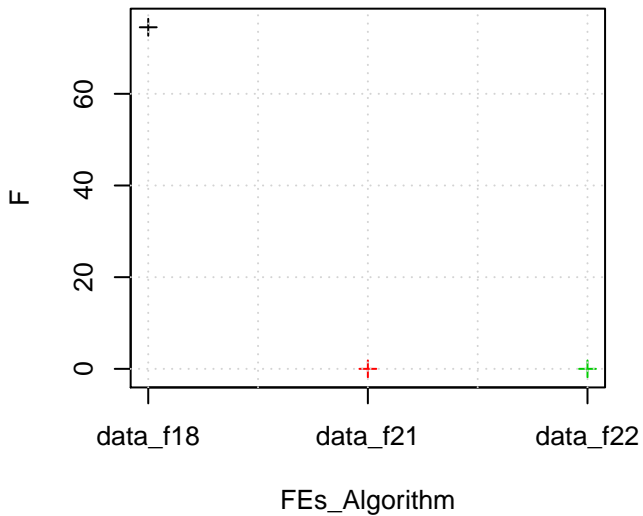
**DIM20-DE-d**



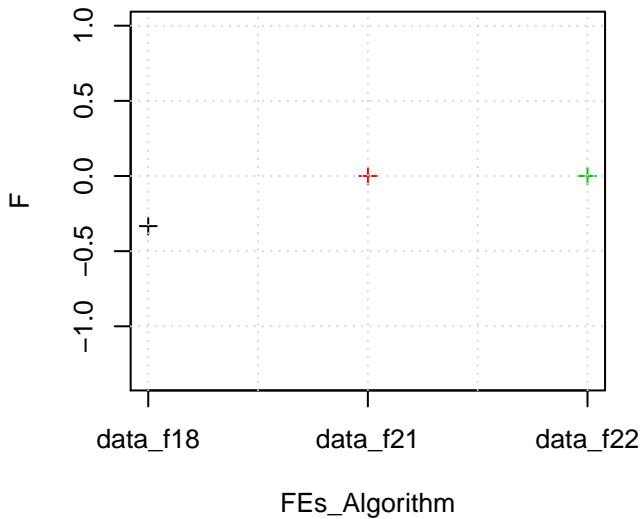
**DIM20-fmincon-a**



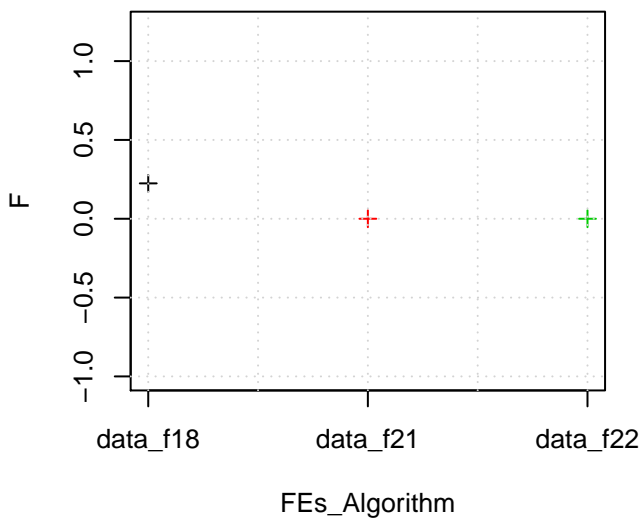
**DIM20-fmincon-b**



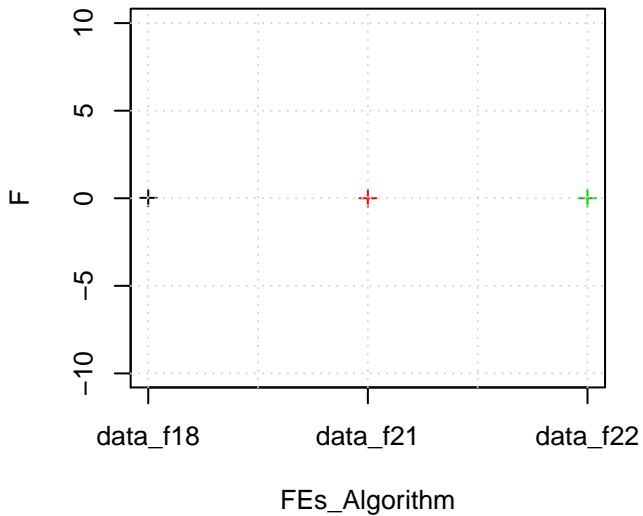
**DIM20-fmincon-c**



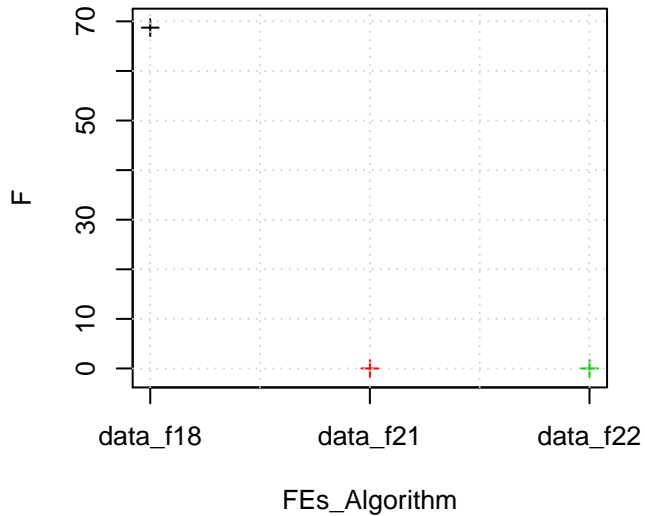
**DIM20-fmincon-d**



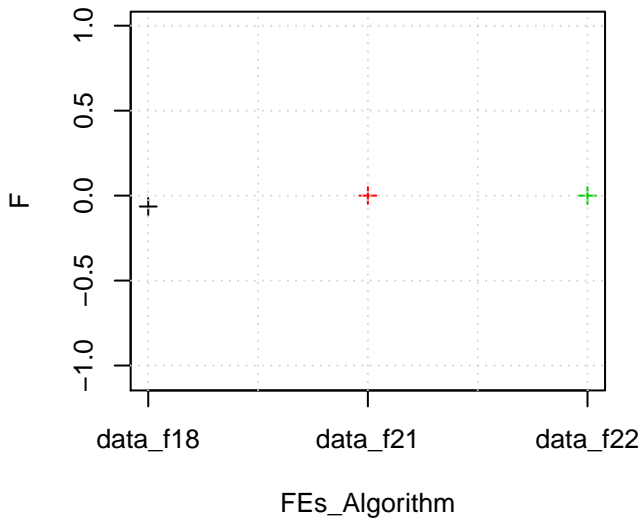
**DIM20-HMLSL-a**



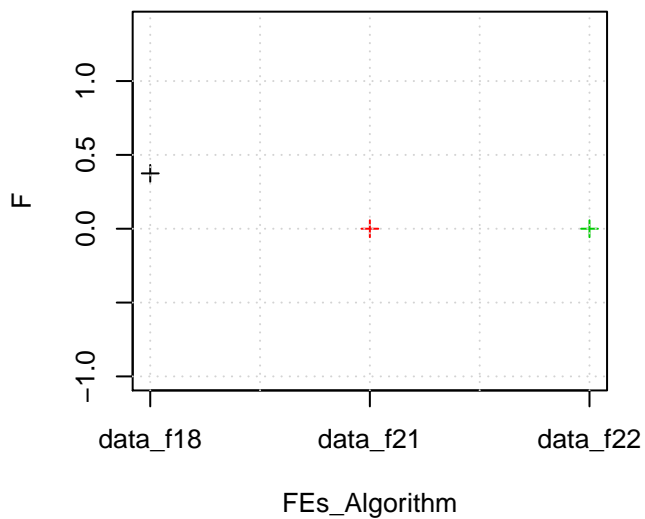
**DIM20-HMLSL-b**



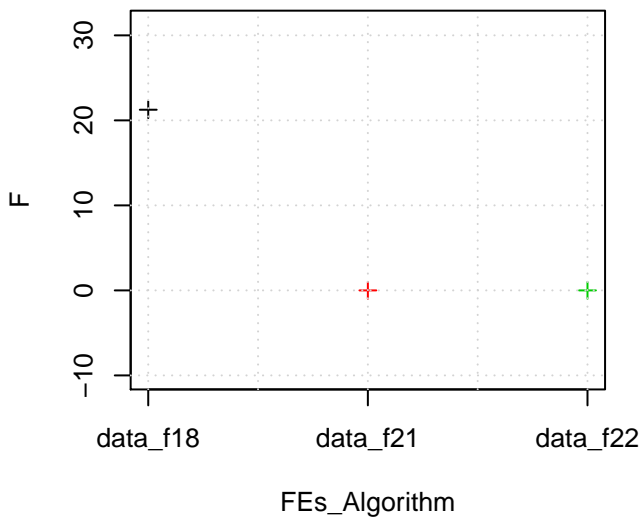
**DIM20-HMLSL-c**



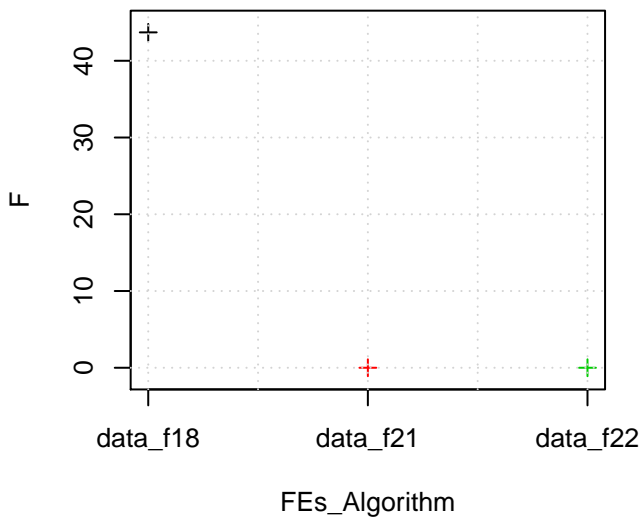
**DIM20-HMLSL-d**



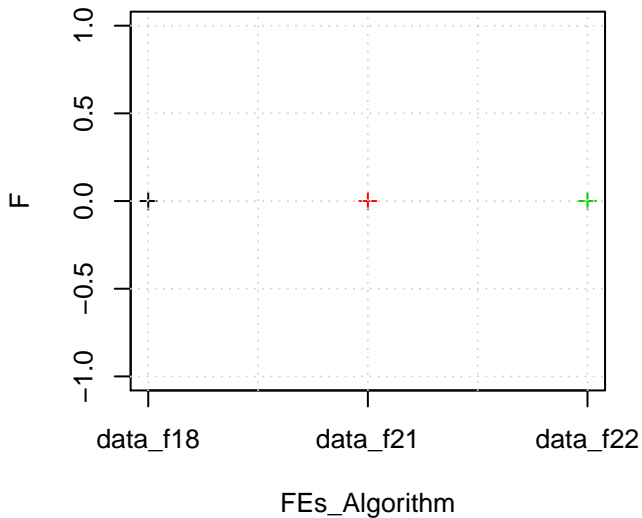
**DIM20-simplex-a**



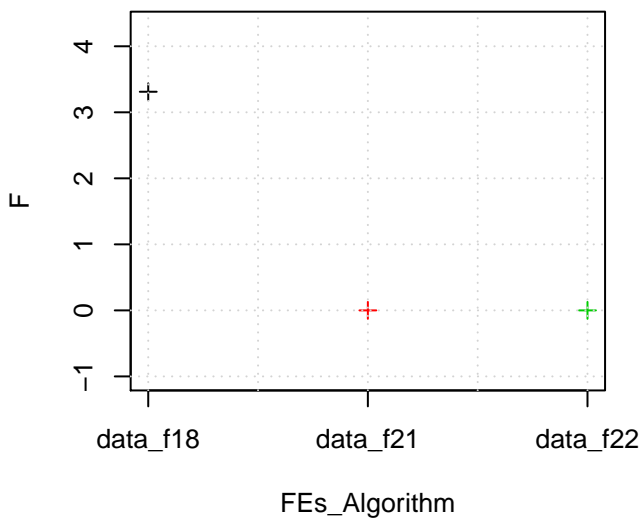
**DIM20-simplex-b**



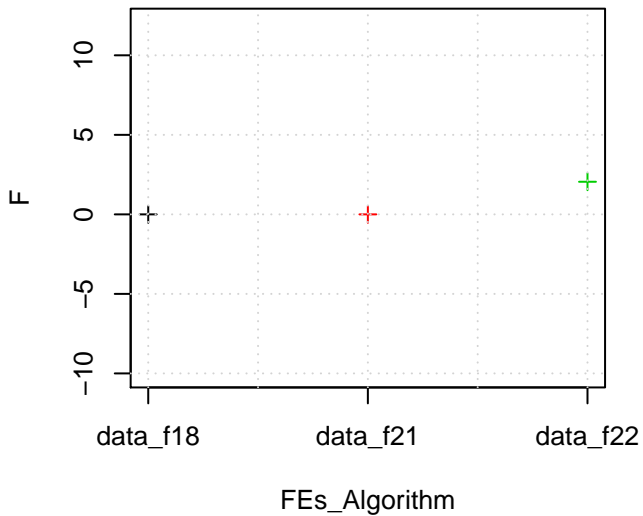
**DIM20-simplex-c**



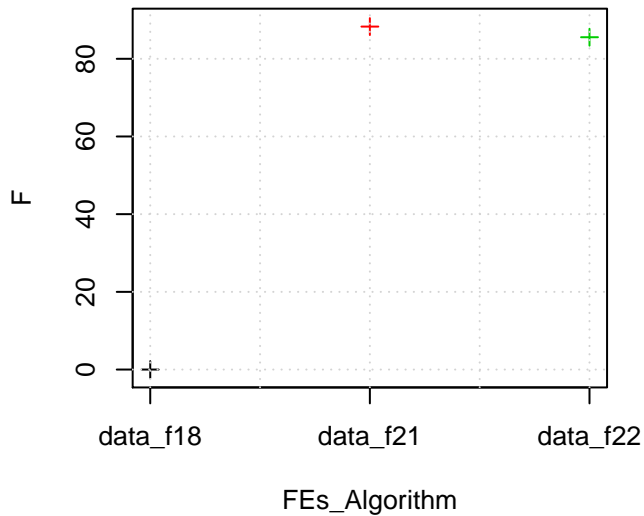
**DIM20-simplex-d**



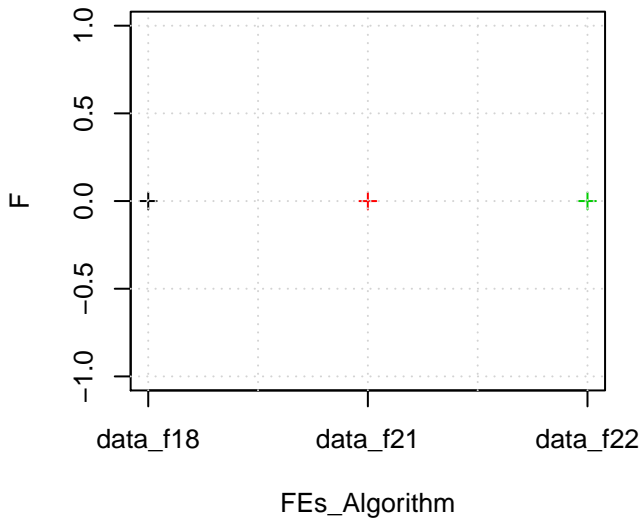
**DIM40-ga100-a**



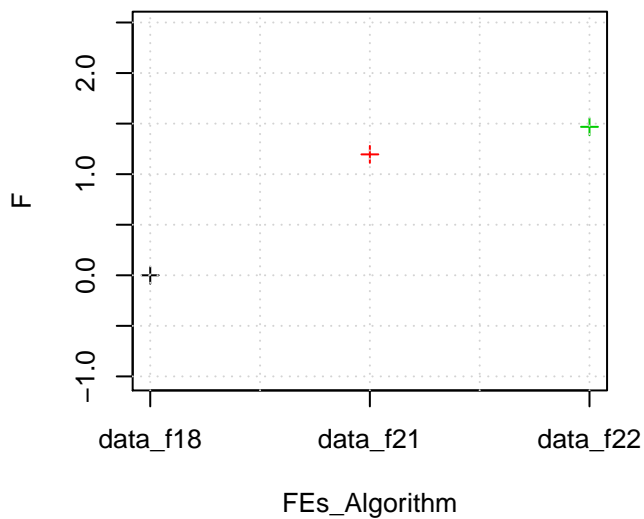
**DIM40-ga100-b**



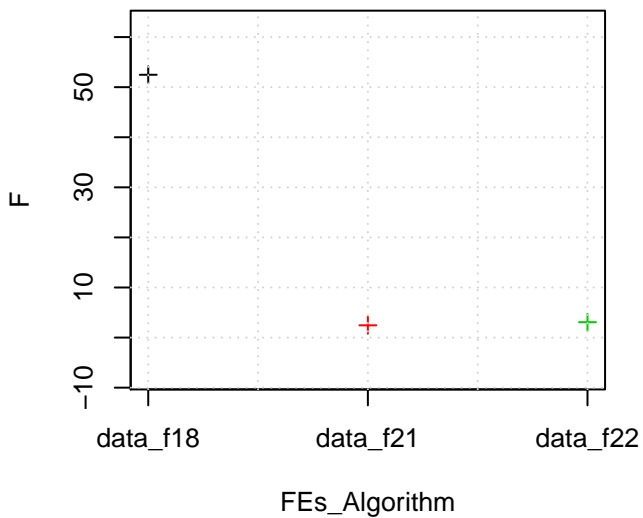
**DIM40-ga100-c**



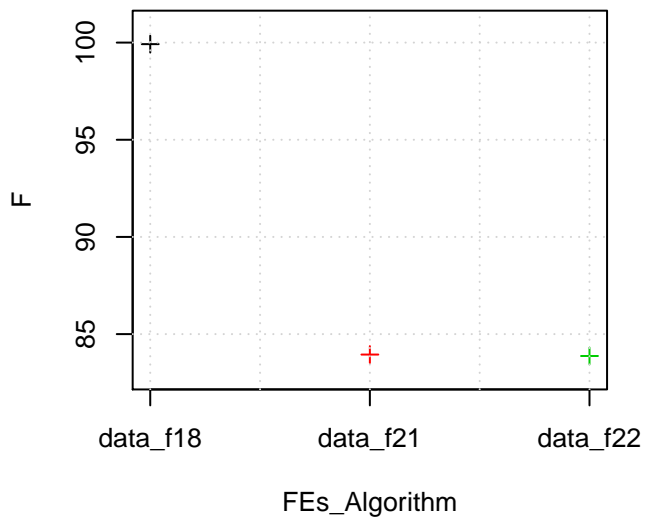
**DIM40-ga100-d**



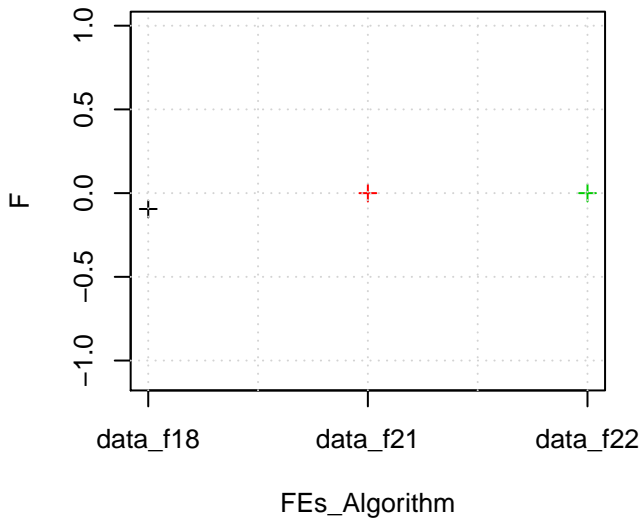
**DIM40-hill-a**



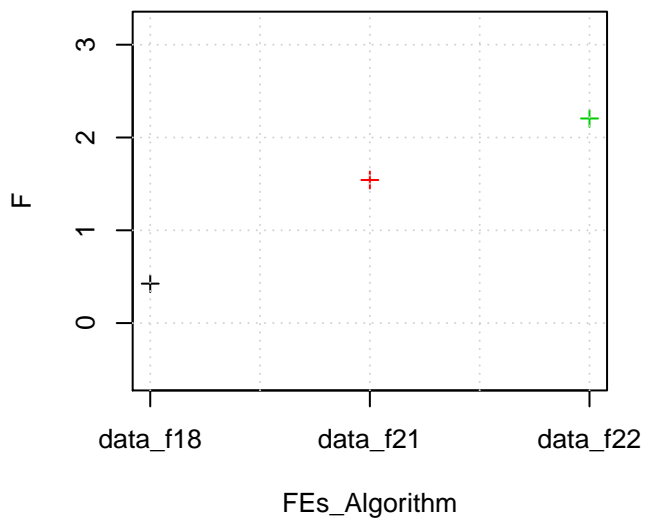
**DIM40-hill-b**



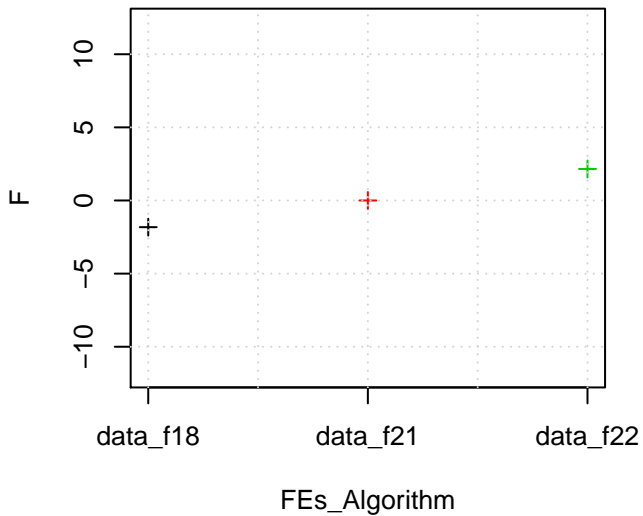
**DIM40-hill-c**



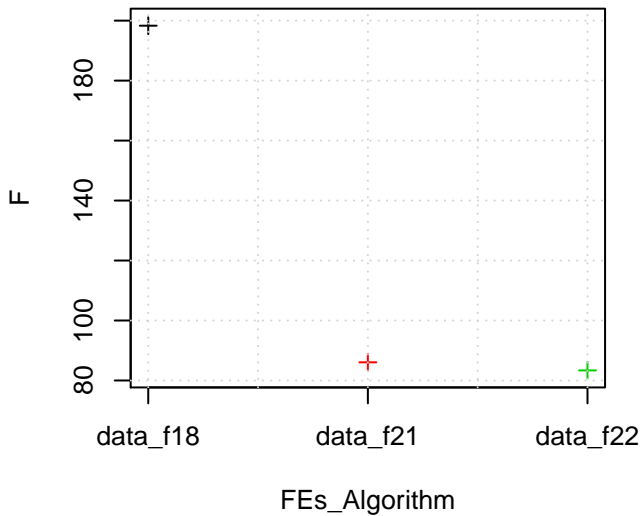
**DIM40-hill-d**



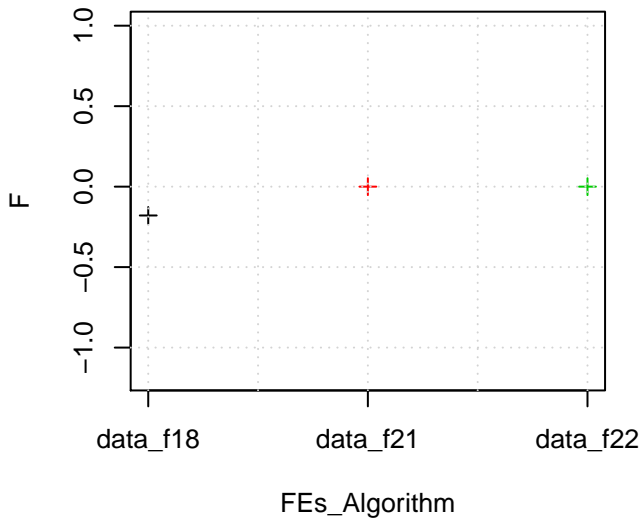
**DIM40-CMAES-a**



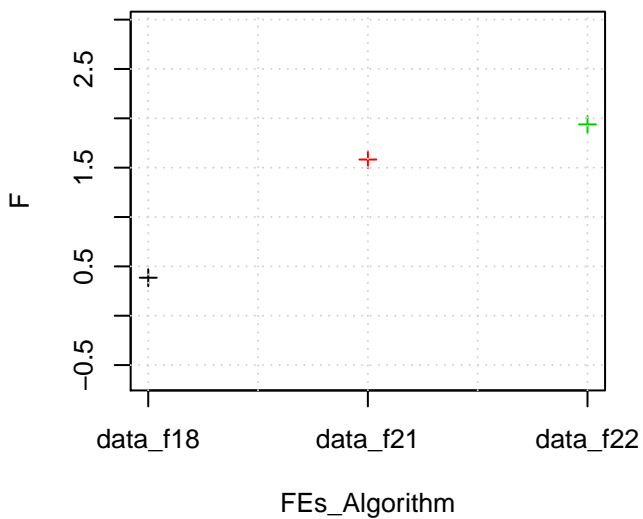
**DIM40-CMAES-b**



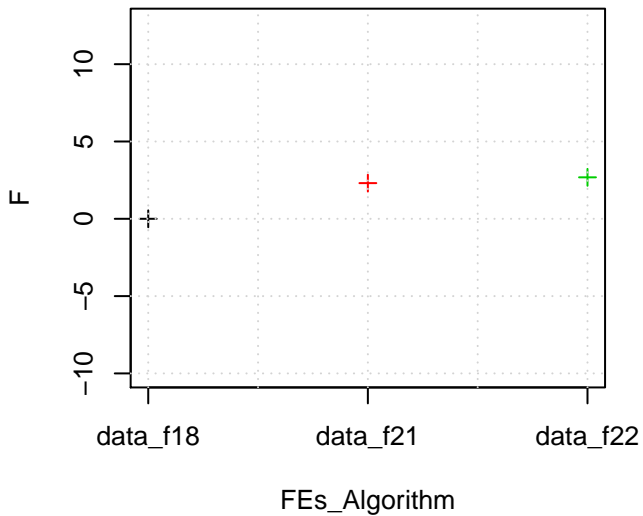
**DIM40-CMAES-c**



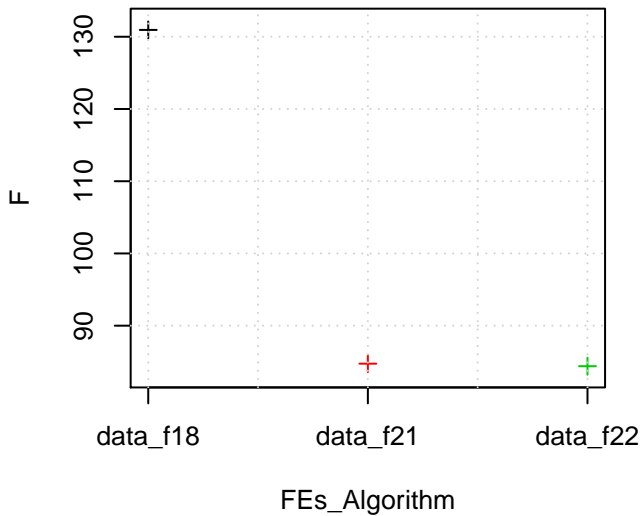
**DIM40-CMAES-d**



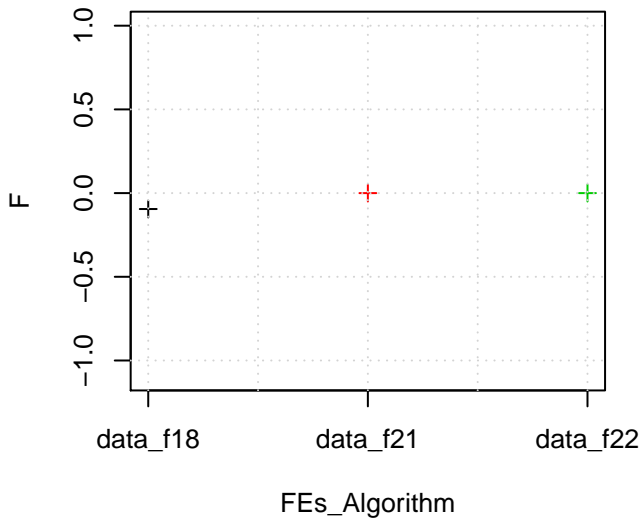
**DIM40-IPOP-a**



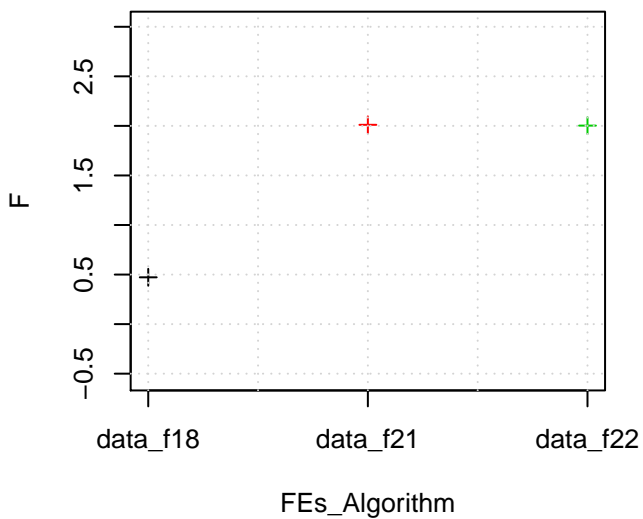
**DIM40-IPOP-b**



**DIM40-IPOP-c**

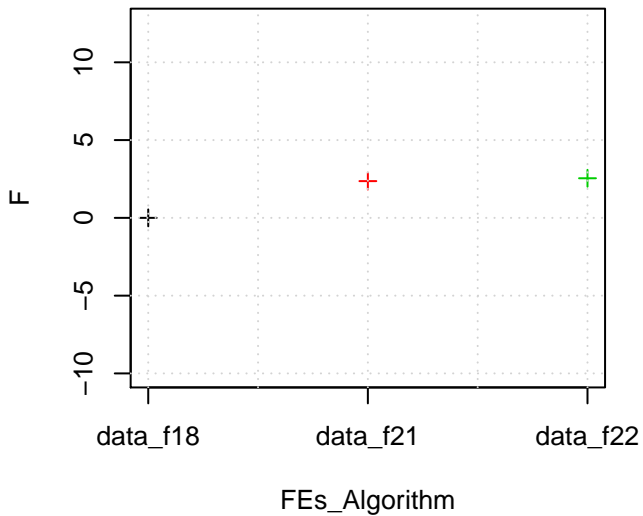


**DIM40-IPOP-d**

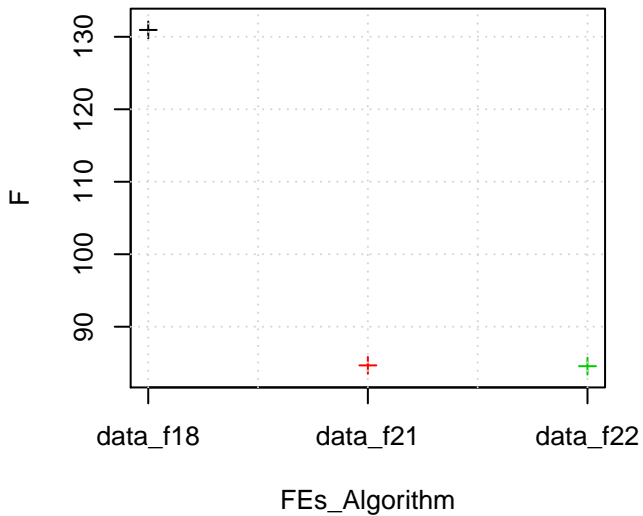




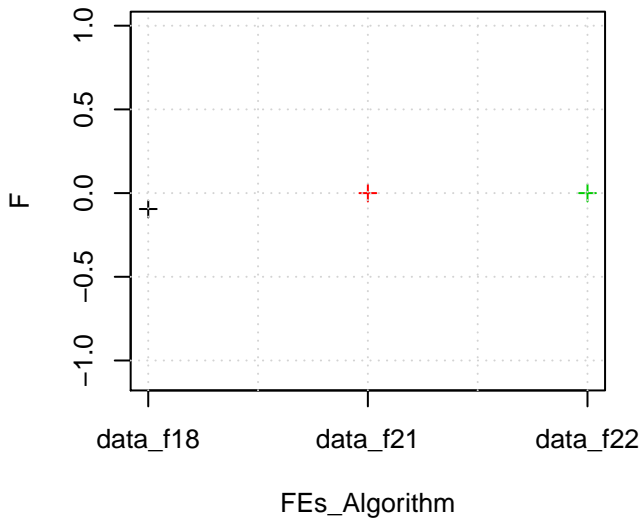
**DIM40-IPOP-500-a**



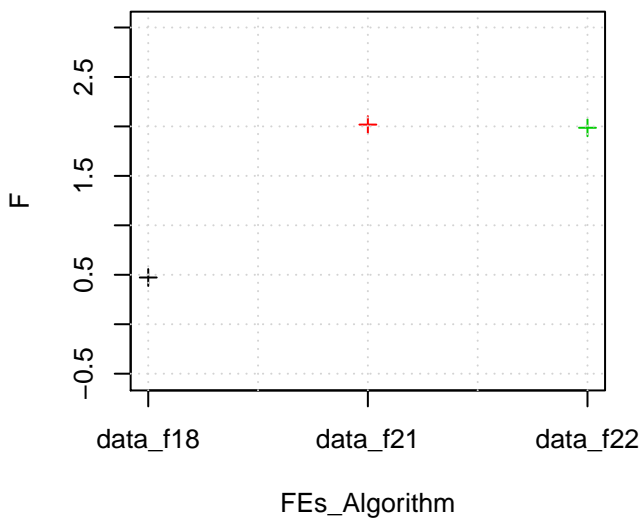
**DIM40-IPOP-500-b**



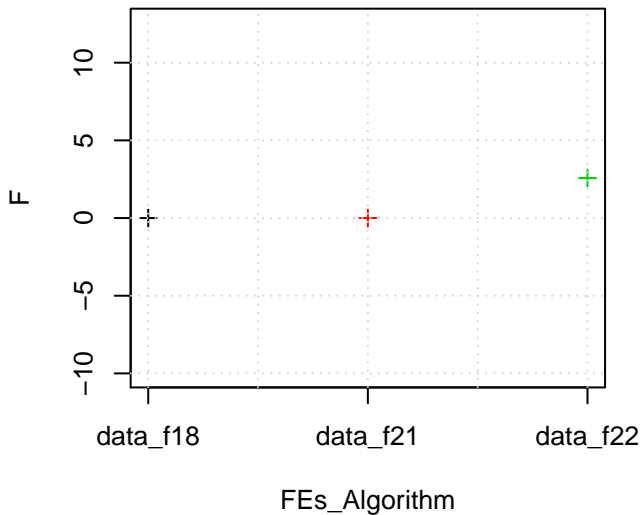
**DIM40-IPOP-500-c**



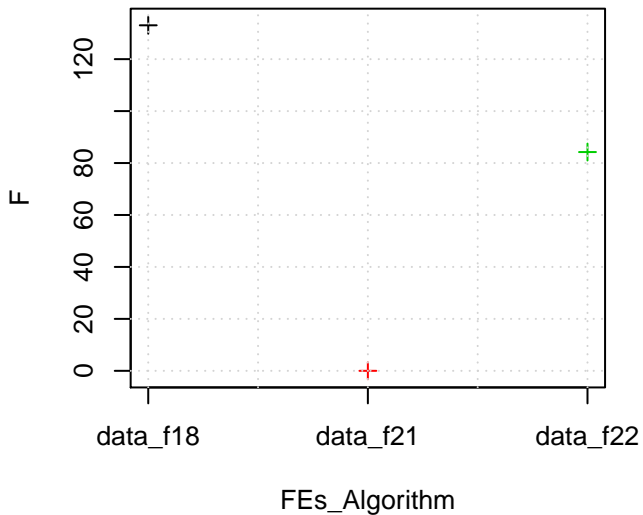
**DIM40-IPOP-500-d**



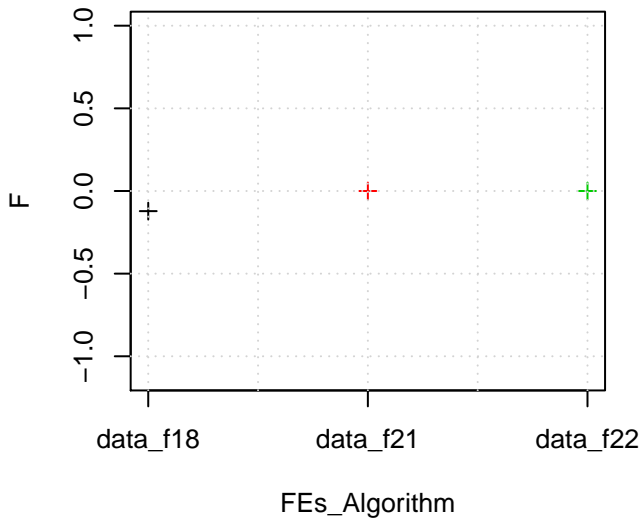
**DIM40-IPOP-tany-a**



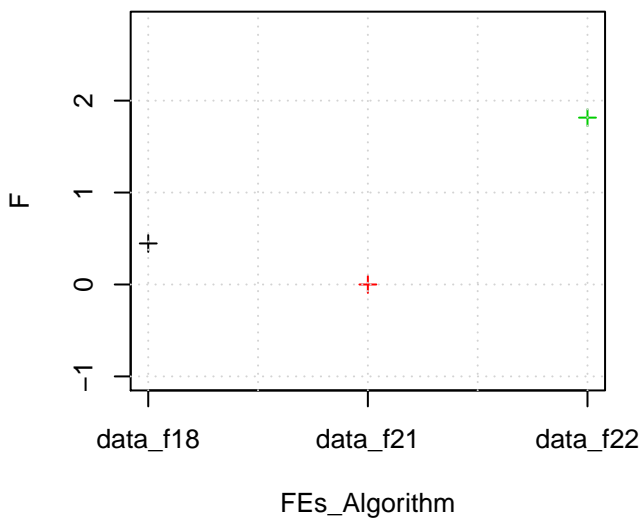
**DIM40-IPOP-tany-b**



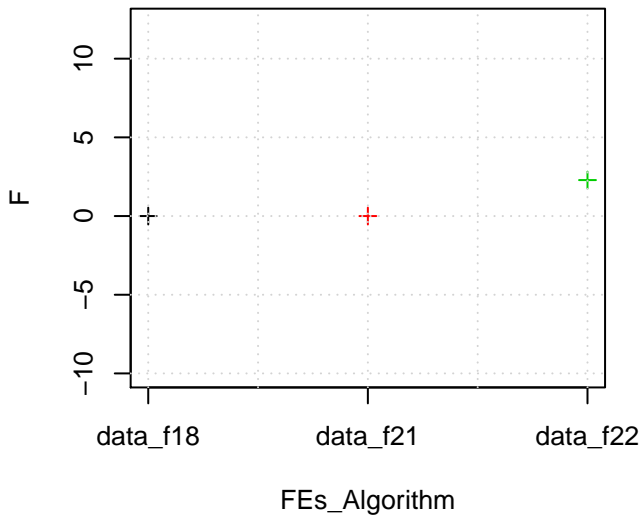
**DIM40-IPOP-tany-c**



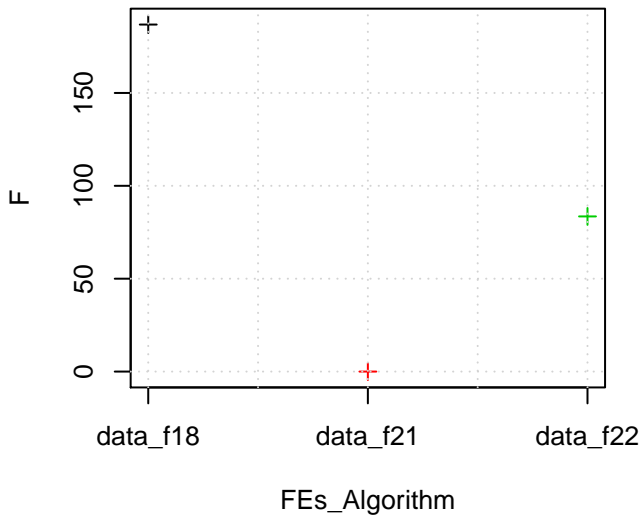
**DIM40-IPOP-tany-d**



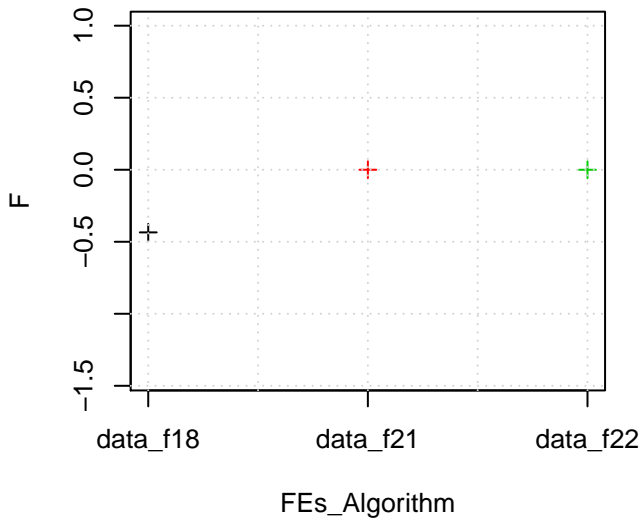
**DIM40-IPOP-texp-a**



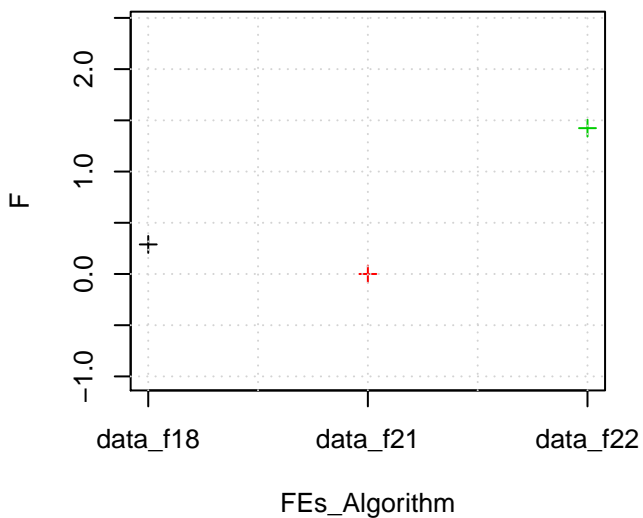
**DIM40-IPOP-texp-b**



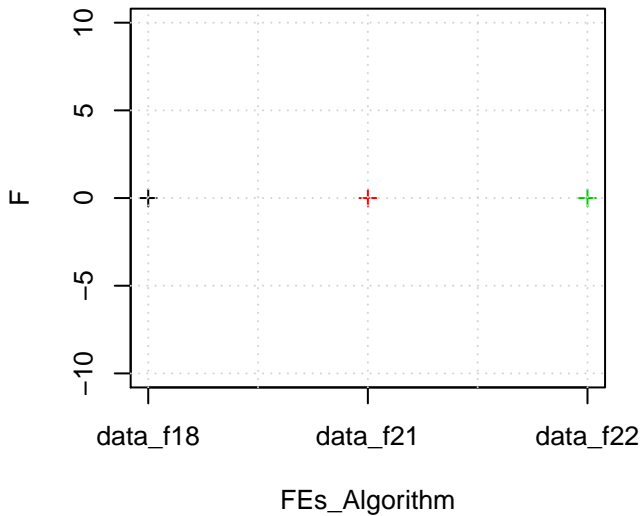
**DIM40-IPOP-texp-c**



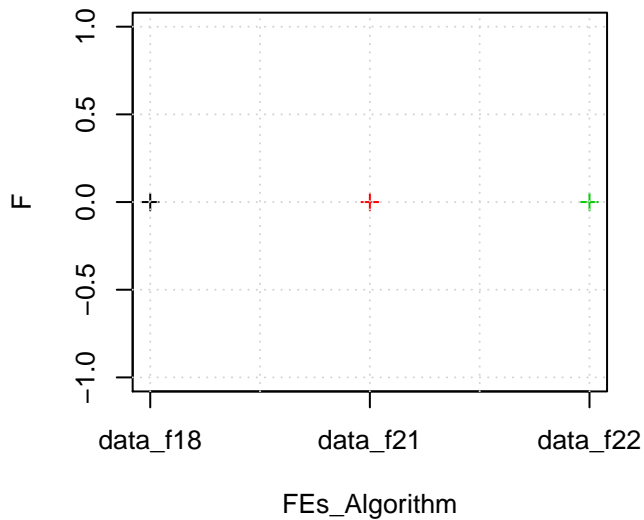
**DIM40-IPOP-texp-d**



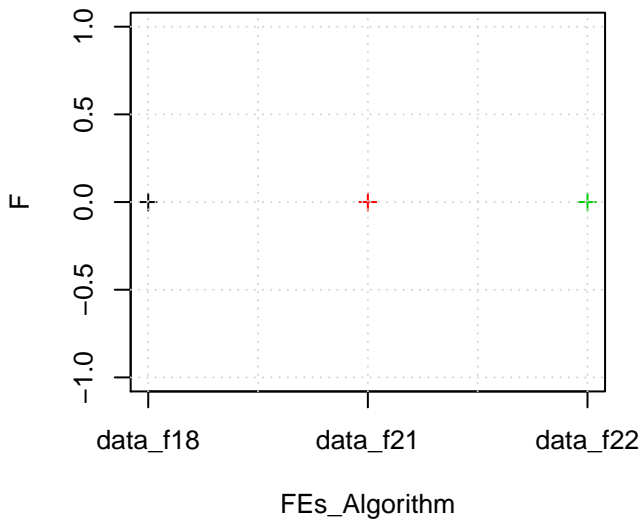
**DIM40-P-DCN-a**



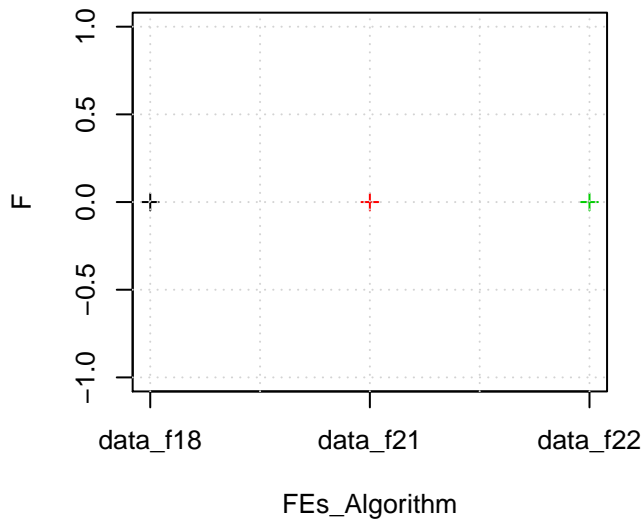
**DIM40-P-DCN-b**



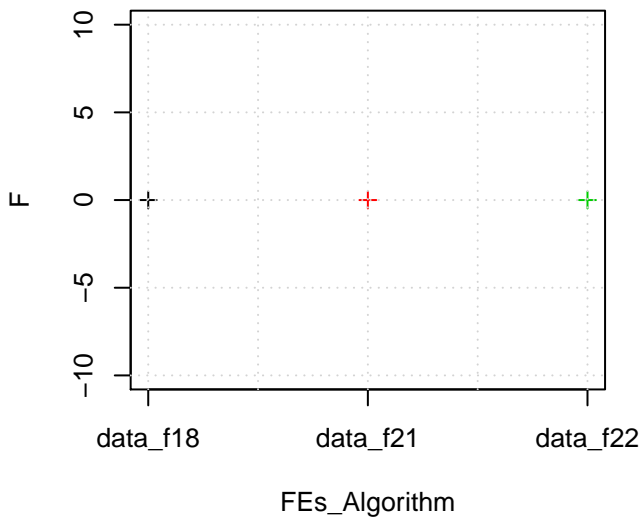
**DIM40-P-DCN-c**



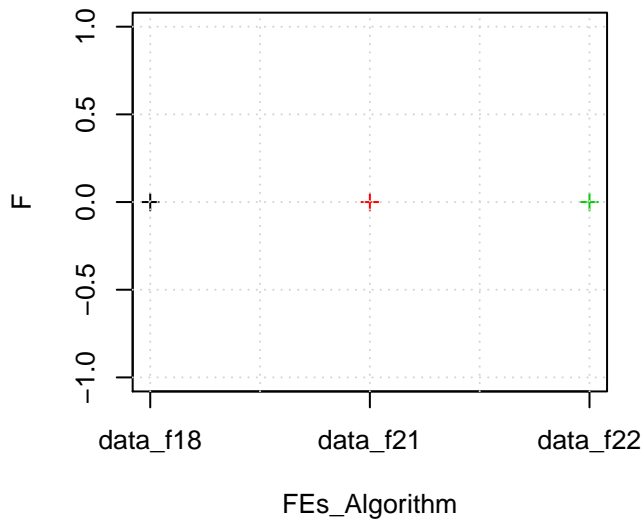
**DIM40-P-DCN-d**



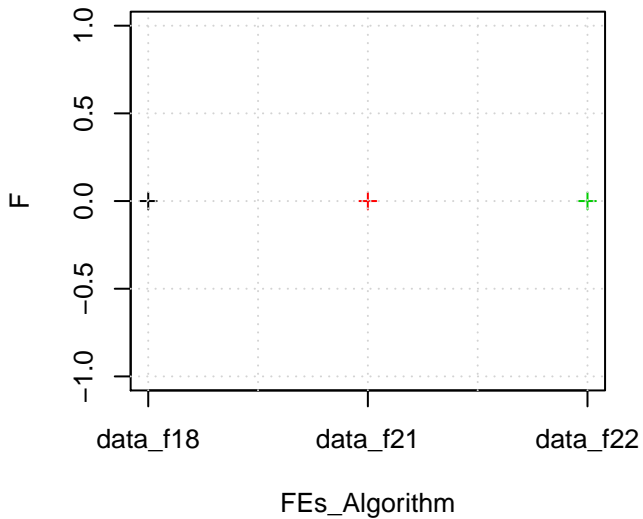
**DIM40-DE-a**



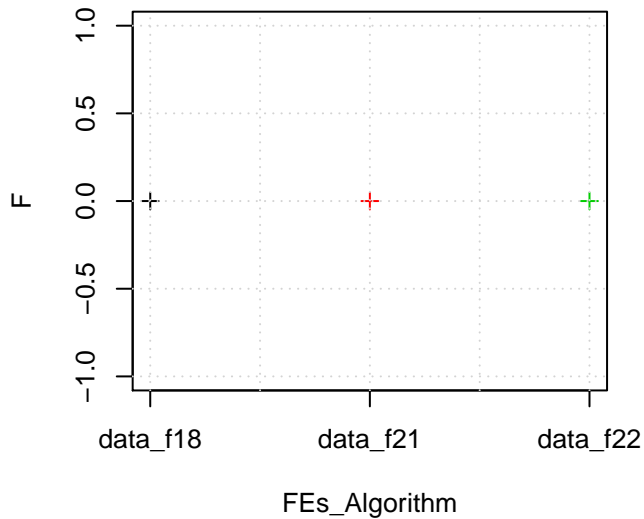
**DIM40-DE-b**



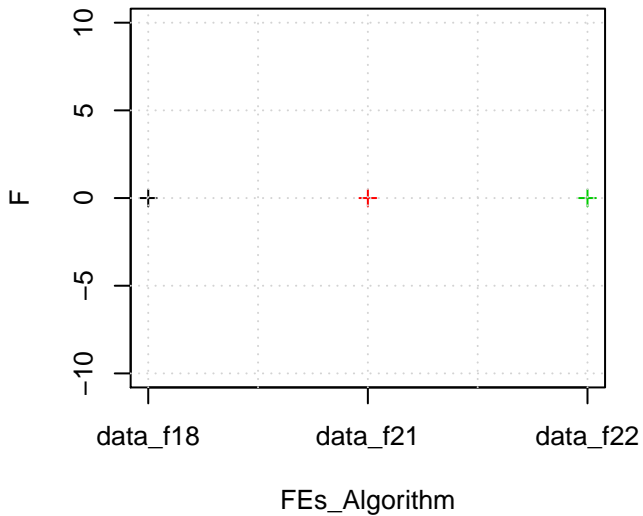
**DIM40-DE-c**



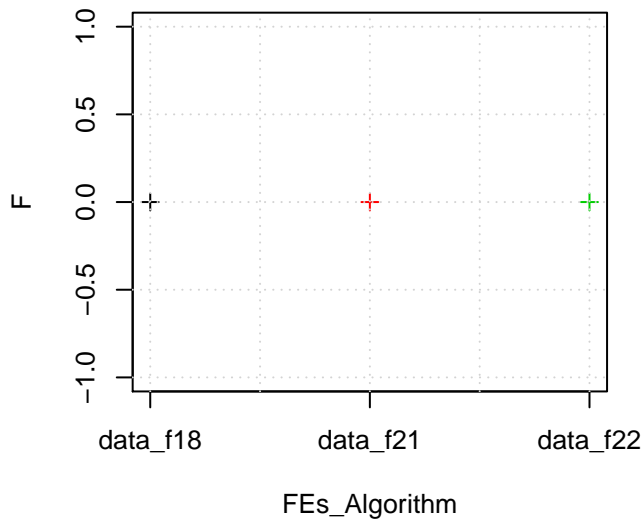
**DIM40-DE-d**



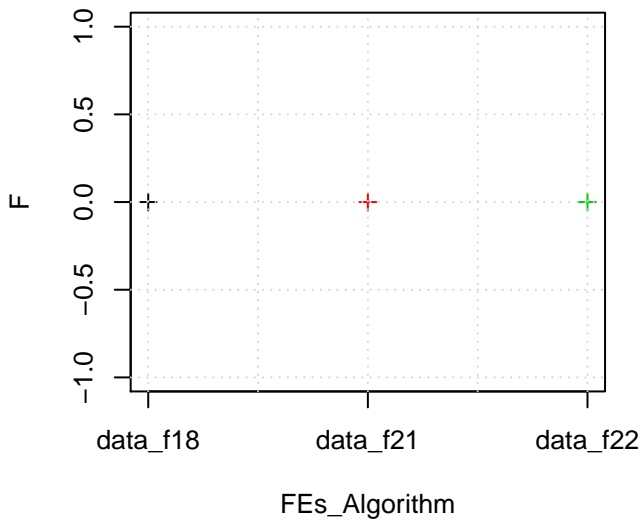
**DIM40-fmincon-a**



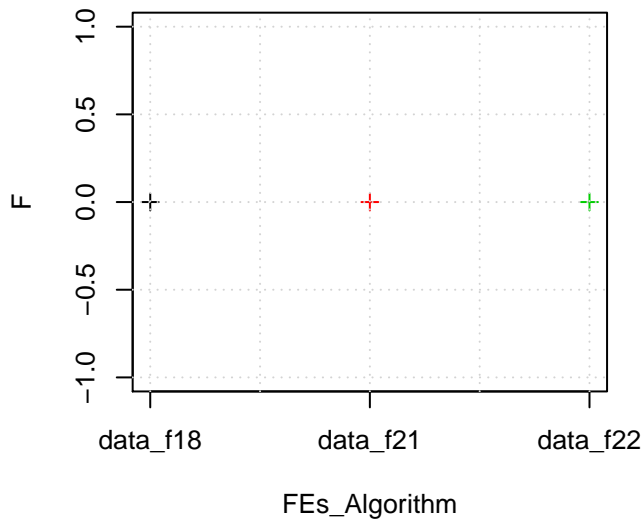
**DIM40-fmincon-b**



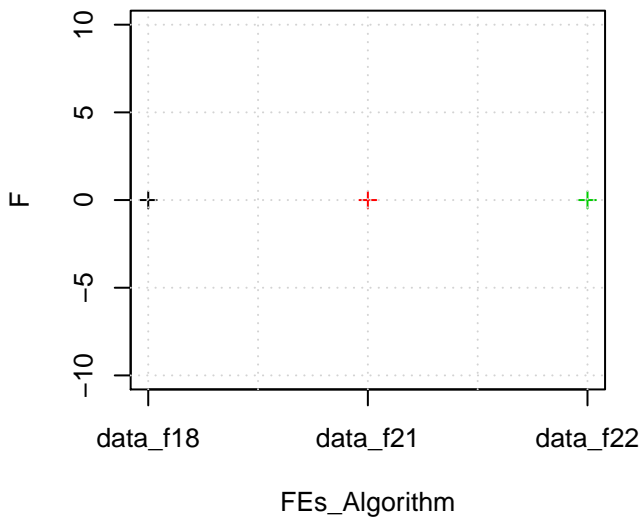
**DIM40-fmincon-c**



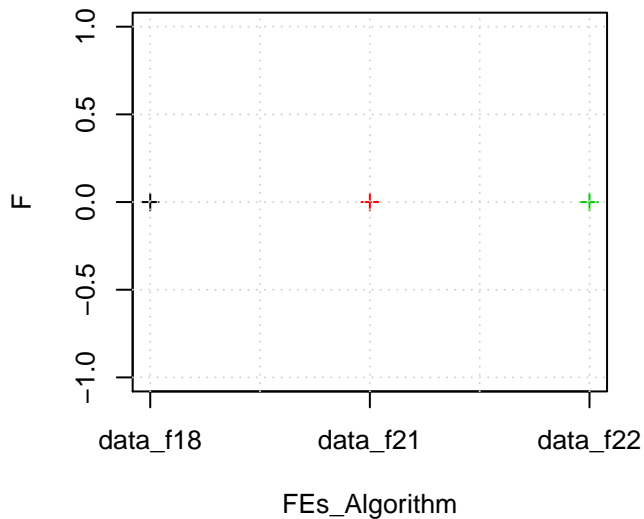
**DIM40-fmincon-d**



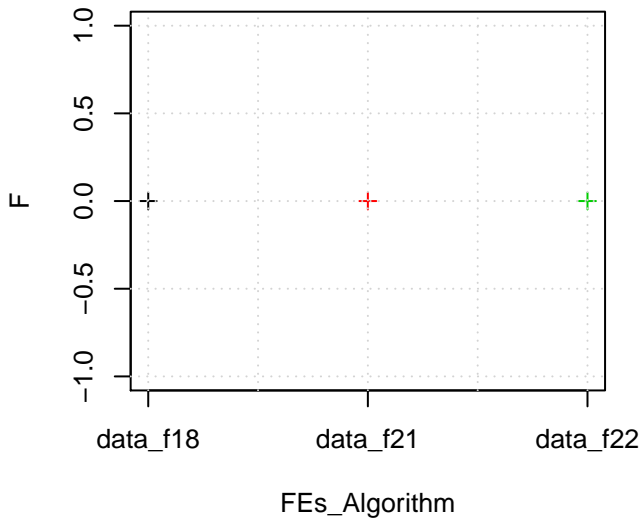
**DIM40-HMLSL-a**



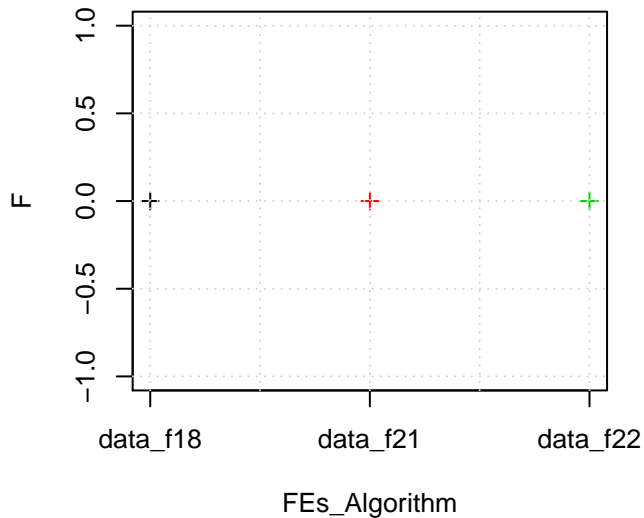
**DIM40-HMLSL-b**



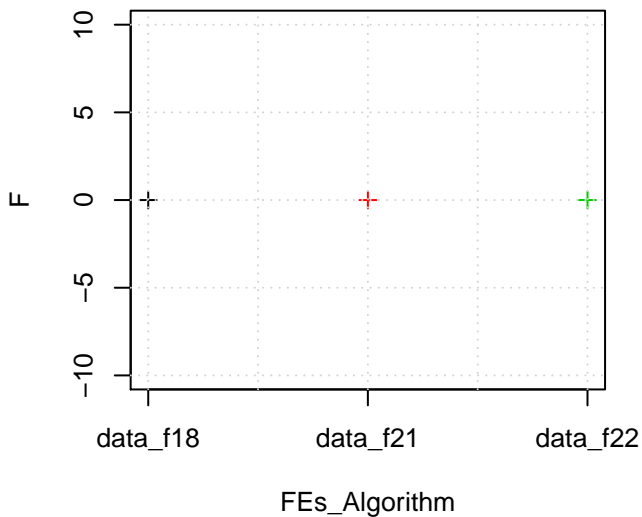
**DIM40-HMLSL-c**



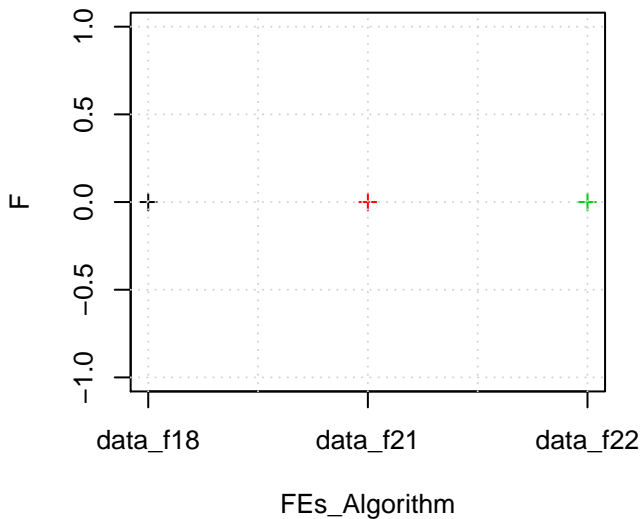
**DIM40-HMLSL-d**



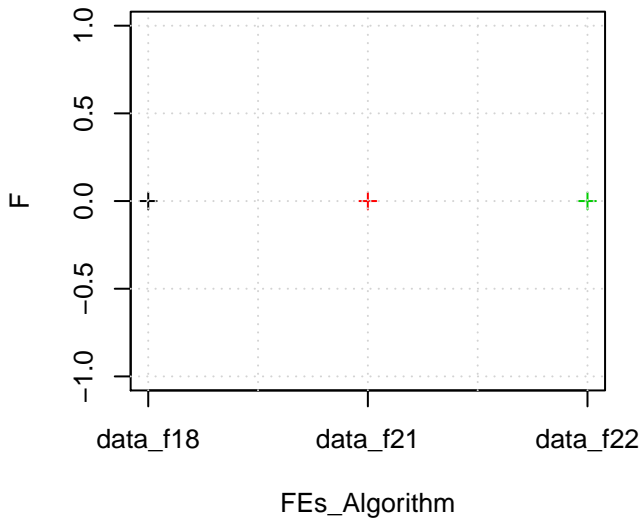
**DIM40-simplex-a**



**DIM40-simplex-b**



**DIM40-simplex-c**



**DIM40-simplex-d**

