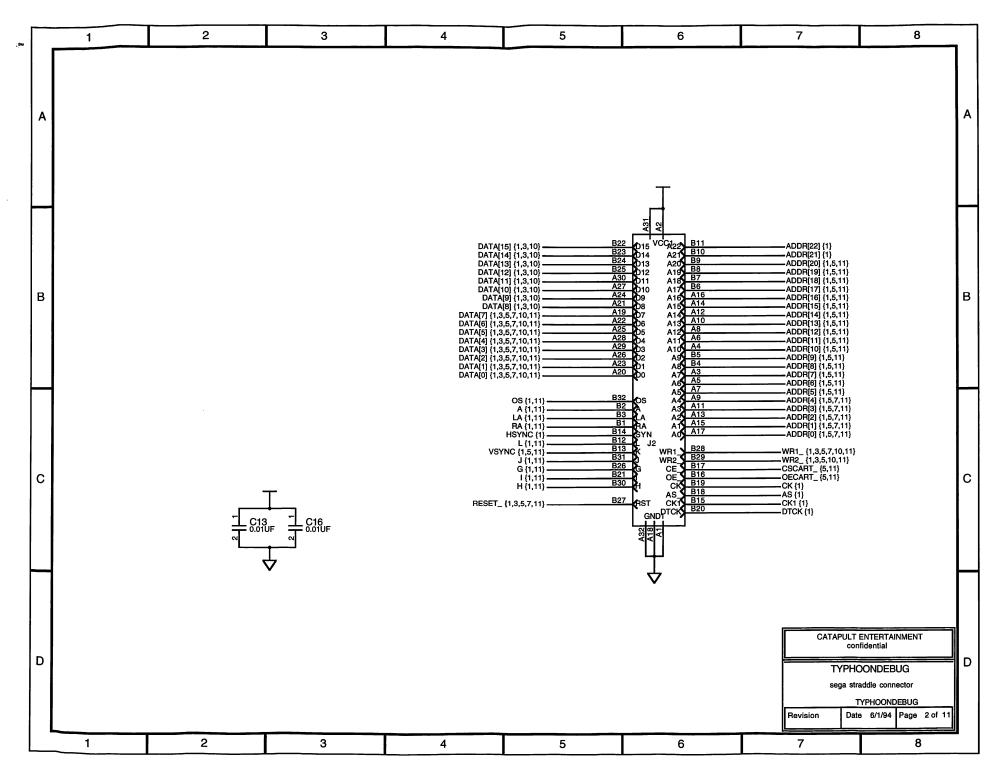


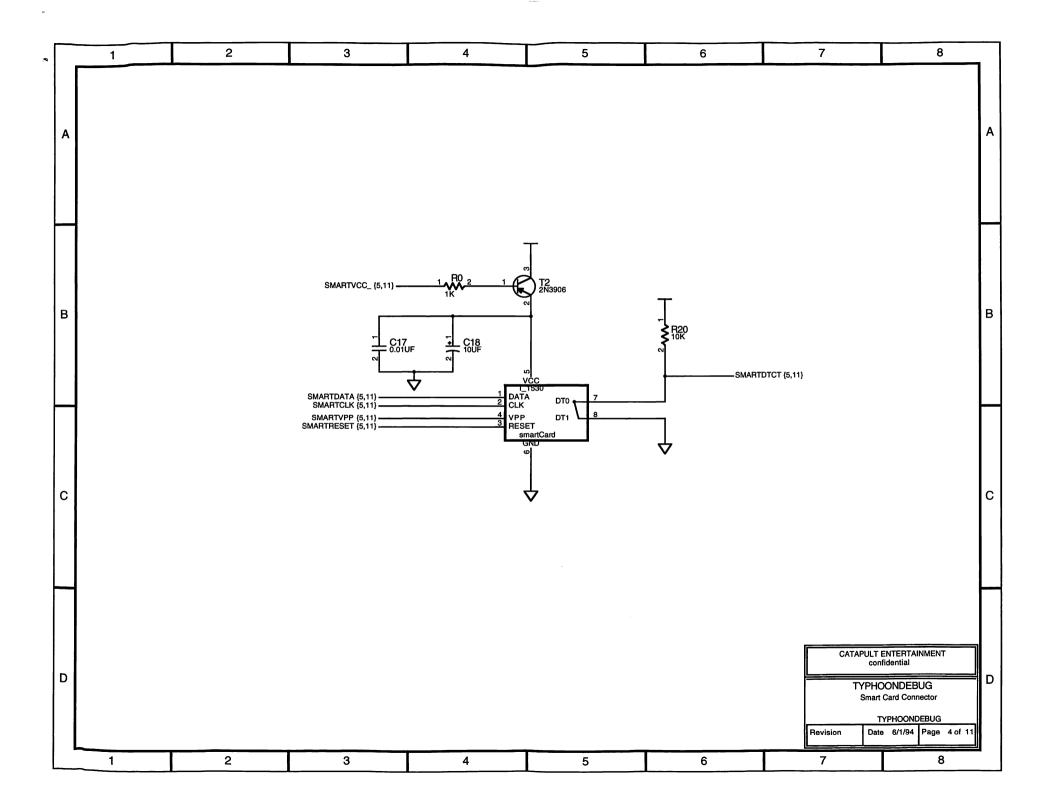
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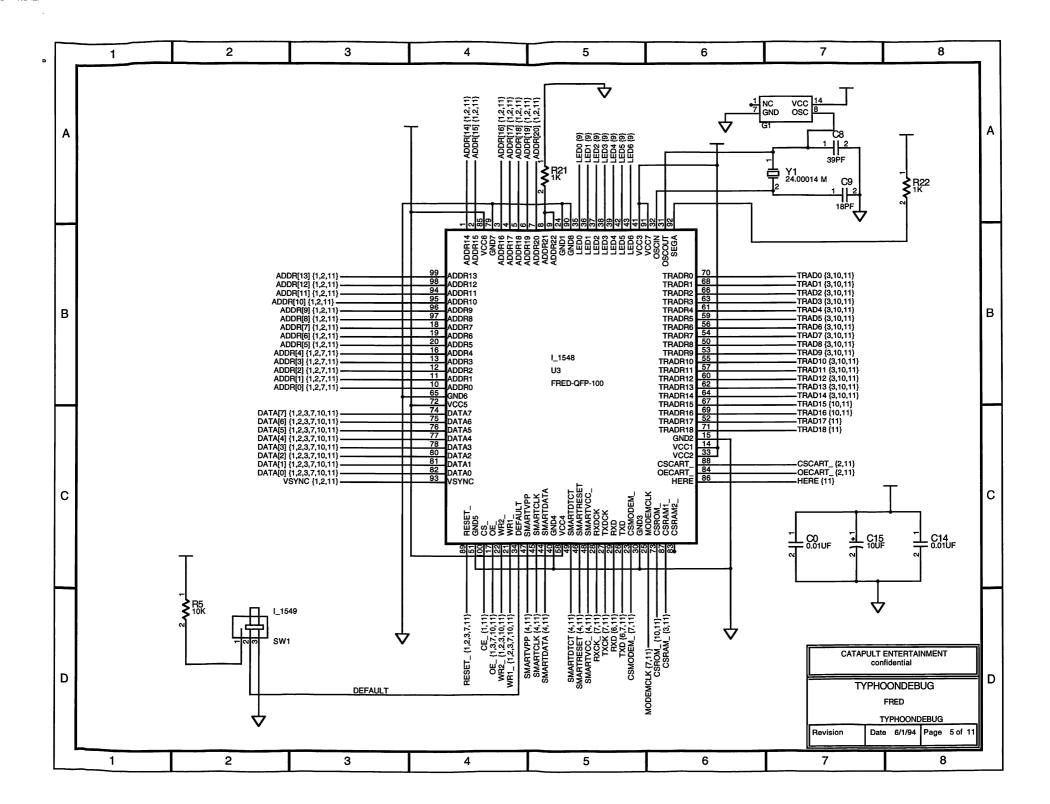


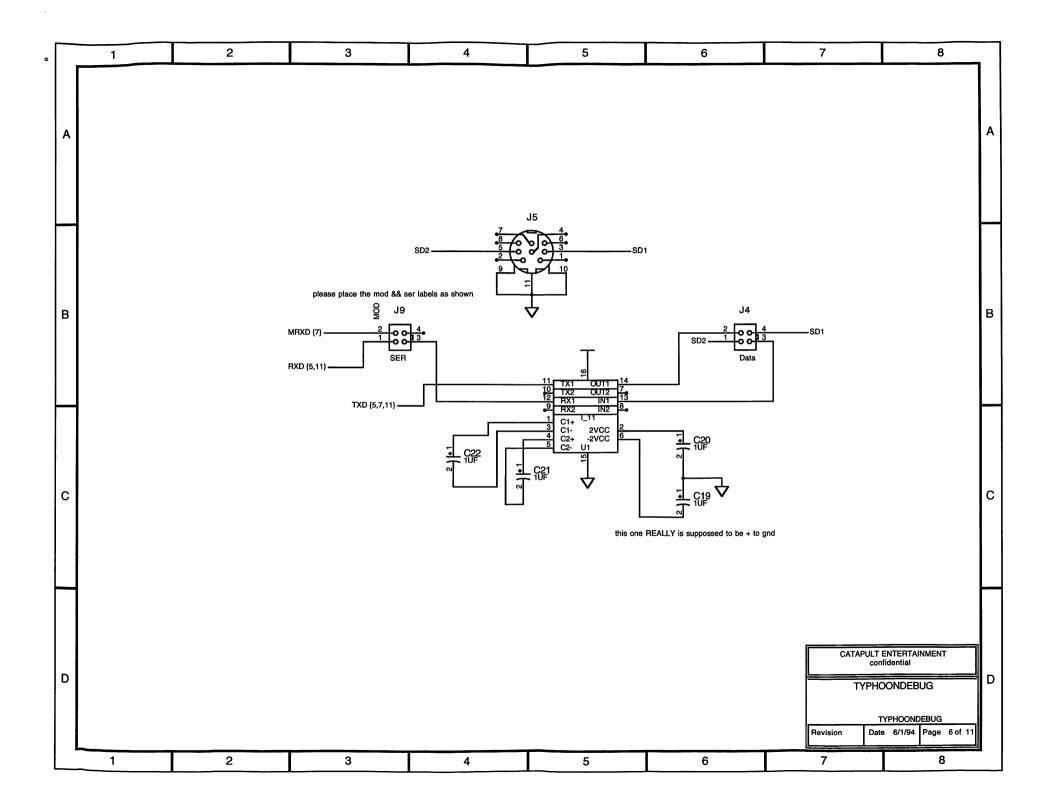
/**A** 

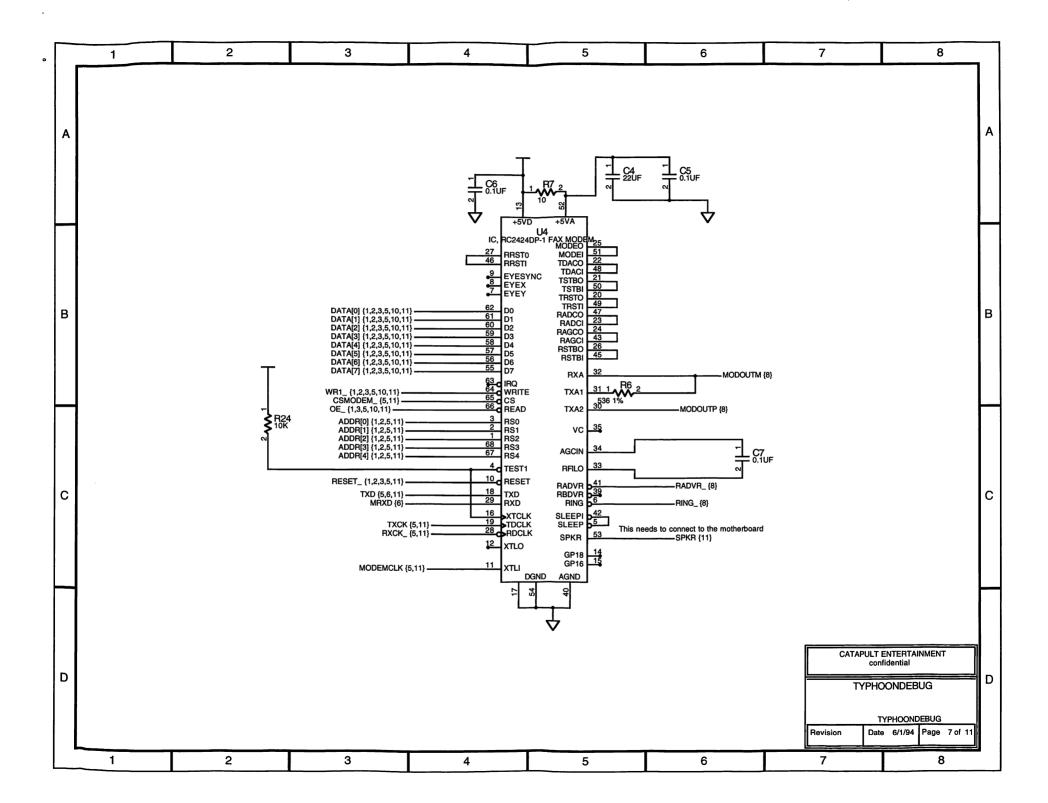
2 7 8 3 4 5 1 6 Α Α IC,SRAM,CMOS,32KX8,100NS 10 A0 IO0 9 A1 IO1 12 DATA[0] 8 A2 IO2 7 A3 IO3 6 A4 IO1 5 A4 IO1 DATA[15:0] TRAD0 {5,10,11} - TRAD1 {5,10,11} -TRAD2 (5,10,11) TRAD3 (5,10,11) -TRAD4 (5,10,11) -TRAD5 (5,10,11) -TRAD6 (5,10,11) -17 DATA[5] 18 DATA[6] 19 DATA[7] 5 A4 A5 A6 105 106 4 A6 3 A7 25 A8 24 A9 21 A10 23 A11 2 A12 26 A13 1 A14 TRAD7 {5,10,11} 107 TRAD8 (5,10,11) В В TRAD9 (5,10,11) TRAD10 {5,10,11} -TRAD11 {5,10,11} -TRAD12 {5,10,11} -C10 0.01UF C11 0.01UF TRAD13 (5,10,11) TRAD14 (5,10,11) -20 CS 22 COE 27 CWE GND PCS\_ {11}-OE\_ {1,5,7,10,11} — WR1\_ {1,2,5,7,10,11} — D15 1 2 1N5817 VCC U7 IC,SRAM,CMOS,32KX8,190NS A0 IO0 11 DATAI81 9 A1 IO1 12 DATAI91 8 A1 IO1 13 DATAI91 С С DATA[15:0] D14 TRAD0 {5,10,11} TRAD1 {5,10,11} 1 2 1N4153 13 DATA[10] 15 DATA[11] 16 DATA[12] 17 DATA[13] 18 DATA[14] 8 A2 7 A3 6 A4 TRAD2 {5,10,11} -102 LITHIUM TRAD3 {5,10,11} -TRAD3 {5,10,11} -TRAD4 {5,10,11} -TRAD5 {5,10,11} -103 104 105 106 6 A4 5 A5 4 A6 3 A7 25 A8 24 A9 21 A10 23 A11 2 A12 26 A13 1 A14 TRAD6 (5,10,11) 19 DATA[15] TRAD7 (5,10,11) -TRAD8 {5,10,11} -TRAD9 {5,10,11} -TRAD10 {5,10,11} -TRAD11 {5,10,11} -PCS\_ {11} TRAD13 {5,10,11} TRAD14 (5,10,11) -20 CS 22 COE 27 CWE GND PCS\_{11} — OE\_{1,5,7,10,11} — WR2\_{1,2,5,10,11} -CATAPULT ENTERTAINMENT T1 MMBT-5134 confidential D D **≱**R4 100K -CSRAM\_ {5,11} **TYPHOONDEBUG** D13 Battery Backed SRAM 1 2 1N4153 -RESET\_ {1,2,5,7,11} TYPHOONDEBUG Revision Date 6/1/94 Page 3 of 11 2 1 4 5 7 8 3 6

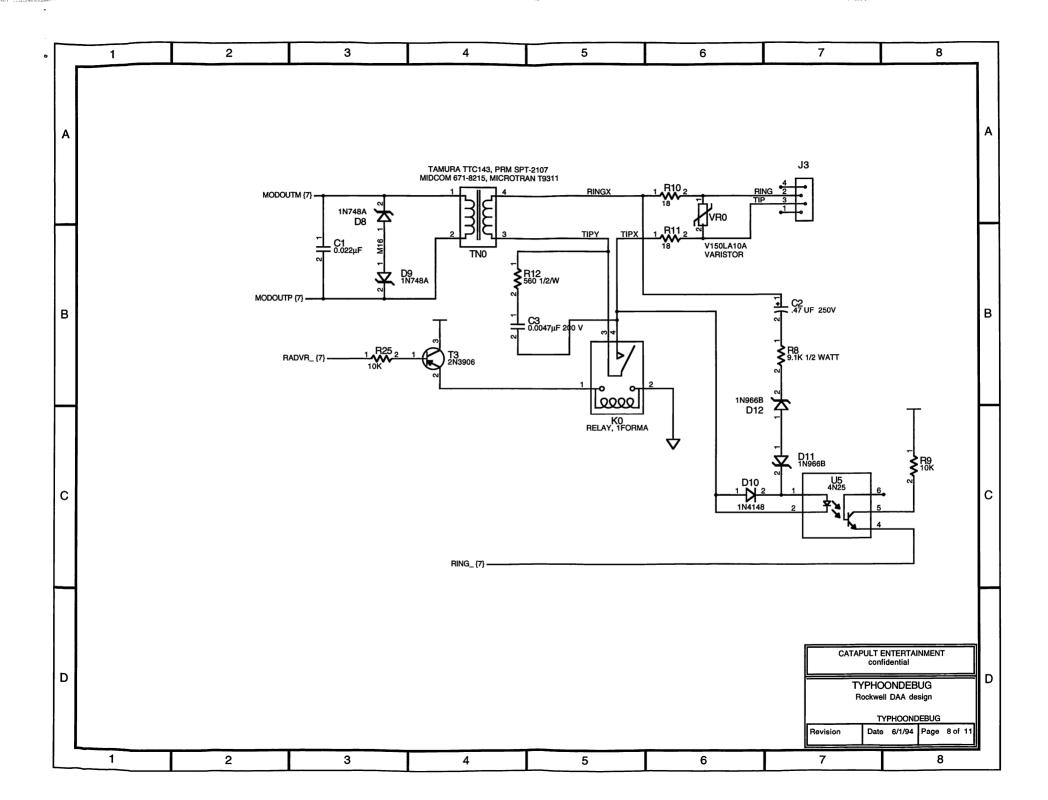
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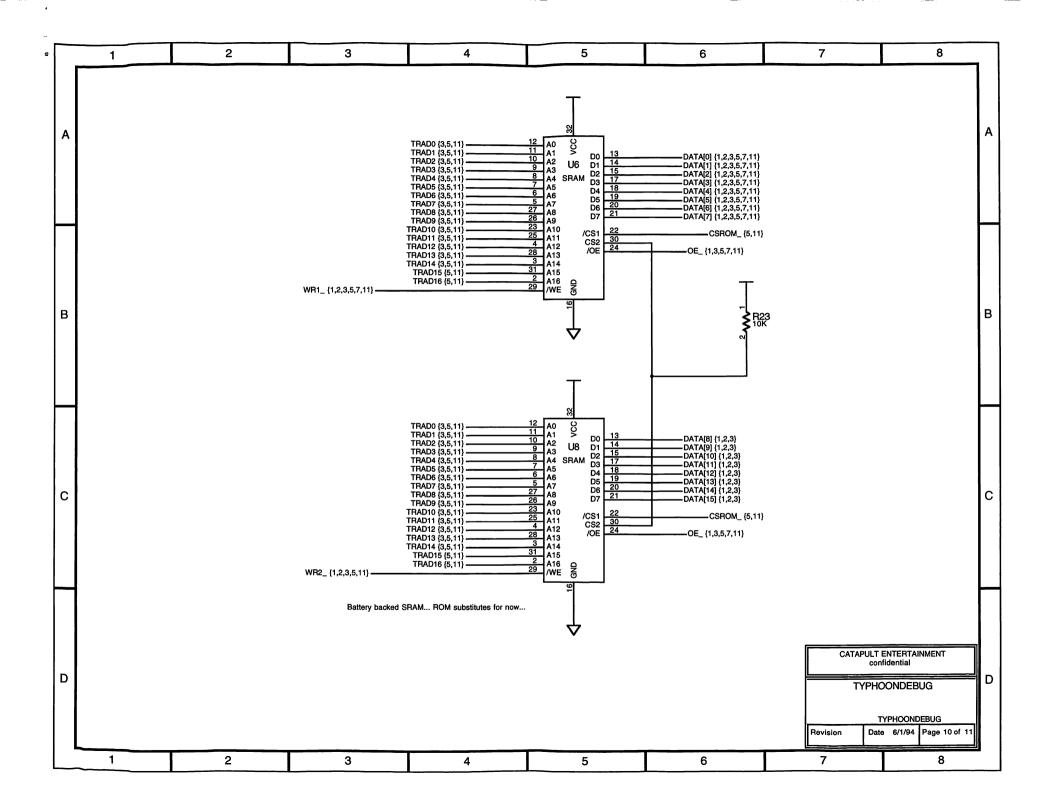


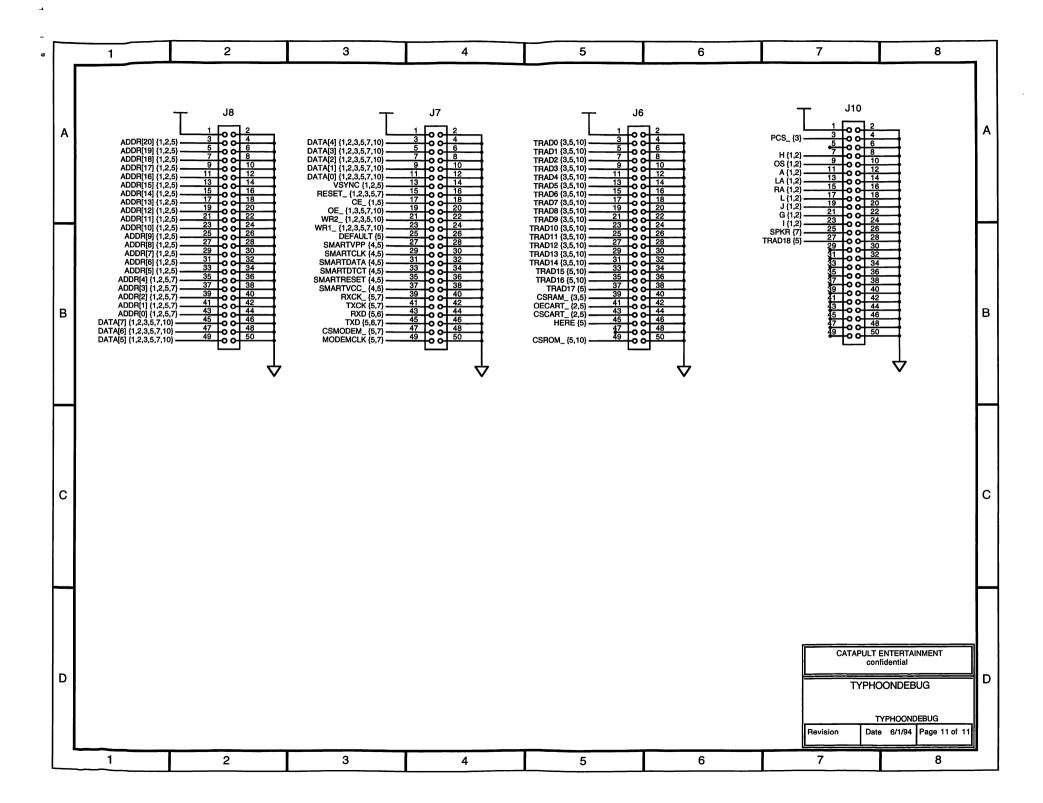




7 8 3 2 4 5 6 1 Α **\$**R15 **≹**R19 **≸**R13 В В -#308-T1-RED #308-T1-RED #308-LED4 (5)-LEDS (5)-LED6 (5)-LED3 (5)-LED0 {5}-С С CATAPULT ENTERTAINMENT confitte的論 D D **TYPHOONDEBUG** TYPHOONDEBUG Revision Date 6/1/94 Page 9 of 11 1 2 7 8 3 4 5 6

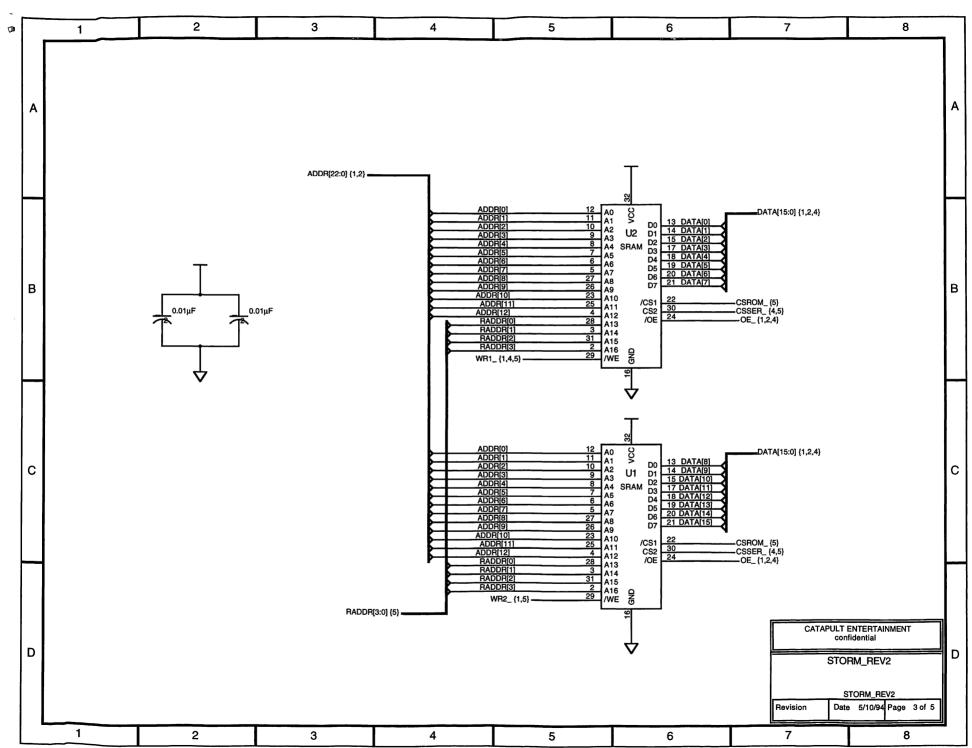
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