Curs 9 - Listeme de operare Settings + system > 4 CPU'S

ligthread . a

FPGA

libm.a

-5 la congilare -01 -02. -03

- doar mul are acces (excludere mutuala) - muter

→ door al care modifica are acces exclusive - nurbock

> un numar limital to ailes acces - semafor

fa pornim / continuent. la parnim l'avulirman toti octo -> baniere

star pana you anautat , variabila conditionalo

> stai pina este amentat das parelete in amenite conditie - > variale ila cond

sunget - tem-init sum op tem-wait semall - sem-destroy

Semafor: valoure -. 1 - semafor bina: foch wait (reserva)

post (elibereada) - suloch

bariera: valoare y pelaca dupa a tonte dan mait

variabile conditionals - signal pt. unal care aste aprile. I broadcast se trejese toate care askapti.

Worker if (logiu) lock(); if (12 1000) }

wait() 3 Maritane

5 signal()

3 unlede();

varioud ci int x; hock (km) i while (1) x ++1 lock (xm); if (x>100) 9 signal (Dc); while (x <= 1000) } wait (ke, km); untele Om); //curata unlock (km); → de ce wait deblocheate mutex? X, t6 0 to -0 € Z 0 **24**0 t, -0€# tio) tu 567891091213147