



1. Description

1.1. Project

Project Name	pinMapF3
Board Name	custom
Generated with:	STM32CubeMX 6.9.2
Date	10/09/2023

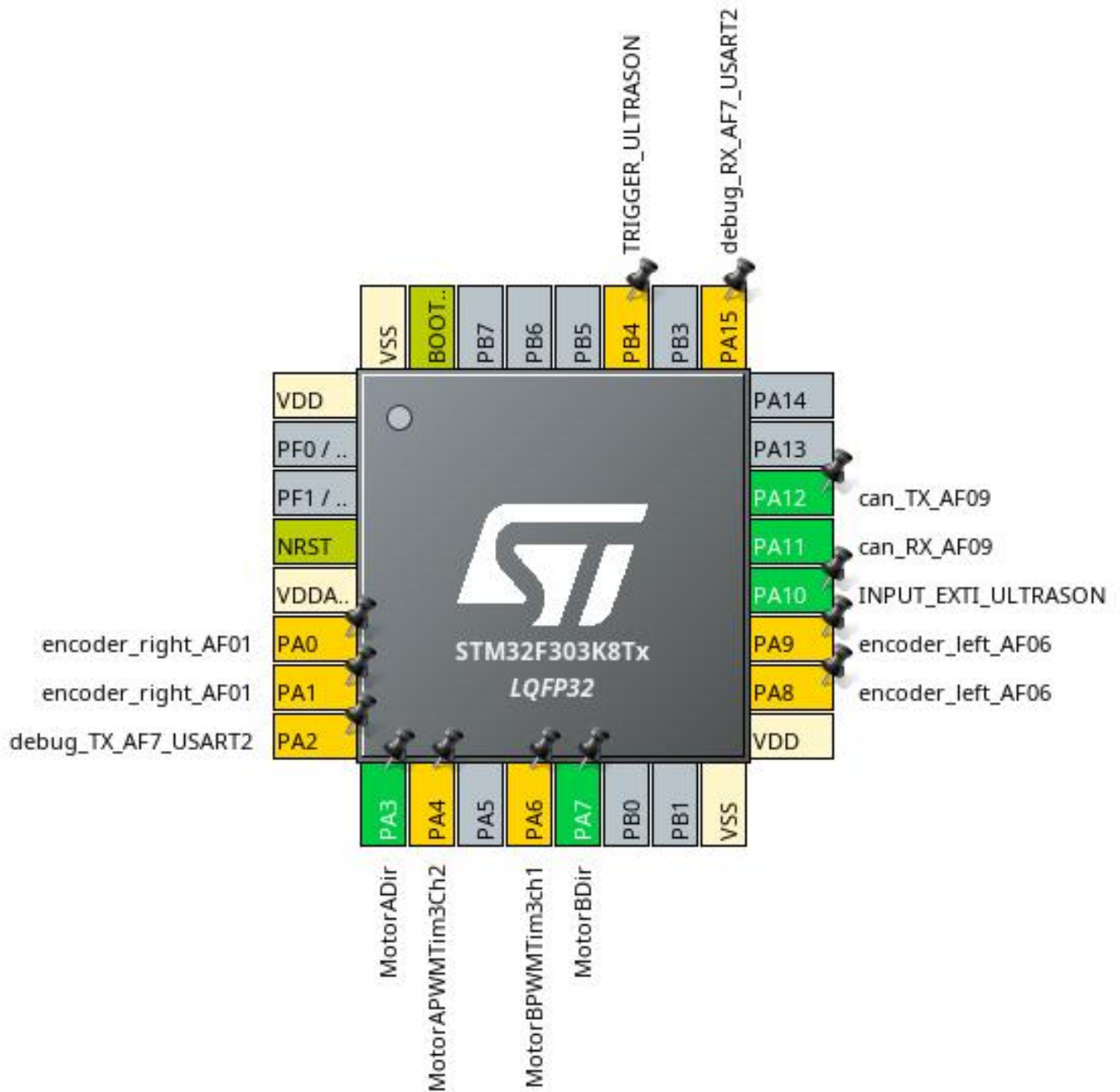
1.2. MCU

MCU Series	STM32F3
MCU Line	STM32F303
MCU name	STM32F303K8Tx
MCU Package	LQFP32
MCU Pin number	32

1.3. Core(s) information

Core(s)	Arm Cortex-M4
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2. Pinout Configuration



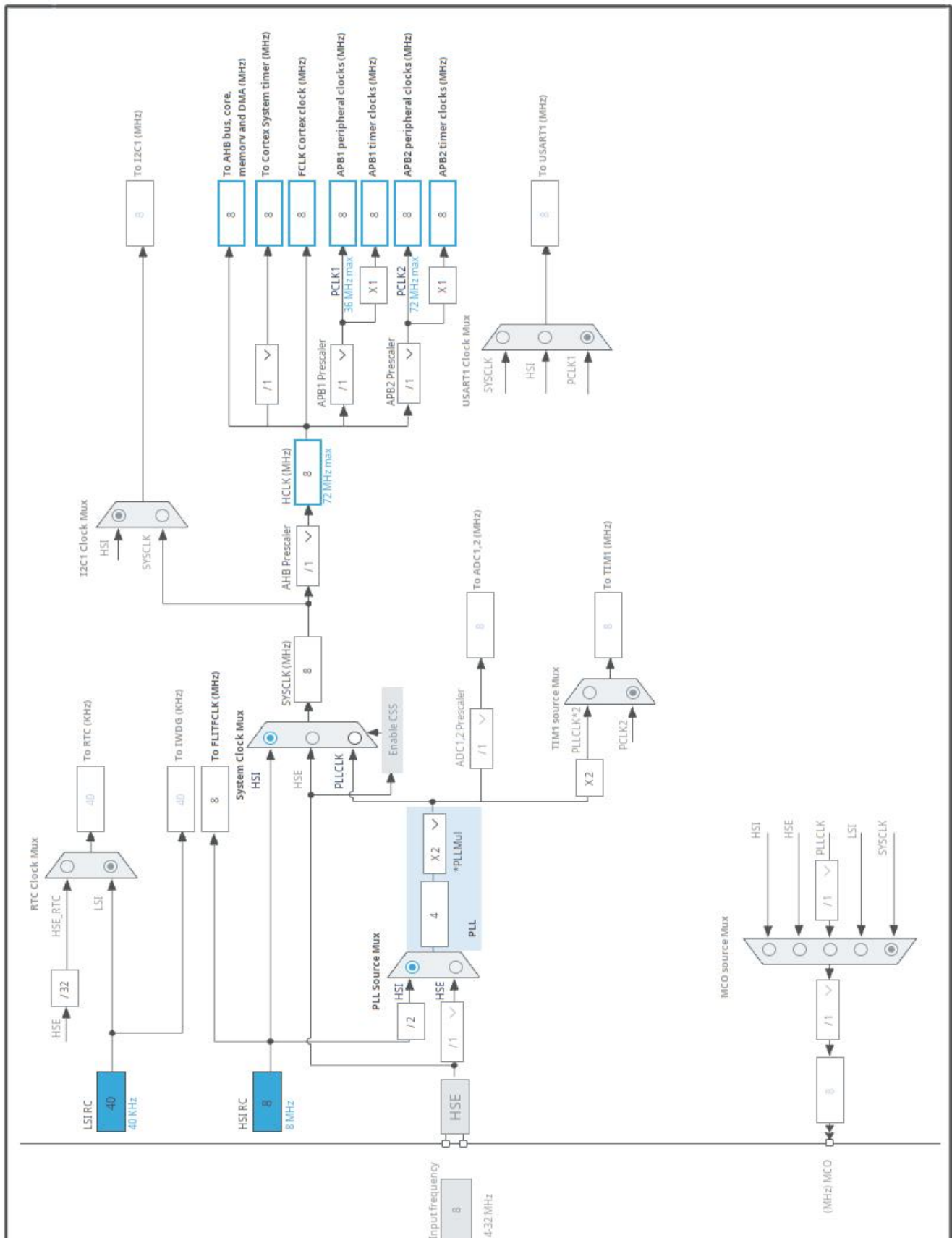
3. Pins Configuration

Pin Number LQFP32	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
4	NRST	Reset		
5	VDDA/VREF+	Power		
6	PA0 *	I/O	TIM2_CH1	encoder_right_AF01
7	PA1 *	I/O	TIM2_CH2	encoder_right_AF01
8	PA2 *	I/O	USART2_TX	debug_TX_AF7_USART2
9	PA3 **	I/O	GPIO_Output	MotorADir
10	PA4 *	I/O	TIM3_CH2	MotorAPWMTim3Ch2
12	PA6 *	I/O	TIM3_CH1	MotorBPWMTim3ch1
13	PA7 **	I/O	GPIO_Output	MotorBDir
16	VSS	Power		
17	VDD	Power		
18	PA8 *	I/O	TIM1_CH1	encoder_left_AF06
19	PA9 *	I/O	TIM1_CH2	encoder_left_AF06
20	PA10	I/O	GPIO_EXTI10	INPUT_EXTI_ULTRASON
21	PA11	I/O	CAN_RX	can_RX_AF09
22	PA12	I/O	CAN_TX	can_TX_AF09
25	PA15 *	I/O	USART2_RX	debug_RX_AF7_USART2
27	PB4 *	I/O	TIM16_CH1	TRIGGER_ULTRASON
31	BOOT0	Boot		
32	VSS	Power		

** The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	pinMapF3
Project Folder	/home/beatrice/code_f3/pinMapF3
Toolchain / IDE	EWARM V8.32
Firmware Package Name and Version	STM32Cube FW_F3 V1.11.0
Application Structure	Basic
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	SystemClock_Config	RCC
3	MX_CAN_Init	CAN

1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F3
Line	STM32F303
MCU	STM32F303K8Tx
Datasheet	DS9866_Rev5

1.2. Parameter Selection

Temperature	25
Vdd	3.6

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

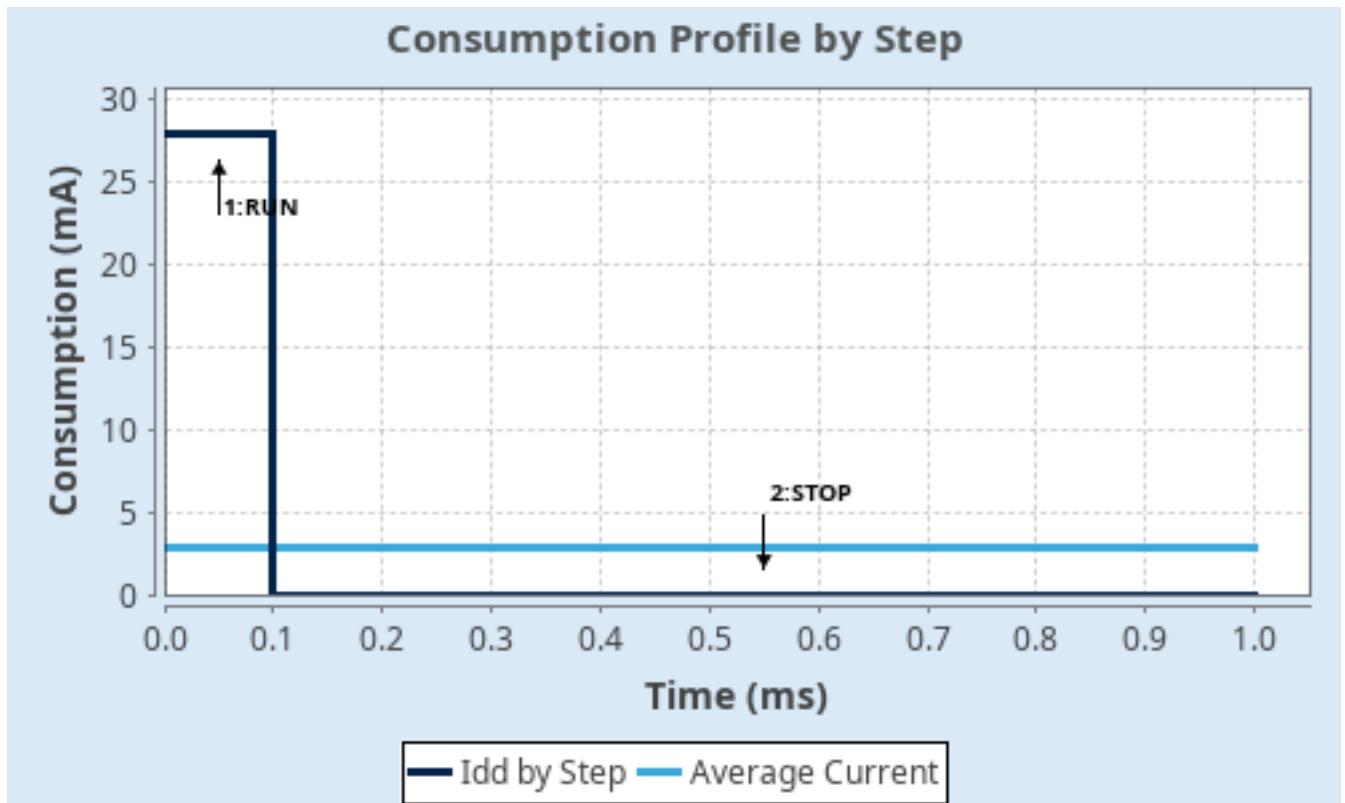
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	RAM	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSEBYP PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	27.84 mA	9.55 μ A
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	98.99	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	2.79 mA
Battery Life	1 month, 20 days, 5 hours	Average DMIPS	90.0 DMIPS

1.6. Chart



2. Peripherals and Middlewares Configuration

2.1. CAN

mode: Activated

2.1.1. Parameter Settings:

Bit Timings Parameters:

Prescaler (for Time Quantum)	16
Time Quantum	2000.0 *
Time Quanta in Bit Segment 1	1 Time
Time Quanta in Bit Segment 2	1 Time
Time for one Bit	6000 *
Baud Rate	166666 *
ReSynchronization Jump Width	1 Time

Basic Parameters:

Time Triggered Communication Mode	Disable
Automatic Bus-Off Management	Disable
Automatic Wake-Up Mode	Disable
Automatic Retransmission	Disable
Receive Fifo Locked Mode	Disable
Transmit Fifo Priority	Disable

Advanced Parameters:

Operating Mode	Normal
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2.2. RCC

2.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

2.3. SYS

Timebase Source: SysTick

* User modified value

3. System Configuration

3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
CAN	PA11	CAN_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	can_RX_AF09
	PA12	CAN_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	can_TX_AF09
Single Mapped Signals	PA0	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	encoder_right_AF01
	PA1	TIM2_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	encoder_right_AF01
	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	debug_TX_AF7_USART2
	PA4	TIM3_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	MotorAPWMTim3Ch2
	PA6	TIM3_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	MotorBPWMTim3ch1
	PA8	TIM1_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	encoder_left_AF06
	PA9	TIM1_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	encoder_left_AF06
	PA15	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	debug_RX_AF7_USART2
	PB4	TIM16_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	TRIGGER_ULTRASON
GPIO	PA3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	MotorADir
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	MotorBDir
	PA10	GPIO_EXTI10	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	INPUT_EXTI_ULTRASON

3.2. DMA configuration

nothing configured in DMA service

3.3. NVIC configuration

3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
CAN TX interrupt	unused		
CAN RX0 interrupt	unused		
CAN RX1 interrupt	unused		
CAN SCE interrupt	unused		
EXTI line[15:10] interrupts	unused		
Floating point unit interrupt	unused		

3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

4. System Views

4.1. Category view

4.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Computing

DMA

GPIO 

NVIC 

RCC 

SYS 

CAN 

5. Docs & Resources

Type	Link
BSDL files	https://www.st.com/resource/en/bsdl_model/stm32f3_bsdl.zip
System View Description	https://www.st.com/resource/en/svd/stm32f3-svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf
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Brochures	https://www.st.com/resource/en/brochure/brstm32f3.pdf
Brochures	https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32nucleo.pdf
Flyers	https://www.st.com/resource/en/flyer/flstmcsuite.pdf
Flyers	https://www.st.com/resource/en/flyer/flpowerstbd.pdf
Flyers	https://www.st.com/resource/en/flyer/fldpstpfc11120.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf
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for related Tools [emulation-in-stm32f30xstm32f31xstm32f37xstm32f38x-microcontrollers-](#)
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for related Tools [configuration-tool-for-stm32f3xxxx-and-stm32f0xxxx-microcontrollers-](#)
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