

RXD

RTS# CTS# 11 ×

CH340T(SSOP20W)

C2 27pF/50V/5%/C0G/C0402

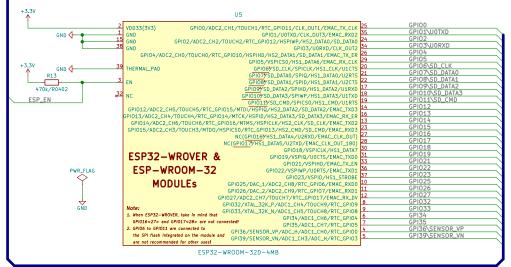
MISB-SWMM-5B-LF(USB\_MICRO)

DTR# DSR# DCD# RI# NOS# IR#

2.2k/R0402

GND1♦ S

## ESP32-WROOM-32x-xxx MODULE



## Fiducials:



Auto program

DTR RTS->EN 100

D3 BAT54C(SOT23-3)

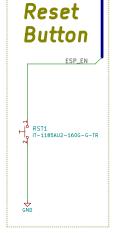






pping Resistors
Pull-Down
Pull-Up
Pull-Down
Pull-Down
Pull-Up
Pull-Up

## **Extensions** 5 6 7 8 9 10 11 12 13 14 15 16 17 18 HN1×19



Software Selectable Pins				
Interface	Signal	Pin		
EMAC	EMAC_MDC_out EMAC_MDI_In EMAC_MDO_out EMAC_CRS_out EMAC_COL_out	Any GPIO		
I2C	I2CEXTO_SCL_In I2CEXTO_SDA_In I2CEXT1_SCL_In I2CEXT1_SDA_In I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SCL_out	Any GPIO		
General Purpose SPI	HSPIQ_in/_out HSPID_In/_out HSPID_In/_out HSPICSQ_in/_out HSPLCSQ_in/_out HSPLCSQ_out VSPIQ_in/_out VSPID_in/_out VSPICK_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_in/_out VSPICSQ_out	Any GPIO		
For more information refer to esp_wroom_32_datasheet_en.pdf.				

		Voltage of Inter	nal LDO(VDD_SDIO)			
Pîn	Default	3.3V	1.8V			
GPI012/MTDI	Pull-Down	0	1	0	Bootstrapping Pins	
		Bootin	g Mode			
Pin	Default	SPI Flash Boot	Download Boot	Set	Settings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care	0			
		Debugging Log on I	JOTXD During Booting			
PIn	Default	UOTXD Toggling	UOTXD Silent			
GPI015/MTD0	Pull-Up	1	0			
		Timing of	SDIO Slave			
Pin	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising—edge:Input Falling—edge:Output	Rising-edge:Input Rising-edge:Output	
GPI015/MTD0	Pull-Up	0	0	1	1	
GPI05	Pull-Up	0	1	0	1	





Sheet: / File: ESP32-DevKit-Lipo\_Rev\_B.sch

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