

## No Charger



## Bootstrapping Pins Settings

Voltage of Internal LDO(VDD_5060)		
Pin	Default	3.3V
GPIO12/MTDI	Pull-Up/Down	1

Bootstrapping Pins Settings					
Pin	Default	Booting Mode			
GPIO0	Pull-Up/Down	Download Boot			
GPIO2	Pull-Up/Down	Don't care			
Debugging Log on UOTXD During Booting					
Pin	Default	UOTXD Toggling			
GPIO15/MTDO	Pull-Up/Down	UOTXD Silent			
Timing of SDO Slew					
Pin	Default	Falling-edgeInput	Rising-edgeInput	Falling-edgeInput	Rising-edgeInput
GPIO15/MTDO	Pull-Up/Down	0	0	1	1

Interface	Signal	Pin
EMAC	EMAC_MDC_out	
	EMAC_MDIO_out	Any GPIO
	EMAC_CRS_out	
	EMAC_COL_out	
I2C	I2CEXT0_SCL_in	
	I2CEXT0_SDA_in	Any GPIO
	I2CEXT1_SCL_in	
	I2CEXT1_SDA_in	
	I2CEXT0_SCL_out	
	I2CEXT0_SDA_out	
	I2CEXT1_SCL_out	
	I2CEXT1_SDA_out	
General Purpose SPI	HSPIDQ_in_/out	
	HSPIDIn_/out	
	HSPICLK_in_/out	
	HSPICLS0_in_/out	
	HSPICLS1_out	
	HSPICLS2_out	
	VSPIDQ_in_/out	Any GPIO
	VSPIDIn_/out	
	VSPICLK_in_/out	
	VSPICLS0_in_/out	
VSPICLS1_out		
	VSPICLS2_out	
	VSPICLS2_out	

For more information refer to  
esp\_wroom\_32\_datasheet\_en.pdf



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Sheet: /  
File: ESP32-DevKit-Lipo\_Rev\_B.sch

**Title: ESP32-DevKit-Lipo**

Size: A3	Date: 2019-04-22
KiCad E.D.A. kicad 5.1.0-unknown-223e24f94ubuntu18.04.1	

Rev: B  
Id: 1/1

