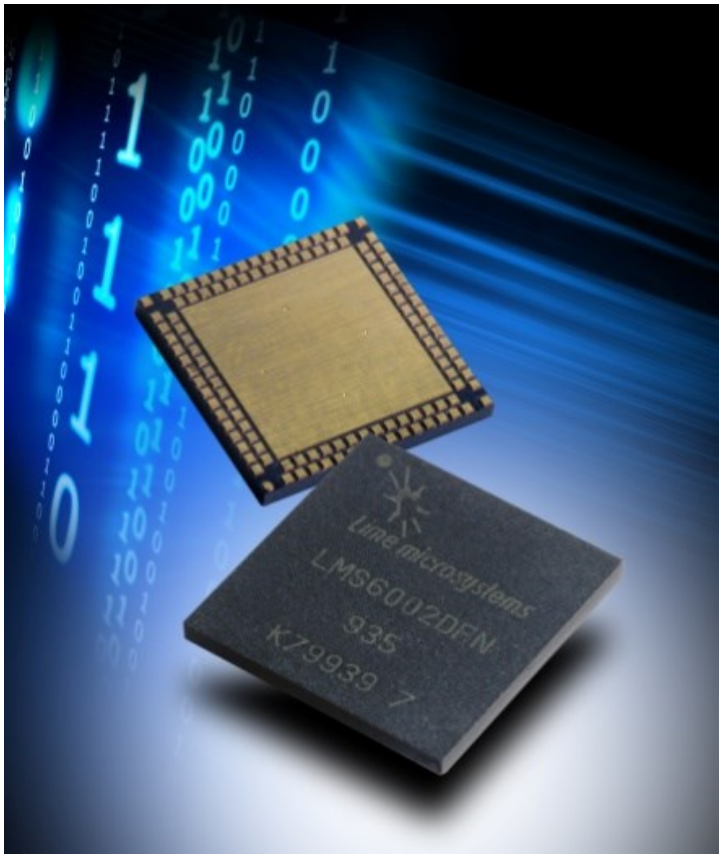
# IQ Imbalance and DC Offset Variation Versus Temperature

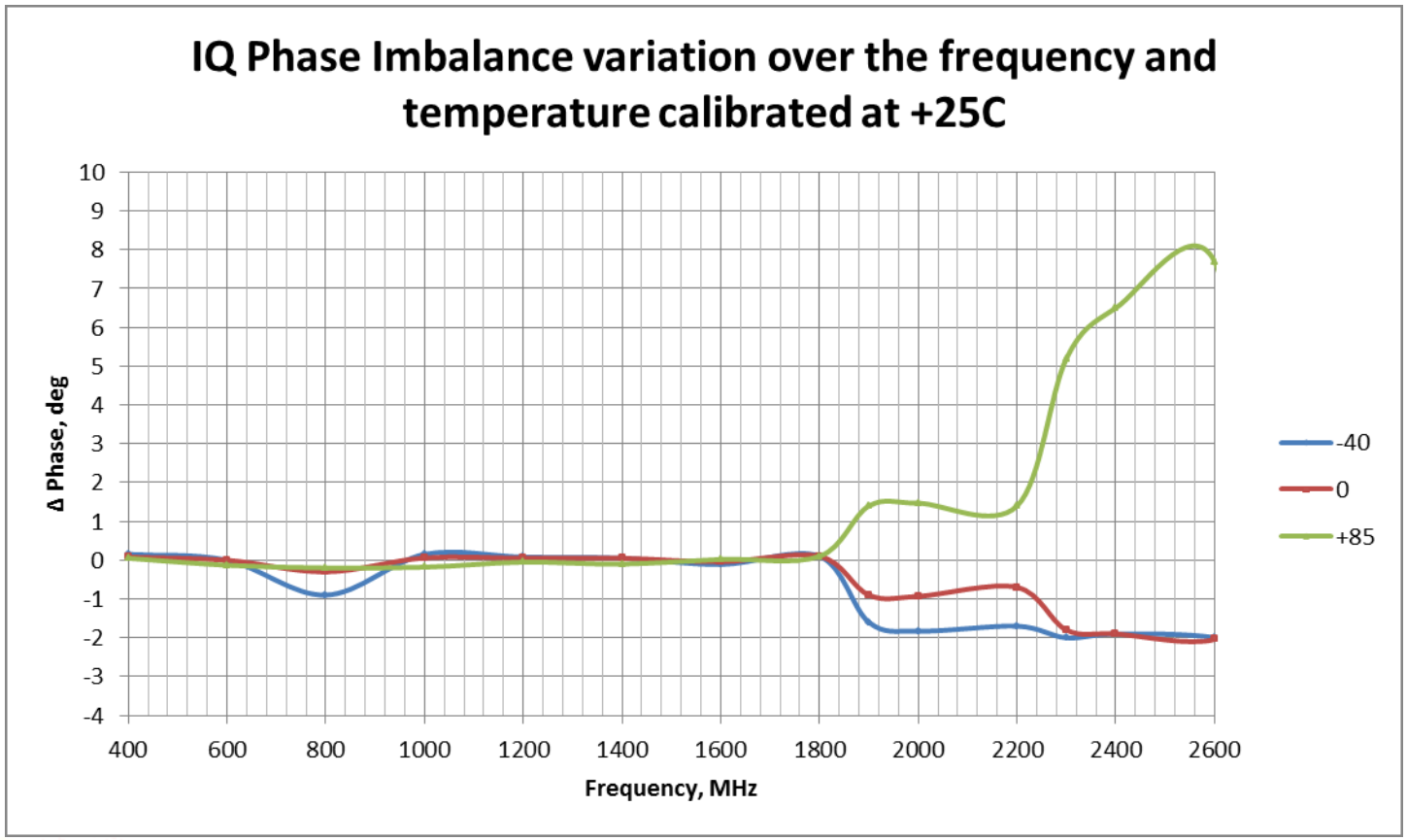
Measured on UWCT



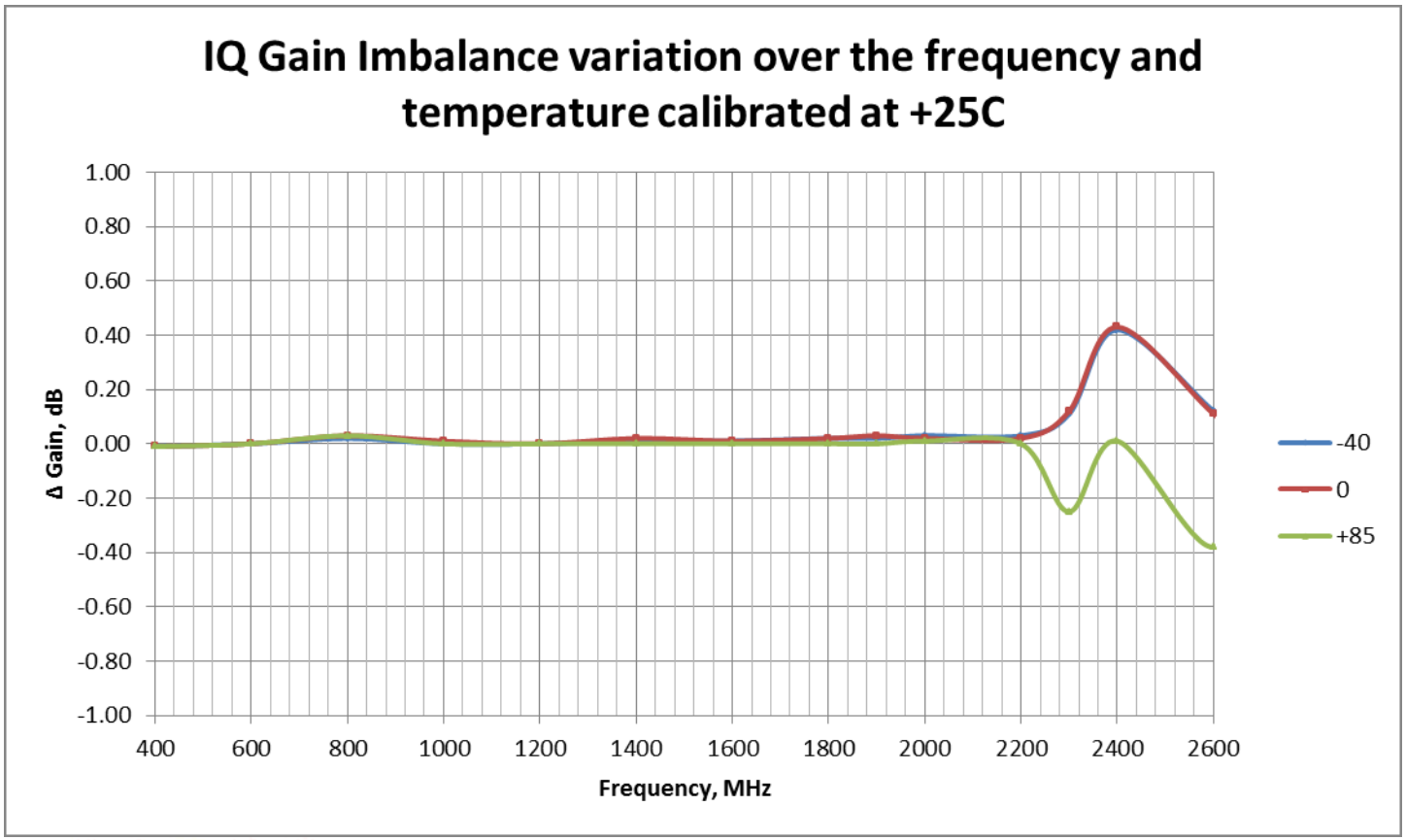
# Tx IQ Phase Error vs Temperature

**Test Description:**
- DACs off
- TXVGA1 and TXVGA2 at max gain
- TXLPF 10MHz
- CM voltage 700mV applied through TX analogue inputs
- TX DC calibration applied

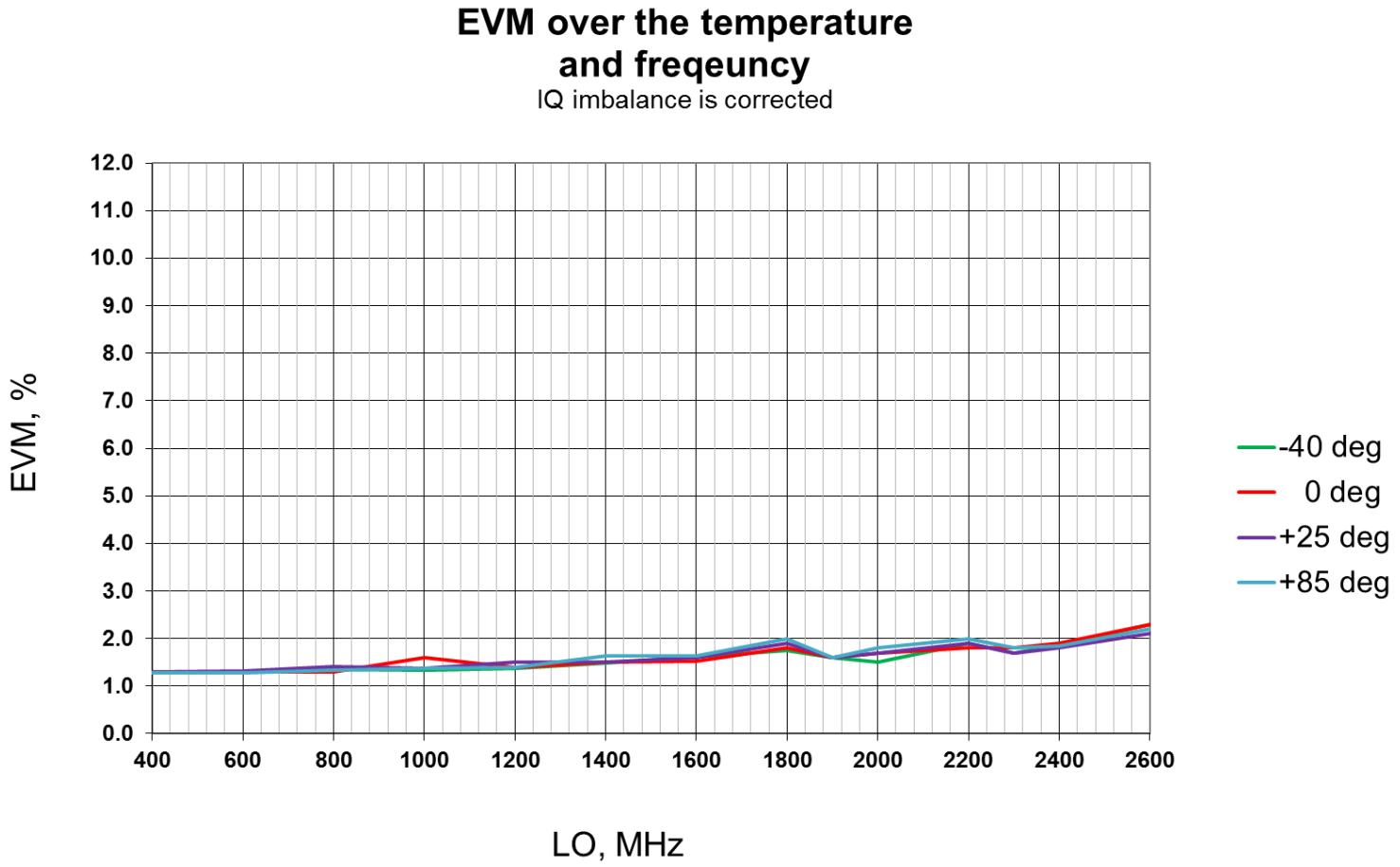
**Test Method:**
- 20 MHz WiMAX test signal applied through analogue TX inputs
- IQ phase/gain error measured
- Temperature swept and IQ imbalance measured without any further calibration



# Tx IQ Gain Error vs Temperature



# EVM vs Temperature



# Tx LO Leakage Level vs Temperature

**Test Description:**

- DACs off
- TXVGA1 and TXVGA2 at max gain
- CM voltage 700mV applied through TX analogue inputs while measuring LO leakage
- TX DC calibration applied
- LO at 2GHz

**Test Method:**

- CM voltage and CW max applied through TX analogue inputs to measure relative LO leakage level
- TX LO leakage calibrated at one temperature
- Temperature swept and LO leakage measured without any further calibration