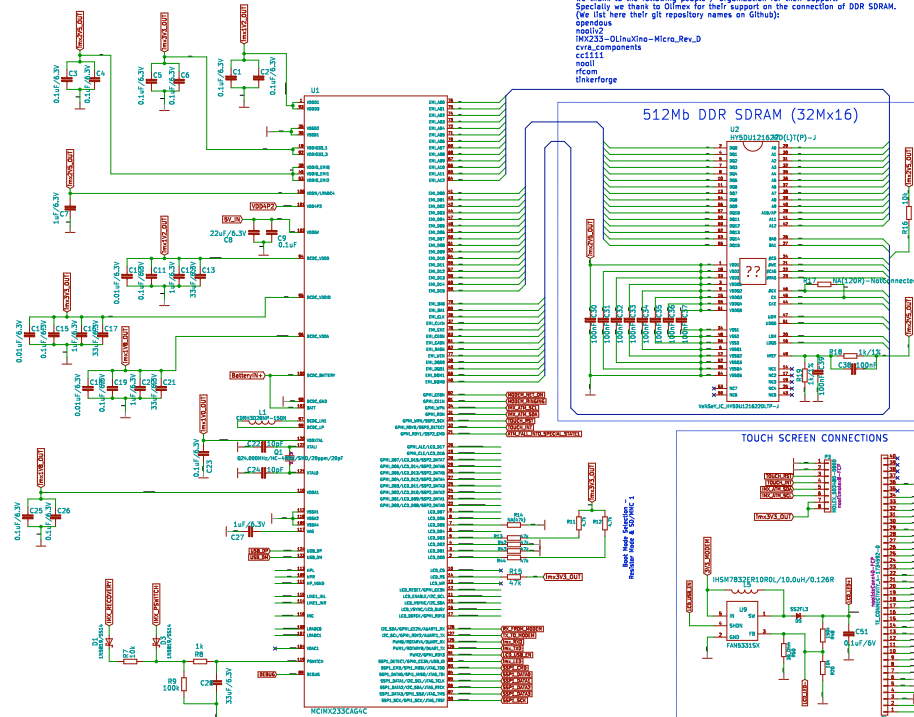


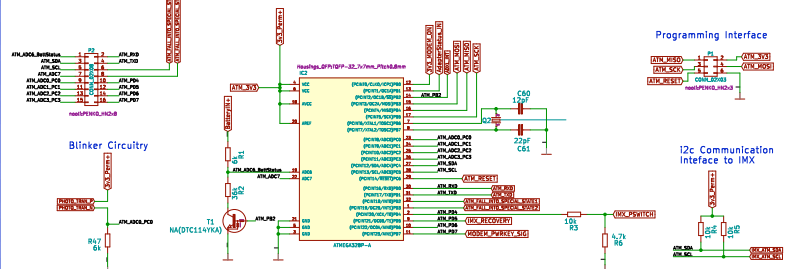
MAIN PROCESSOR (IMX233) RELATED CONNECTIONS & MEMORY UNIT (64 MB RAM)

We thank to the following people / organization for their support.
Specially we thank to Olimex for their support on the connection of DDR SDRAM.
(We list here their git repository names on Github):
openm2
imx233-OLinuxino-Micro_Rev_0
cvix_components
cc1111
nogl
rfcom
Interfuge

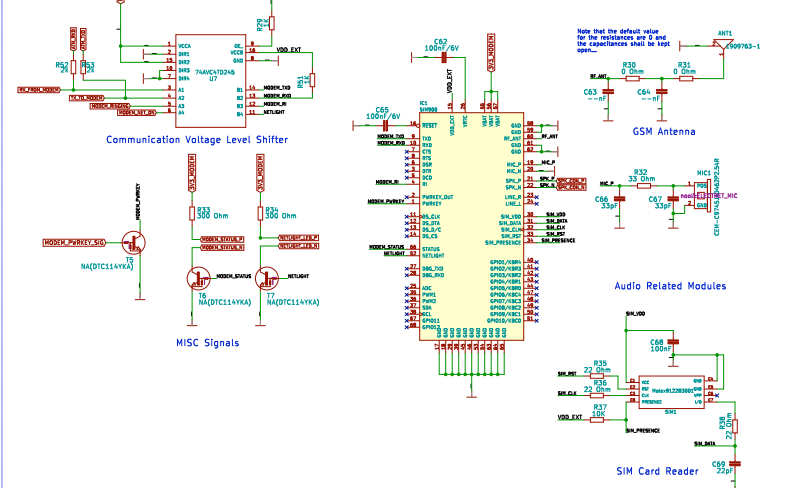


ATMEGA External Connector

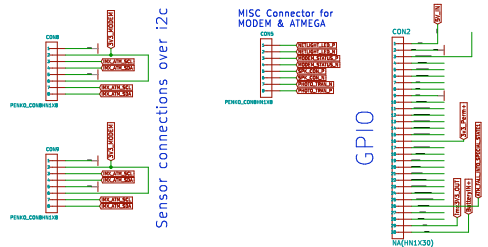
ATMEGA POWER MANAGEMENT CIRCUITRY



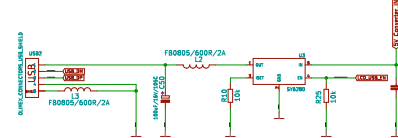
SIM900 MODEM CIRCUITRY



IMX233 GPIO & POWER CONNECTION OUTPUT



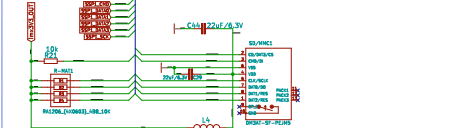
USB HOST - DEVELOPMENT PURPOSES



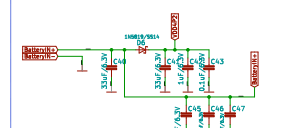
UART DEBUG / SJTAG & LED TEST CIRCUITRY



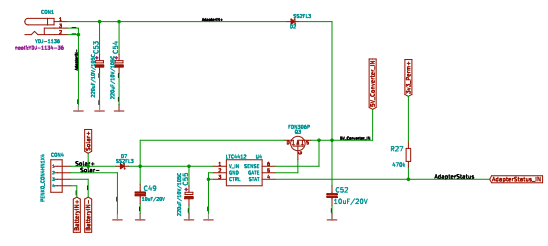
MICRO SD CARD & JTAG CONNECTIONS



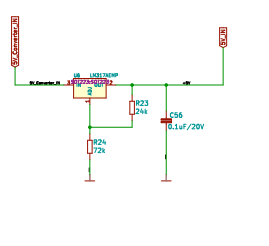
BATTERY INPUT CIRCUITRY



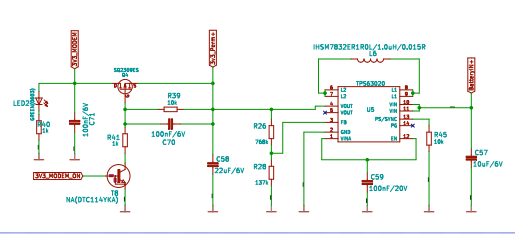
INPUT POWER SOURCE SELECTOR CIRCUIT



IMXX233 5V INPUT POWER



3v3_Perm+ GENERATOR CIRCUITRY (convertable to 3v5_Perm+)



ACCELEROMETER CIRCUITRY

