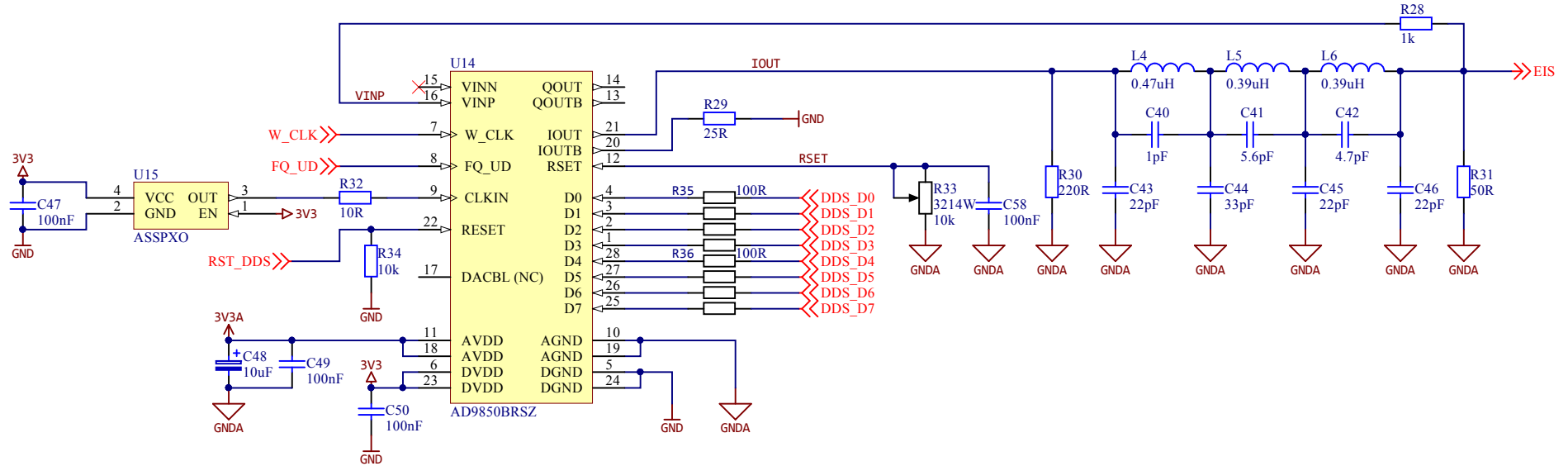
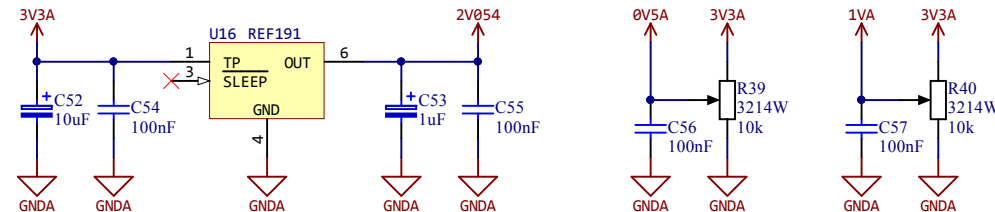


DDS module



VREF voltages



Title **Analog2.SchDoc**

Size: **A4**

Author: **Łukasz Przeniosło**

Date: **2015-11-08**

Time: **23:23:29**

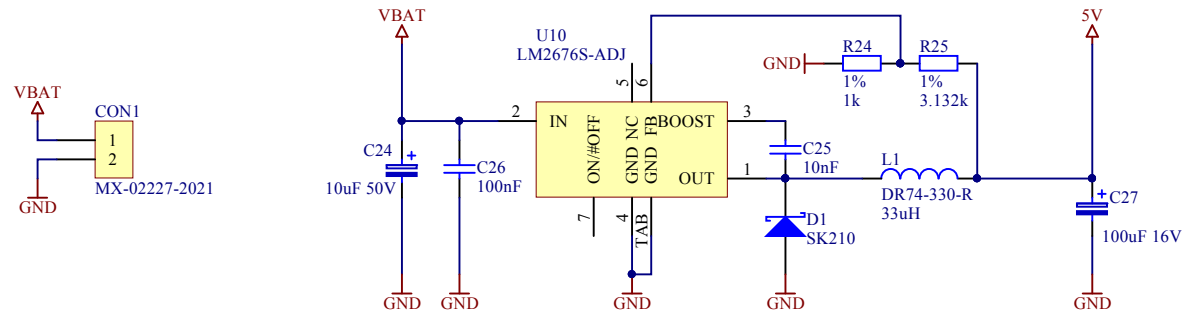
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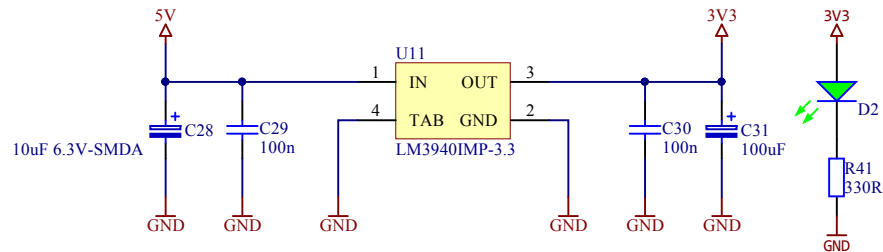
Zachodniopomorski
Uniwersytet Technologiczny
w Szczecinie
Wydział Elektryczny
Katedra KISSE



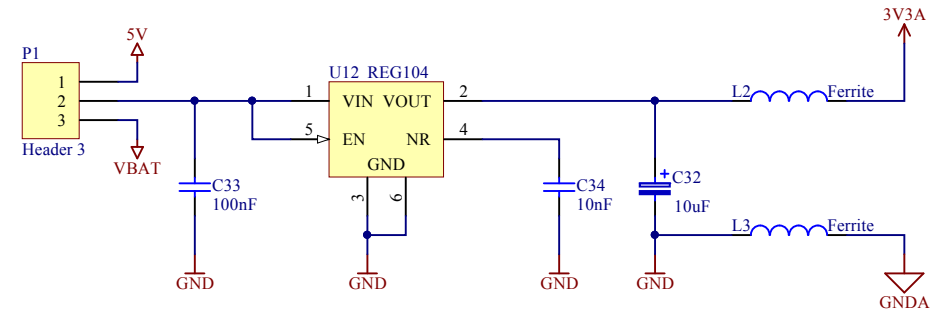
VBAT to 5V conversion



5V to 3.3V Converter



Analog 3.3V Converter



Use jumper to set input voltage for the analog circuit LDO.
VBAT is more stable, but LDO will get hot.
5V gives low drop, but is unstable (DC-DC converter output)

Beagle cape interface

