USB

18pF
C3
Q1
C4
18pF
12Mhz

T1
DTA114EET1G (2 X 10K)
+3V3

R1
1K5

USB-5V

R2 10K

PIO0_6-CONNECT
PIO0_3-VBUS

J1
USB
VBUS
D-
D+
ID
GND

U2
NUF2042XV6T1G
VBUSDMO
DMI
DPI
GND DPO

USB_DM
USB_DP

MCU

+3V3
R5
10K

XTALIN
XTLOUT

USB_DM 13
USB_DP 14

+3V3
6 VDD
C5
0.1uF

+3V3
29 VDD
C6
0.1uF

G1*5 GND

U3 LPC11U35FHI33/501

Pin	Signal	TP
2	RESET/PIO0_0	PIO0_0-RESET	TP20
3	CLKOUT/CT32B0_MAT2/USB_FTOGGLE/PIO0_1	PIO0_1-ISP_EN	TP17
8	SSEL0/CT16B0_CAP0/PIO0_2	PIO0_2	TP22
9	USB_VBUS/PIO0_3	PIO0_3-VBUS	TP25
10	SCL/PIO0_4	PIO0_4	TP23
11	SDA/PIO0_5	PIO0_5	TP26
15	USB_CONNECT/SCK0/PIO0_6	PIO0_6-CONNECT	TP2
16	CTS/PIO0_7	PIO0_7	TP3
17	MISO0/CT16B0_MAT0/PIO0_8	PIO0_8	TP4
18	MOSI0/CT16B0_MAT1/PIO0_9	PIO0_9-SWDO	TP5
19	SWCLK/SCK0/CT16B0_MAT2/PIO0_10	PIO0_10-SWDCLK	TP24
21	TD/AD0/CT32B0_MAT3/PIO0_11	PIO0_11	TP7
22	TMS/AD1/CT32B1_CAP0/PIO0_12	PIO0_12	TP8
23	TDO/AD2/CT32B1_MAT0/PIO0_13	PIO0_13	TP9
24	TRST/AD3/CT32B1_MAT1/PIO0_14	PIO0_14	TP10
25	SWDIO/AD4/CT32B1_MAT2/PIO0_15	PIO0_15-SWDIO	TP11
26	AD5/CT32B1_MAT3/WAKEUP/PIO0_16	PIO0_16	TP12
30	RTS/CT32B0_CAP0/SCLK/PIO0_17	PIO0_17	TP15
31	RXD/CT32B0_MAT0/PIO0_18	PIO0_18	TP16
32	TXD/CT32B0_MAT1/PIO0_19	PIO0_19	TP18
7	CT16B1_CAP0/PIO0_20	PIO0_20-LED1	TP21
12	CT16B1_MAT0/MOSI1/PIO0_21	PIO0_21	TP1
20	AD6/CT16B1_MAT1/MISO1/PIO0_22	PIO0_22	TP6
27	AD7/PIO0_23	PIO0_23	TP13
28	DCD/CT16B0_MAT2/SCK1/PIO1_15	PIO1_15	TP14
1	DTR/SSEL1/PIO1_19	PIO1_19	TP19

+3V3 USB-5V
TP30
TP27
EXT-VIN
TP29
TP28

POWER

USB-5V
D3
BAS40-05

U1
MCP1754ST-3302E/CB
VIN VOUT
VIN VOUT
GND
GND

EXT-VIN 1

C1
1u
C2
1u
+3V3

SWD DEBUGGER PORT
(OPTIONAL)

+3V3
J2
R4 10K +3V3

PIO0_15-SWDIO
PIO0_10-SWDCLK
PIO0_9-SWDO
PIO0_0-RESET

R3 10K

SWD HEADER

USER INTERFACE

+3V3
R6
1K5
PWR
D2
GRN

PIO0_20-LED1
R7
10K
LED1
D1
RED

+3V3
C7
0.1uF
PIO0_1-ISP_EN
D4
BAS40-05
PIO0_0-RESET

ISP
S1

RST
S2

* Early boards had the LED
  connected to PIO0_7.

OSH. Share alike and attributions are optional. Commercial use ok.

ARM PRO MINI

TITLE: arm-pro-mini

Document Number:

REV:
3

Date: not saved!

Sheet: 1/1