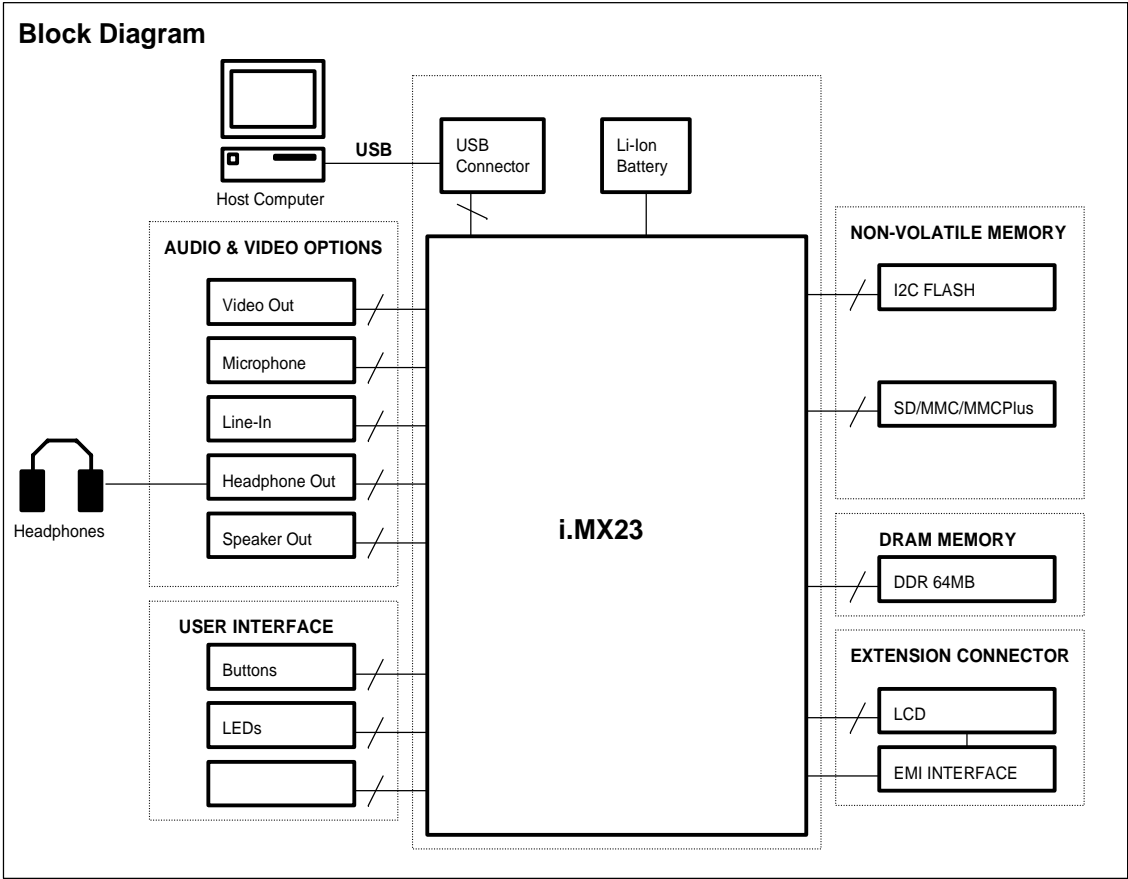


# LOCUX IMX233 CPU MODULE REV 1.0

Rev. 1.0

**Revision History**  
REV. 1.0 - 04/04/2012  
- Original Release



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B	<BLOCK DIAGRAM>	1.0	
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## i.MX233 Bootmode Resistor Selection

### Option 1: OTP

LCD\_RS = 0  
This mode requires that the appropriate boot mode be written into the OTP boot mode register.

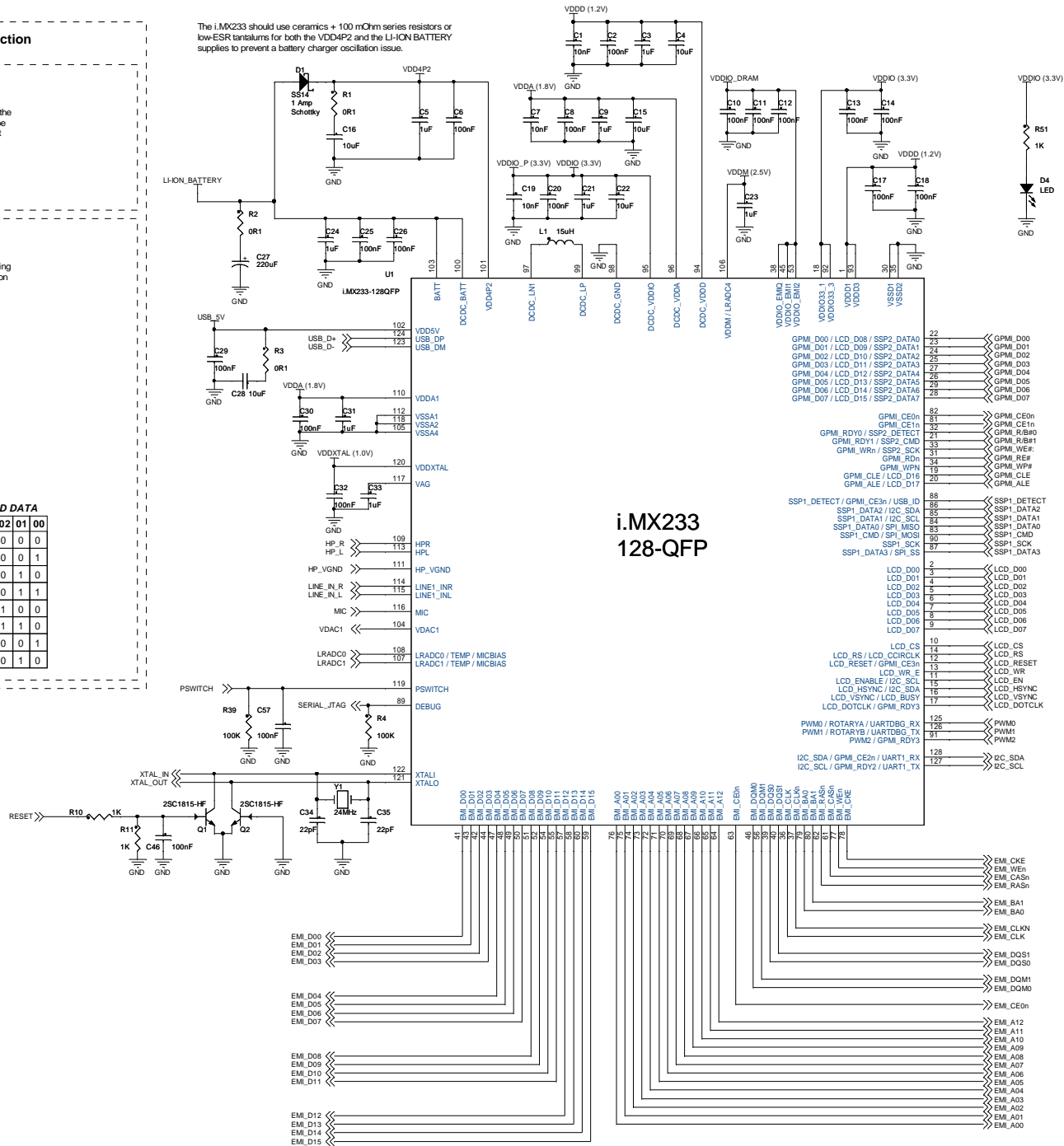
### Option 2: Resistor

LCD\_RS = 1  
Select boot option by using external pins configuration

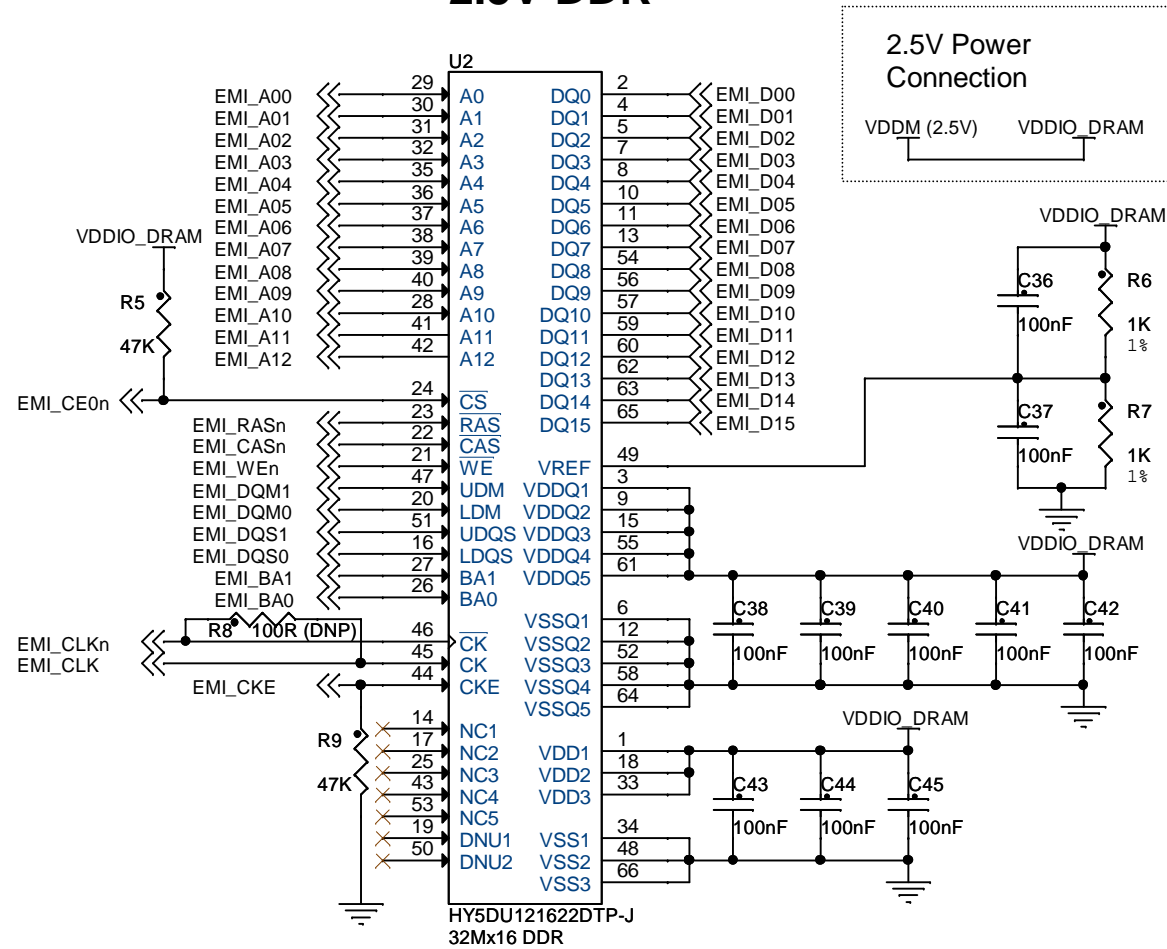
LCD\_D05 = ETM Enable Bit  
Low = ETM disabled  
High = ETM enabled

BOOT MODE	LCD DATA			
	03	02	01	00
USB	0	0	0	0
I2C Master	0	0	0	1
SPI Flash 1 Master	0	0	1	0
SPI Flash 2 Master	0	0	1	1
NAND	0	1	0	0
DEBUG	0	1	1	0
SD/MMC 1	1	0	0	1
SD/MMC 2	1	0	1	0

The i.MX233 should use ceramics + 100 mOhm series resistors or low-ESR tantalums for both the VDD4P2 and the L-I-ON BATTERY supplies to prevent a battery charger oscillation issue.



## 2.5V DDR



Title

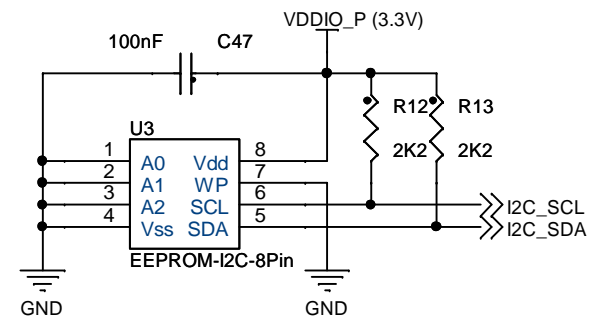
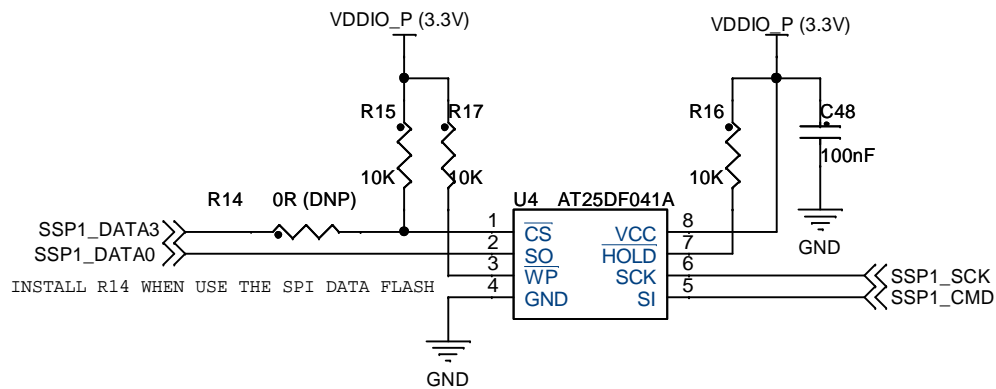
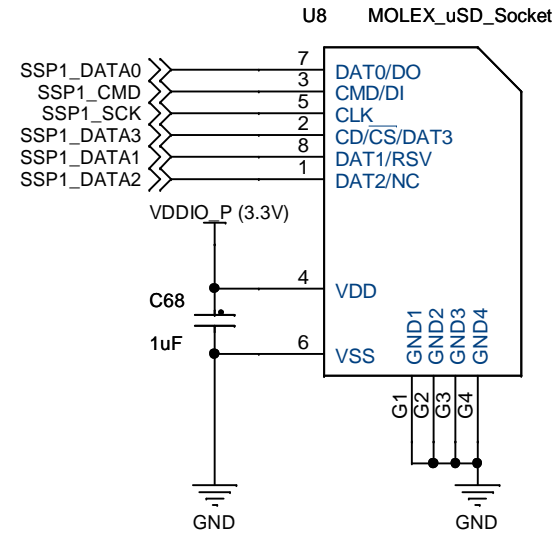
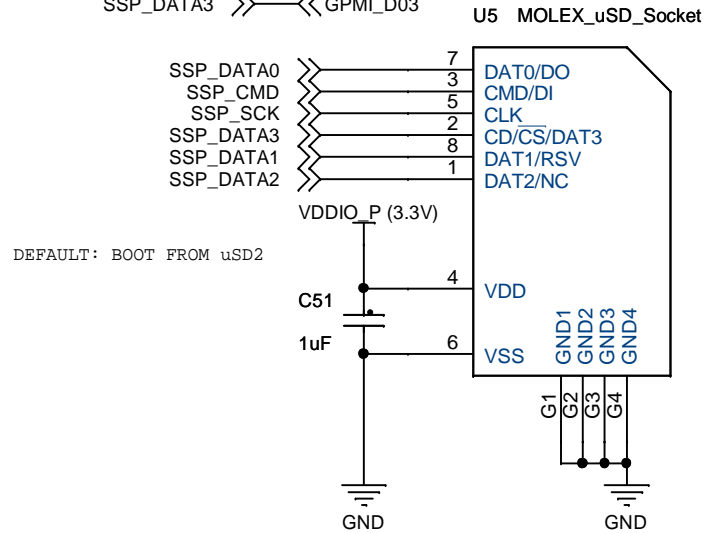
LOCUX IMX233 CPU MODULE

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	<DRAM MEMORY>	

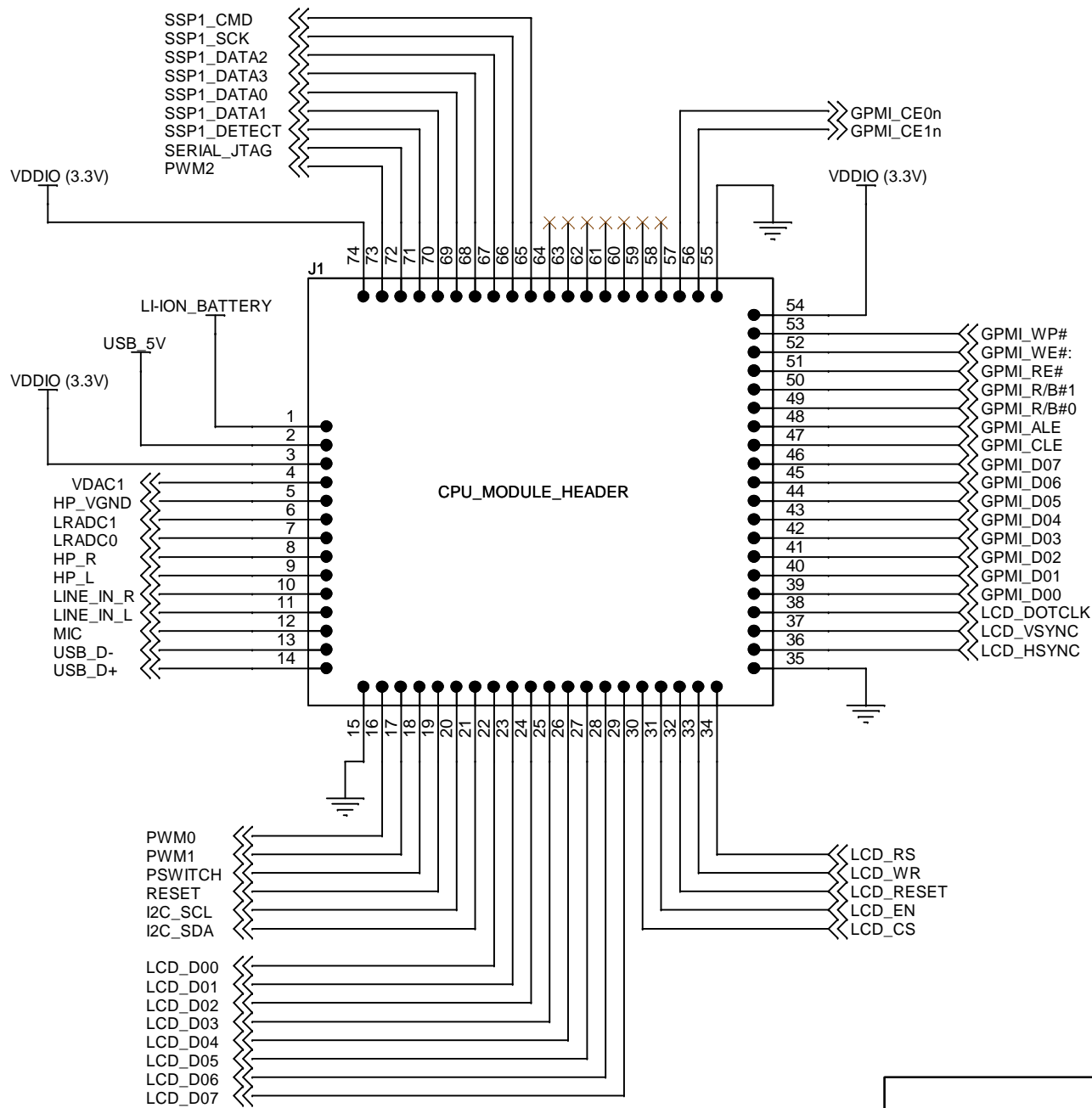
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## SSP2 (uSD Interface)

On Page		i.MX23 Pin
Net Name		Assignment
SSP_CMD		GPML_R/B#1
SSP_SCK		GPML_WE#:
SSP_DATA0		GPML_D00
SSP_DATA1		GPML_D01
SSP_DATA2		GPML_D02
SSP_DATA3		GPML_D03



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