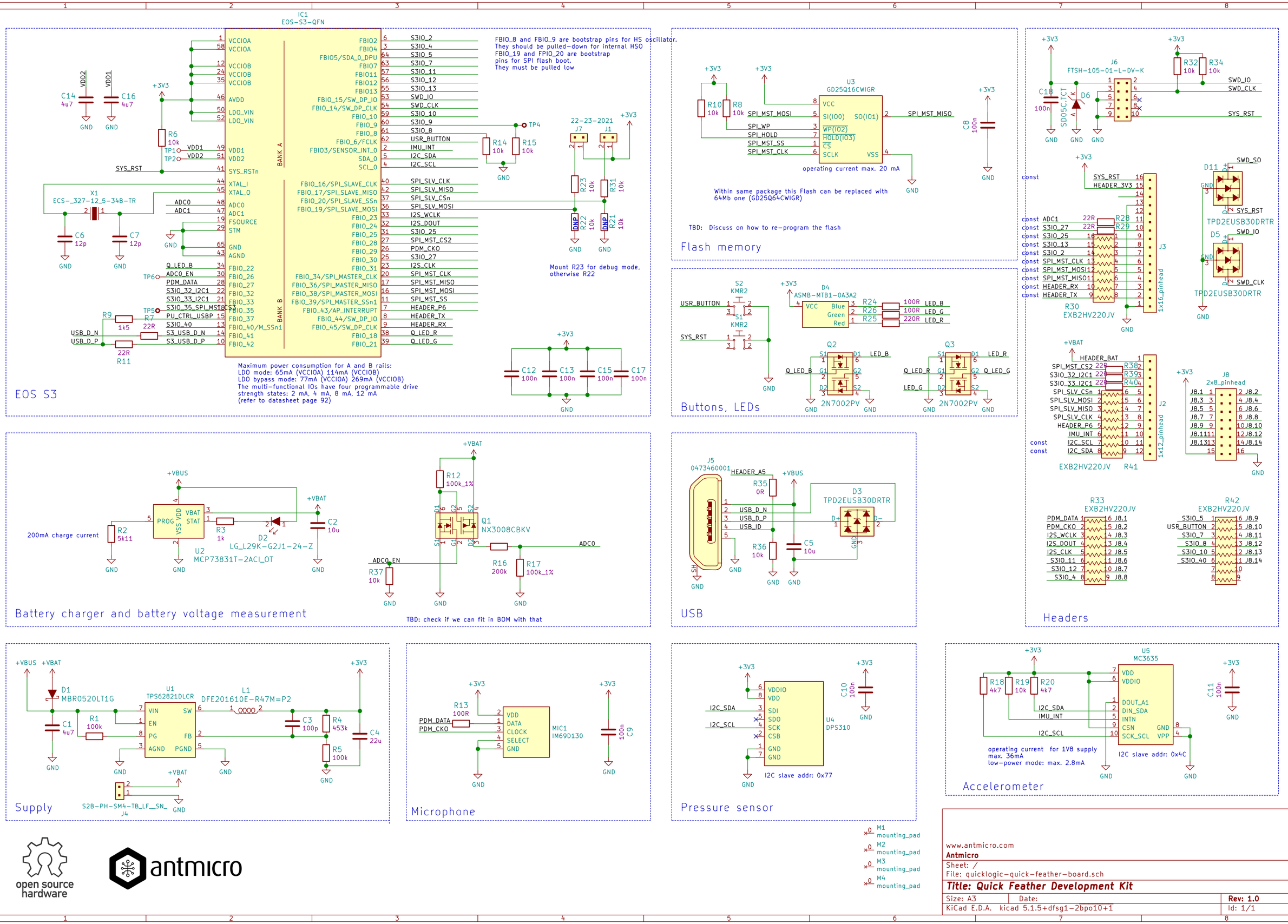
IC1
EOS-S3-QFN
FBIO_8 and FBIO_9 are bootstrap pins for HS oscillator.
They should be pulled-down for internal HSO
FBIO_19 and FPIO_20 are bootstrap
pins for SPI flash boot.
They must be pulled low
Maximum power consumption for A and B rails:
LDO mode: 65mA (VCCIOA) 114mA (VCCIOB)
LDO bypass mode: 77mA (VCCIOA) 269mA (VCCIOB)
The multi-functional IOs have four programmable drive
strength states: 2 mA, 4 mA, 8 mA, 12 mA
(refer to datasheet page 92)
EOS S3
Mount R23 for debug mode,
otherwise R22
U3
GD25Q16CWIGR
operating current max. 20 mA
Within same package this Flash can be replaced with
64Mb one (GD25Q64CWIGR)
TBD: Discuss on how to re-program the flash
Flash memory
Buttons, LEDs
2N7002PV
J6
FTSH-105-01-L-DV-K
SD05C.TCT
TPD2EUSB30DRTR
EXB2HV220JV
J3
1x16_pinhead
J2
1x12_pinhead
J8
2x8_pinhead
R41
R33
R42
Headers
Battery charger and battery voltage measurement
200mA charge current
U2
MCP73831T-2ACI_OT
LG_L29K-G2J1-24-Z
Q1
NX3008CBKV
TBD: check if we can fit in BOM with that
USB
J5
0473460001
D3
TPD2EUSB30DRTR
Supply
U1
TPS62821DLCR
MBR0520LT1G
S2B-PH-SM4-TB_LF__SN_
J4
Microphone
MIC1
IM69D130
Pressure sensor
U4
DPS310
I2C slave addr: 0x77
Accelerometer
U5
MC3635
operating current for 1V8 supply
max. 36mA
low-power mode: max. 2.8mA
I2C slave addr: 0x4C
M1 mounting_pad
M2 mounting_pad
M3 mounting_pad
M4 mounting_pad
www.antmicro.com
Antmicro
Sheet: /
File: quicklogic-quick-feather-board.sch
Title: Quick Feather Development Kit
Size: A3
Date:
Rev: 1.0
KiCad E.D.A. kicad 5.1.5+dfsg1-2bpo10+1
Id: 1/1
open source hardware
antmicro