

File: Data Interface Connector.sch

Sheet: /Data Interface Connector/

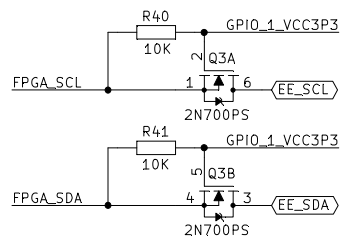
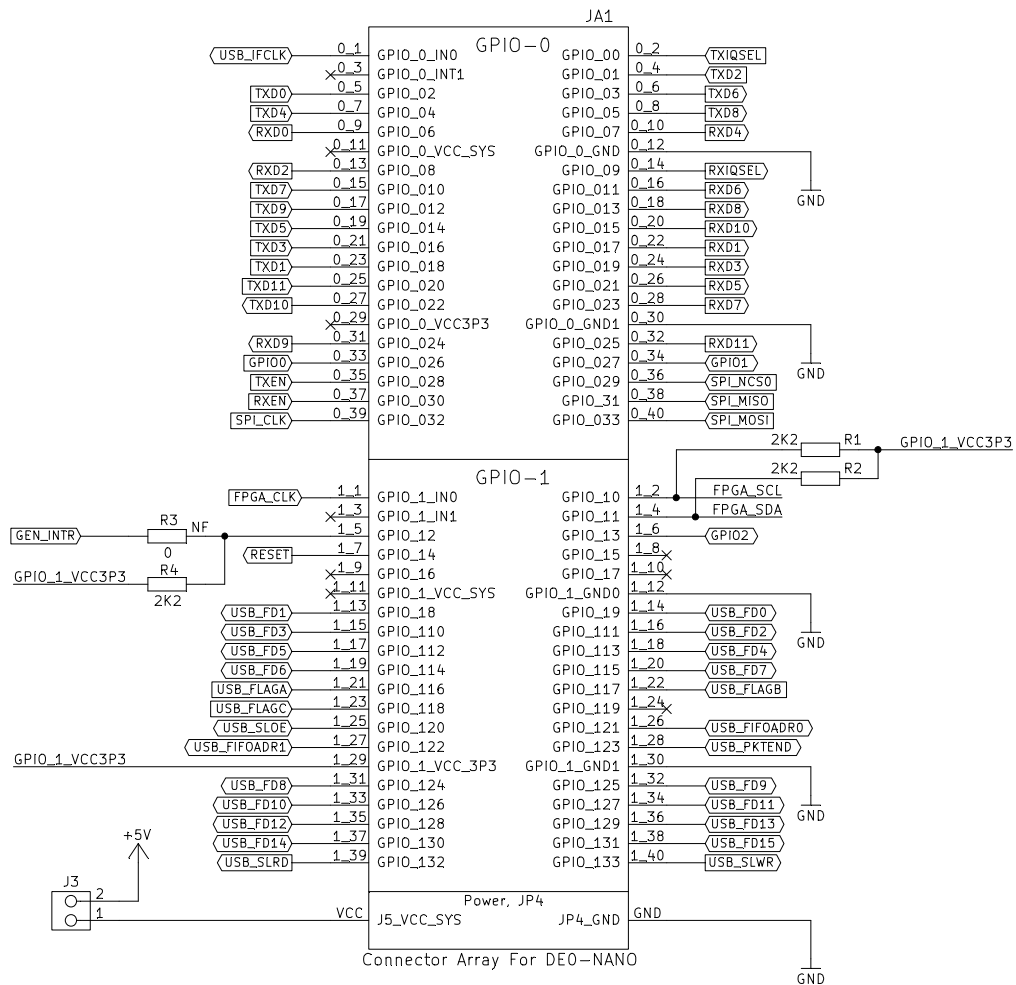
Title: Digital Interface v1.0

Size: User Date: 25 oct 2012

KiCad E.D.A. eeschema (2012-08-04 BZR 3667)-testing

Rev: 3

Id: 2/7



File: FPGA Module.sch

Sheet: /FPGA Module/

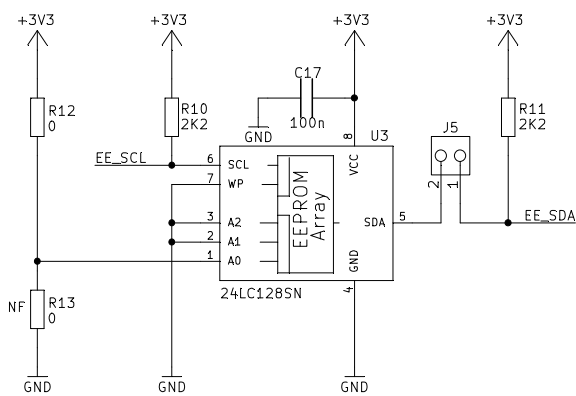
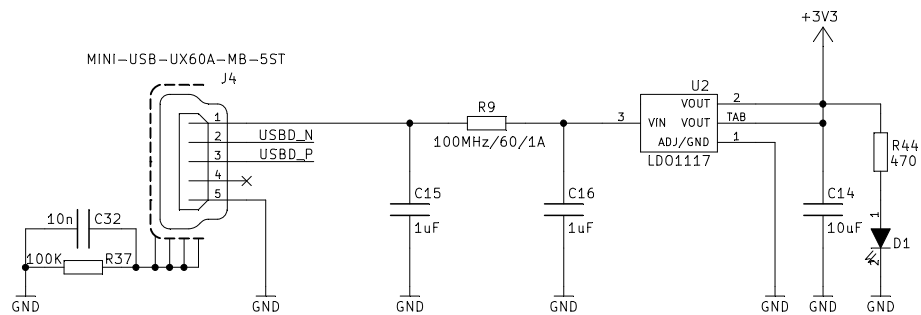
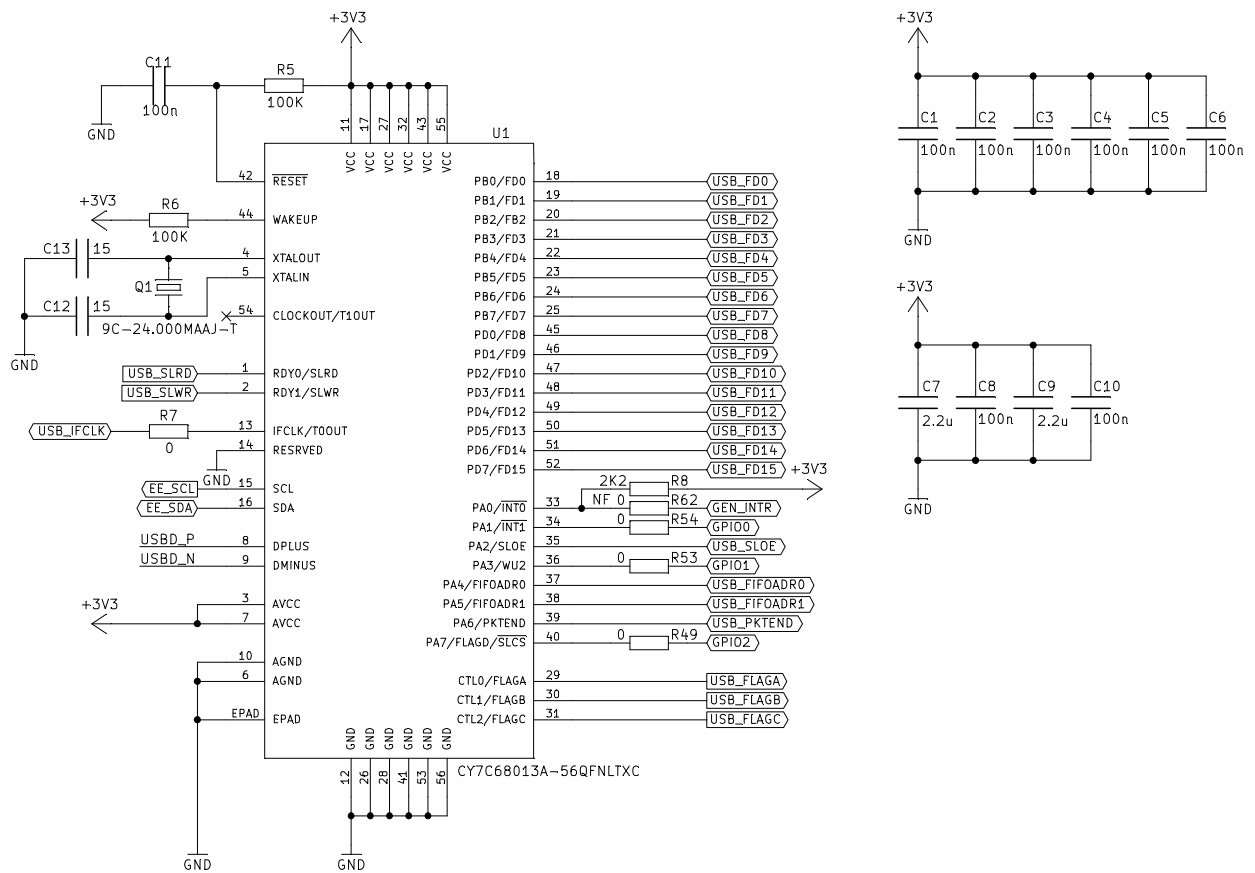
Title: Digital Interface v1.0

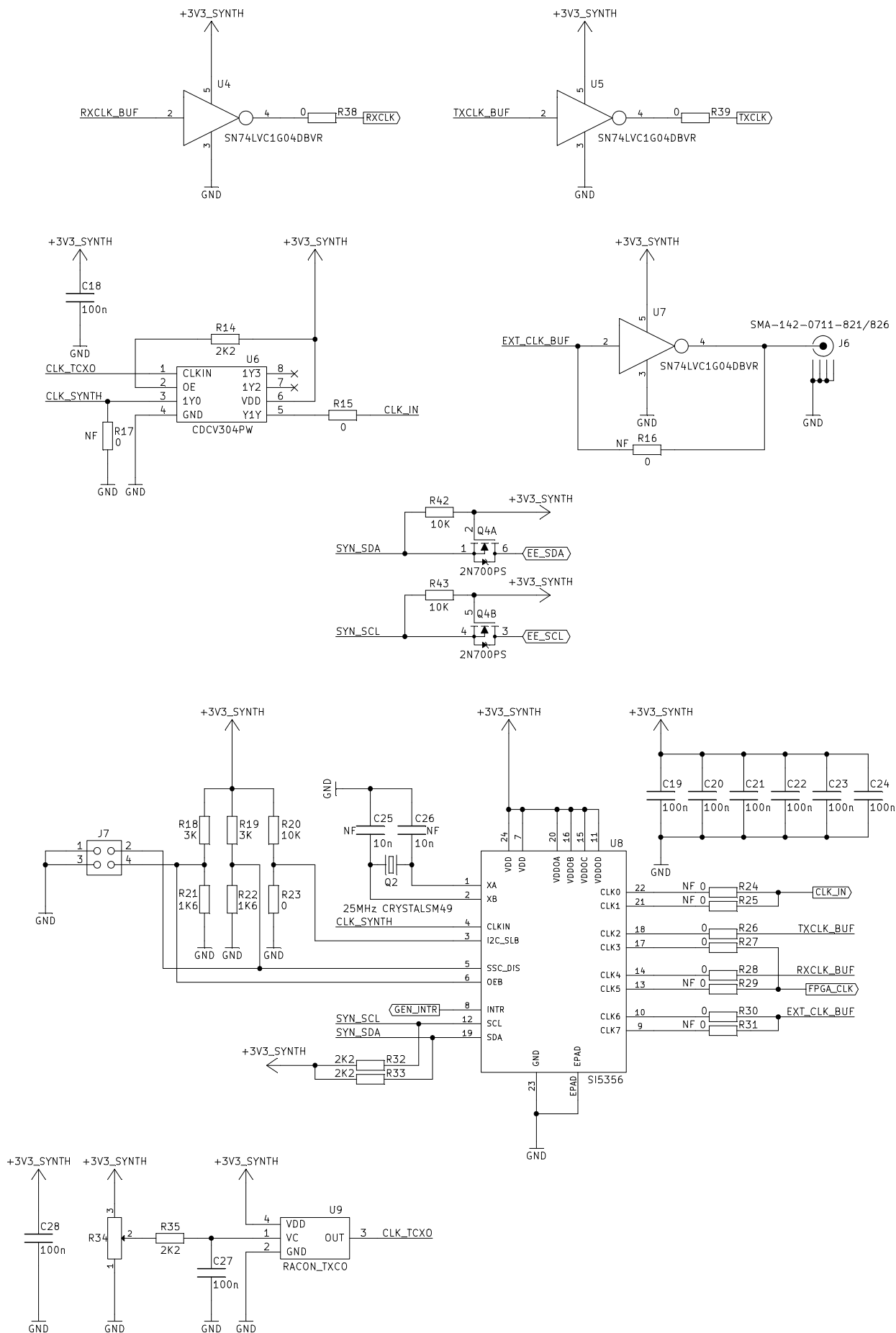
Size: User Date: 25 oct 2012

KiCad E.D.A. eeschema (2012-08-04 BZR 3667)-testing

Rev: 3

Id: 3/7





File: Clock Generator.sch

Sheet: /Clock Generator/

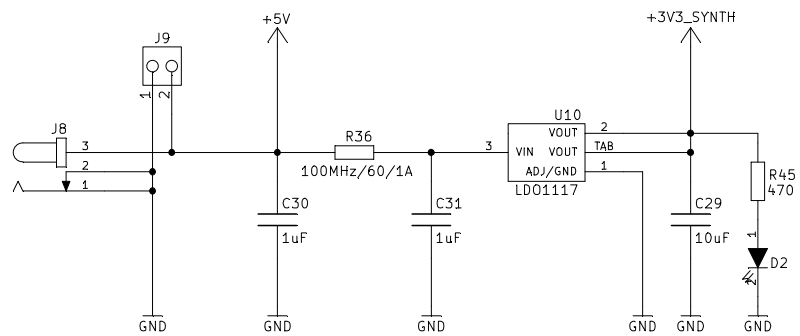
Title: Digital Interface v1.0

Size: User Date: 25 oct 2012

Rev: 3

KiCad E.D.A. eeschema (2012-08-04 BZR 3667)-testing

Id: 5/7



File: Power Supply for Clock Generator.sch		
Sheet: /Power Supply for Clock Generator/		
Title: Digital Interface v1.0		
Size: User	Date: 25 oct 2012	Rev: 3
KiCad E.D.A.	eeschema (2012-08-04 BZR 3667)-testing	Id: 6/7

