

# Programming the FPGA Configuration Flash

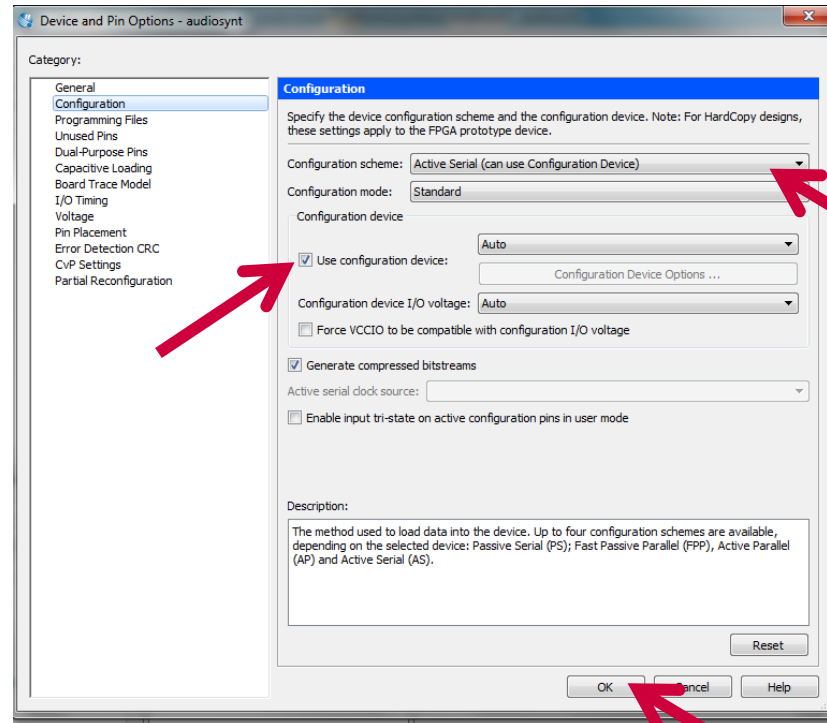
# Setting Quartus to generate \*.pof

Quartus must be configured to generate a Programmers Object File (pof)  
Which is suited to be programmed into the External Flash (Configuration Device)

The screenshot shows the Quartus II 32-bit interface. The 'Device...' menu is open, with a red arrow pointing to the 'Settings...' option. The 'Available devices' list is also visible, with a red arrow pointing to the 'Device and Pin Options...' button. The 'Available devices' list contains the following data:

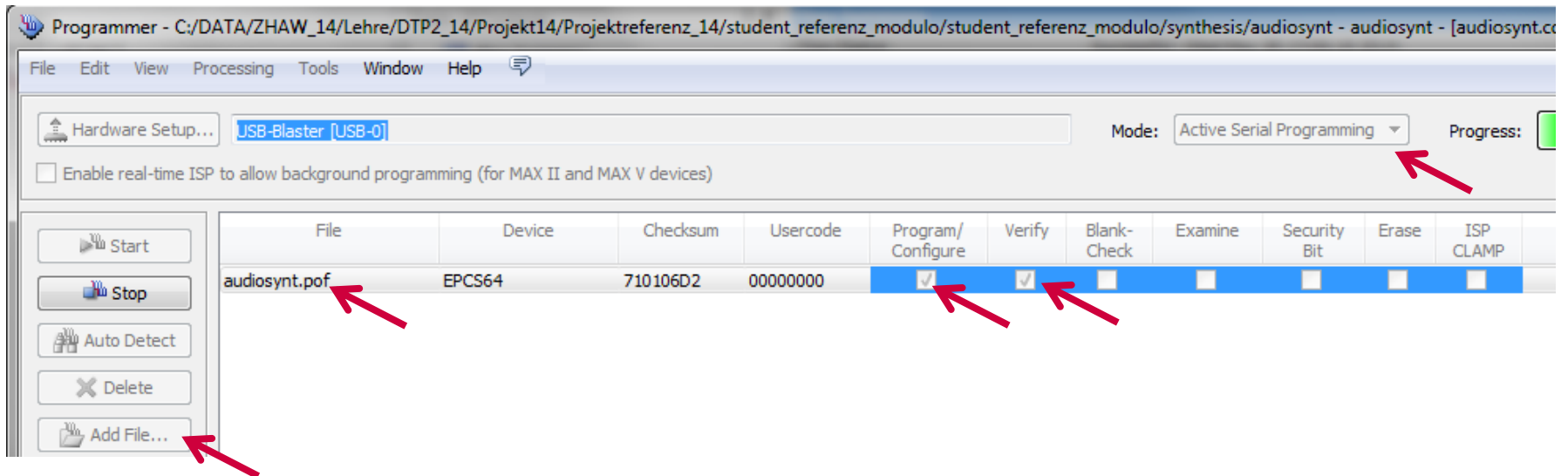
Name	Core Voltage	LEs	User I/Os	Memory Bits	Embedded multiplier
EP4CE75F29C9L	1.0V	75408	427	2810880	400
EP4CE75F29I7	1.2V	75408	427	2810880	400
EP4CE75F29I8L	1.0V	75408	427	2810880	400
EP4CE75U19I7	1.2V	75408	293	2810880	400
EP4CE115F23C7	1.2V	114480	281	3981312	532
EP4CE115F23C8	1.2V	114480	281	3981312	532

# Setting Device Options



After saving the menu, recompile the device and check whether the \*.pof file is generated located in the *synthesis* or *output* directory

1. Set the *RUN-PRG* Switch on the DE115 board to *PRG*
2. Open the Programmer and set it to *Active Serial Programming*
3. Remove the .sof and add the .pof file



4. Programm and Verify the device
5. Set the Switch back to *RUN*.
6. Turn on and off the power of the board to load the configuration