ITCT Embedded Controller Design (ECD) Project Date: Feb. 4/11 Author: Al Jeffrey

Module		Port	MODE	Pin			
Port T	=			1			
•	Stepper Motor Coil Pair 2 In	PT7	GPIO OUT	14			
•	Stepper Motor Coil Pair 2 Out	PT6	GPIO OUT	13			
•	Stepper Motor Coil Pair 1 In	PT5	GPIO OUT	12			
•	Stepper Motor Coil Pair 1 M	PT4	GPIO OUT	11			
Port T	Stepper Motor Con Fair 1 Out						
Timer Module							
ımer		PT3	Timer Out	8			
•	RC Servo 2 Out	PT2	Timer Out	7			
•	RC Servo 1 Out	PT1	Input Capture	6			
•	DC Motor Encoder 2 In	PT0	Input Capture	5			
•	DC Motor Encoder 1 In		input Suptairs	~			
Port S							
•	LED 2 (Green)	PS3	GPIO Out	66			
•	LED 1 (Green)	PS2	GPIO Out	65			
Port S	223 (3.66.1)						
Serial Communications Interface (SCI)							
•	SCI Transmit	PS1	SCITX	64			
•	SCI Receive	PS0	SCIRX	63			
Port M	SOFFIECEIVE						
Serial Peripheral Interface (SPI)							
•	SCK	PM5	SPI SCK	70			
•	MOSI	PM4	SPI MOSI	71			
•		PM3	SPIN SS	72			
•	N_SS	PM2	SPI MISO	73			
•	MISO	1 1012	31 1 111130	73			
Port M		PM1	GPIO Reserved	74			
•	CAN_TX	PM0	GPIO Reserved	75			
•	CAN_RX	FIVIU	GFIO Reserved	75			
Port J							
Serial	Peripheral Interface (SPI)						
•	SPI_CS2	PJ6	GPIO Out	69			
•	SPI_CS1	PJ7	GPIO Out	68			
Port P							
•	Keypad wake	PP7	GPIO Reserved	78			
•	ROMCTL (Pull up with 10K and connect to	PP6	ROMCTL In	67			
	header pin for GPIO)						
Port P	,						
PWM N	Module						
•	DC Motor 2 PWM	PP5	PWM Out	79			
•	DC Motor 1 PWM	PP4	PWM Out	80			
Port P							
•	Unused	PP3	GPIO Unused	1			
•	Unused	PP2	GPIO Unused	2			
•	Unused	PP1	GPIO Unused	3			
•	Unused	PP0	GPIO Unused	4			
Port A							
TOILAL		PAD7	GPIO IN	58			
•	Stepper Switch Loft	PAD7	GPIO IN	57			
•	Stepper Switch Left	PAD5	GPIO IN	56			
•	Unused/Keyboard scan 5	PAD3 PAD4	GPIO Reserved	55			
•	Unused/Keyboard scan 4	PAD4 PAD3		55			
•	Analog Input 3/Keyboard scan 3	PAD3 PAD2	Analog In/GPIO Analog In	53			
•	Analog Input 2	PAD2 PAD1		53			
•	Analog Input 1	PAD1 PAD0	Analog In Analog In	52			
	Analog Input 0	LEADU	I AHAIUU III	1 (3) 1			

ITCT Embedded Controller Design (ECD) Project

Port A				
• Unu	sed	PA7	GPIO Unused	48
• LCD)_RW	PA6	GPIO Out	47
• LCD)_RS	PA5	GPIO Out	46
• LCD	E	PA4	GPIO Out	45
• LCD	Data I/O 3	PA3	GPIO I/O	44
• LCD	Data I/O 2	PA2	GPIO I/O	43
LCD	Data I/O 1	PA1	GPIO I/O	42
• LCD) Data I/O 0	PA0	GPIO I/O	41
Port B				
• Unu	sed	PB7	GPIO Unused	23
• Unu	sed	PB6	GPIO Unused	22
• Unu	sed	PB5	GPIO Unused	21
• Unu	sed	PB4	GPIO Unused	20
• DC I	Motor 2 Direction B	PB3	GPIO Out	19
• DC I	Motor 2 Direction A	PB2	GPIO Out	18
• DC I	Motor 1 Direction B	PB1	GPIO Out	17
• DC I	Motor 1 Direction A	PB0	GPIO Out	16