REV	DATE	BY	CHANGE
A	12/14/2011	BBT	Initial release
A1	02/02/2012	BBT	Added one more button. Fixed reset and power button issues. Added a pull up to WP of EEPROM
A2	04/30/2012	BBT	1. LCD_AVDD_EN Signal is moved from J2-42 (GPIO0_7) to J1-20 (GPIO1_31)

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Title: BeagleBone LCD7 Cape COVER PAGE

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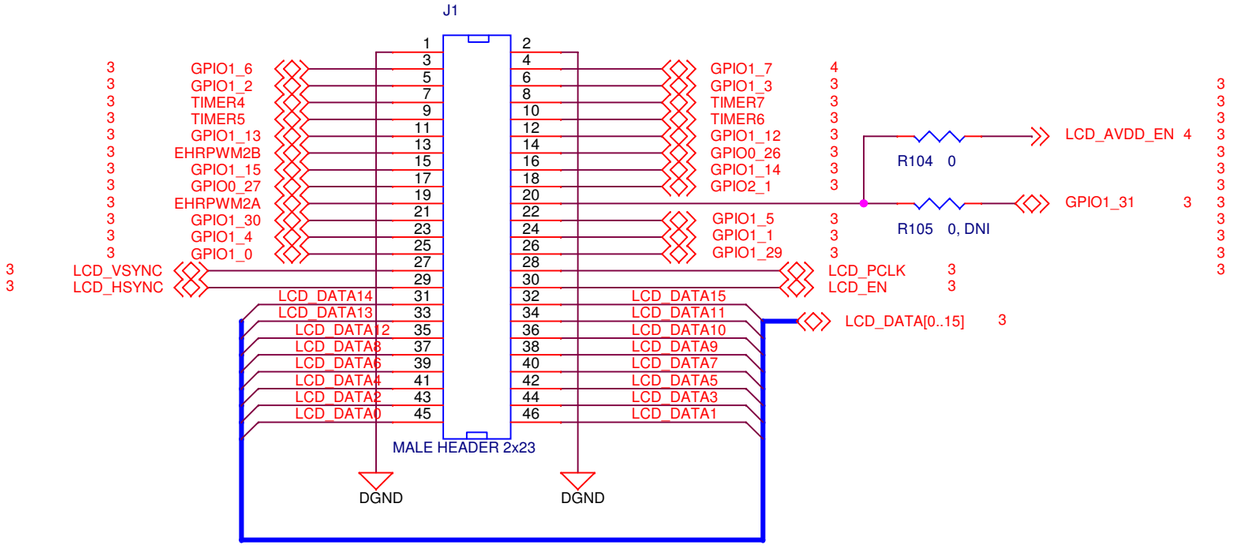
CONN A IN



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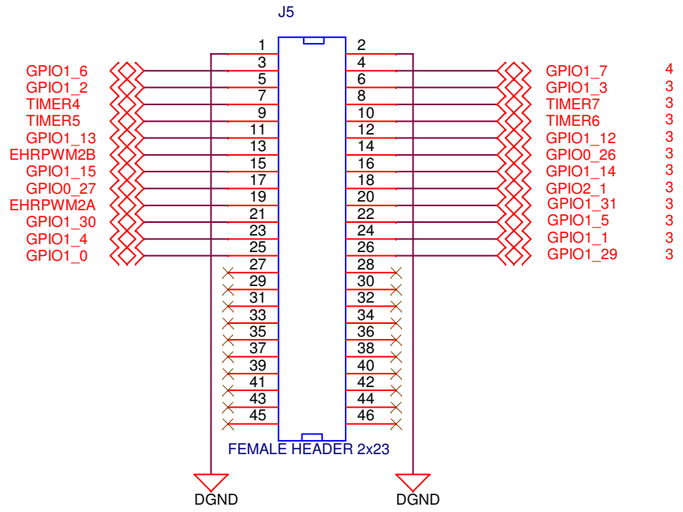

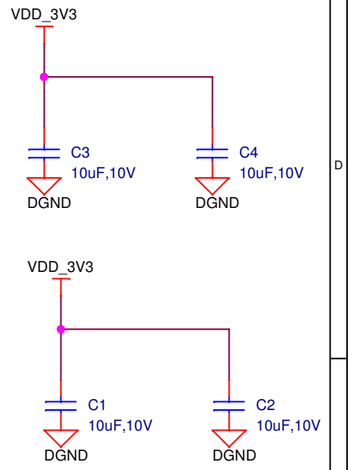

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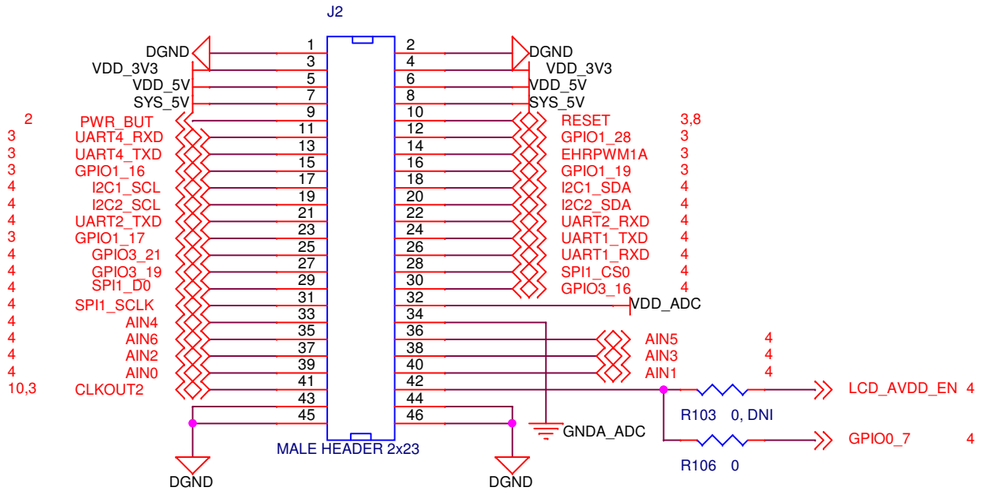

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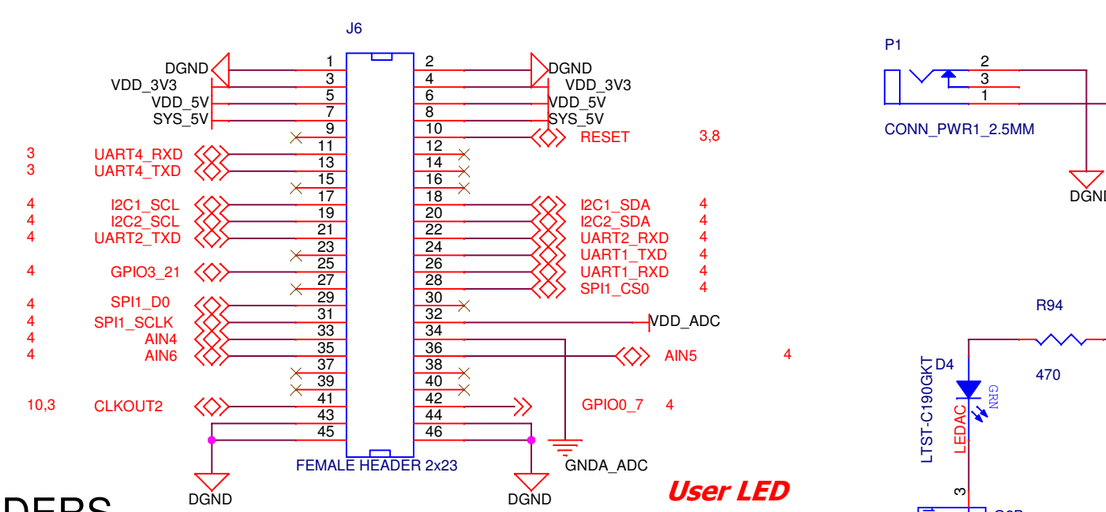

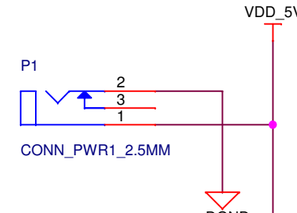

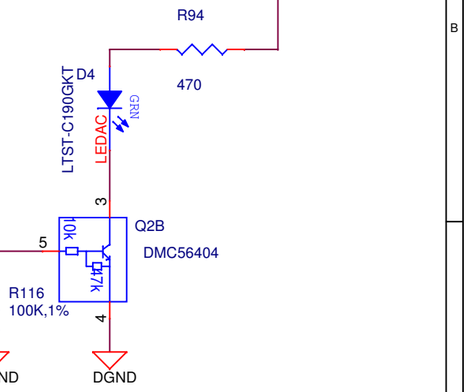

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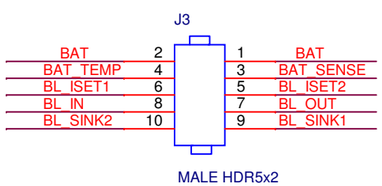

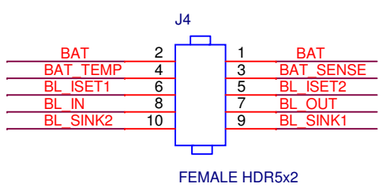

AUX_BL

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Title: BeagleBone LCD7 Cape HOST CONNECTORS

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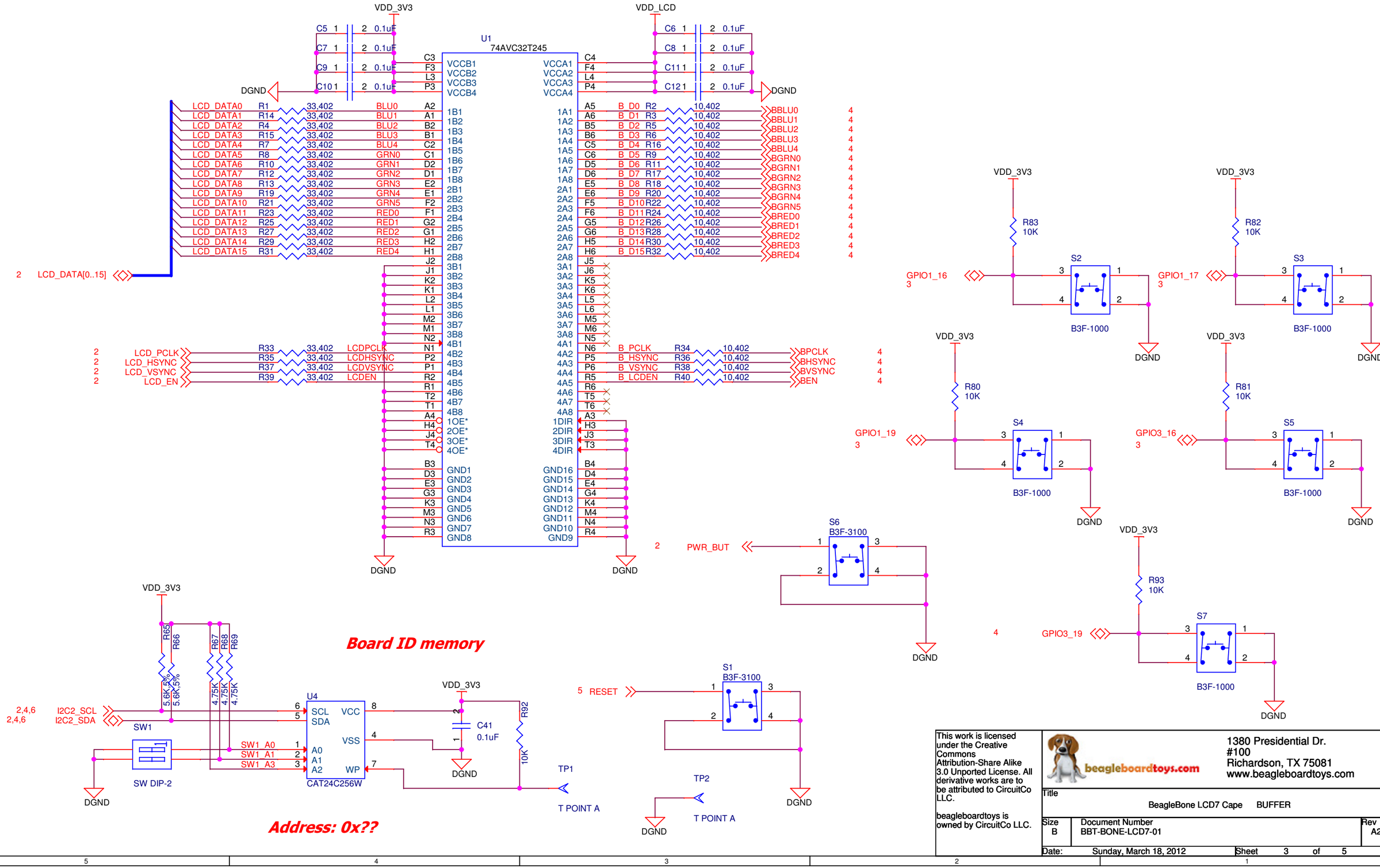
VDD_3V3
VDD_LCD
U1
74AVC32T245
C5 1 2 0.1uF
C7 1 2 0.1uF
C9 1 2 0.1uF
C10 1 2 0.1uF
C6 1 2 0.1uF
C8 1 2 0.1uF
C11 1 2 0.1uF
C12 1 2 0.1uF
DGND
LCD_DATA0 R1 33,402 BLU0
LCD_DATA1 R14 33,402 BLU1
LCD_DATA2 R4 33,402 BLU2
LCD_DATA3 R15 33,402 BLU3
LCD_DATA4 R7 33,402 BLU4
LCD_DATA5 R8 33,402 GRN0
LCD_DATA6 R10 33,402 GRN1
LCD_DATA7 R12 33,402 GRN2
LCD_DATA8 R13 33,402 GRN3
LCD_DATA9 R19 33,402 GRN4
LCD_DATA10 R21 33,402 GRN5
LCD_DATA11 R23 33,402 RED0
LCD_DATA12 R25 33,402 RED1
LCD_DATA13 R27 33,402 RED2
LCD_DATA14 R29 33,402 RED3
LCD_DATA15 R31 33,402 RED4
2 LCD_DATA[0..15]
B_D0 R2 10,402 BBLU0
B_D1 R3 10,402 BBLU1
B_D2 R5 10,402 BBLU2
B_D3 R6 10,402 BBLU3
B_D4 R16 10,402 BBLU4
B_D5 R9 10,402 BGRN0
B_D6 R11 10,402 BGRN1
B_D7 R17 10,402 BGRN2
B_D8 R18 10,402 BGRN3
B_D9 R20 10,402 BGRN4
B_D10 R22 10,402 BGRN5
B_D11 R24 10,402 BRED0
B_D12 R26 10,402 BRED1
B_D13 R28 10,402 BRED2
B_D14 R30 10,402 BRED3
B_D15 R32 10,402 BRED4
4
LCD_PCLK R33 33,402 LCDPCLK
LCD_HSYNC R35 33,402 LCDHSYNC
LCD_VSYNC R37 33,402 LCDVSYNC
LCD_EN R39 33,402 LCDEN
2
B_PCLK R34 10,402 BPCLK
B_HSYNC R36 10,402 BHSYNC
B_VSYNC R38 10,402 BVSYNC
B_LCDEN R40 10,402 BEN
GND1
GND2
GND3
GND4
GND5
GND6
GND7
GND8
GND9
GND10
GND11
GND12
GND13
GND14
GND15
GND16
1OE*
2OE*
3OE*
4OE*
1DIR
2DIR
3DIR
4DIR
VCCA1
VCCA2
VCCA3
VCCA4
VCCB1
VCCB2
VCCB3
VCCB4
VDD_3V3
R83 10K
R82 10K
S2
S3
GPIO1_16
GPIO1_17
3
B3F-1000
DGND
R80 10K
R81 10K
S4
S5
GPIO1_19
GPIO3_16
S6
B3F-3100
PWR_BUT
R93 10K
S7
GPIO3_19
Board ID memory
R65 5.6K,5%
R66 5.6K,5%
R67 4.75K
R68 4.75K
R69 4.75K
2,4,6 I2C2_SCL
2,4,6 I2C2_SDA
SW1
SW DIP-2
SW1_A0
SW1_A1
SW1_A3
U4
SCL
SDA
VCC
VSS
A0
A1
A2
WP
CAT24C256W
C41 0.1uF
R92 10K
TP1
T POINT A
Address: 0x??
5 RESET
S1
B3F-3100
TP2
T POINT A
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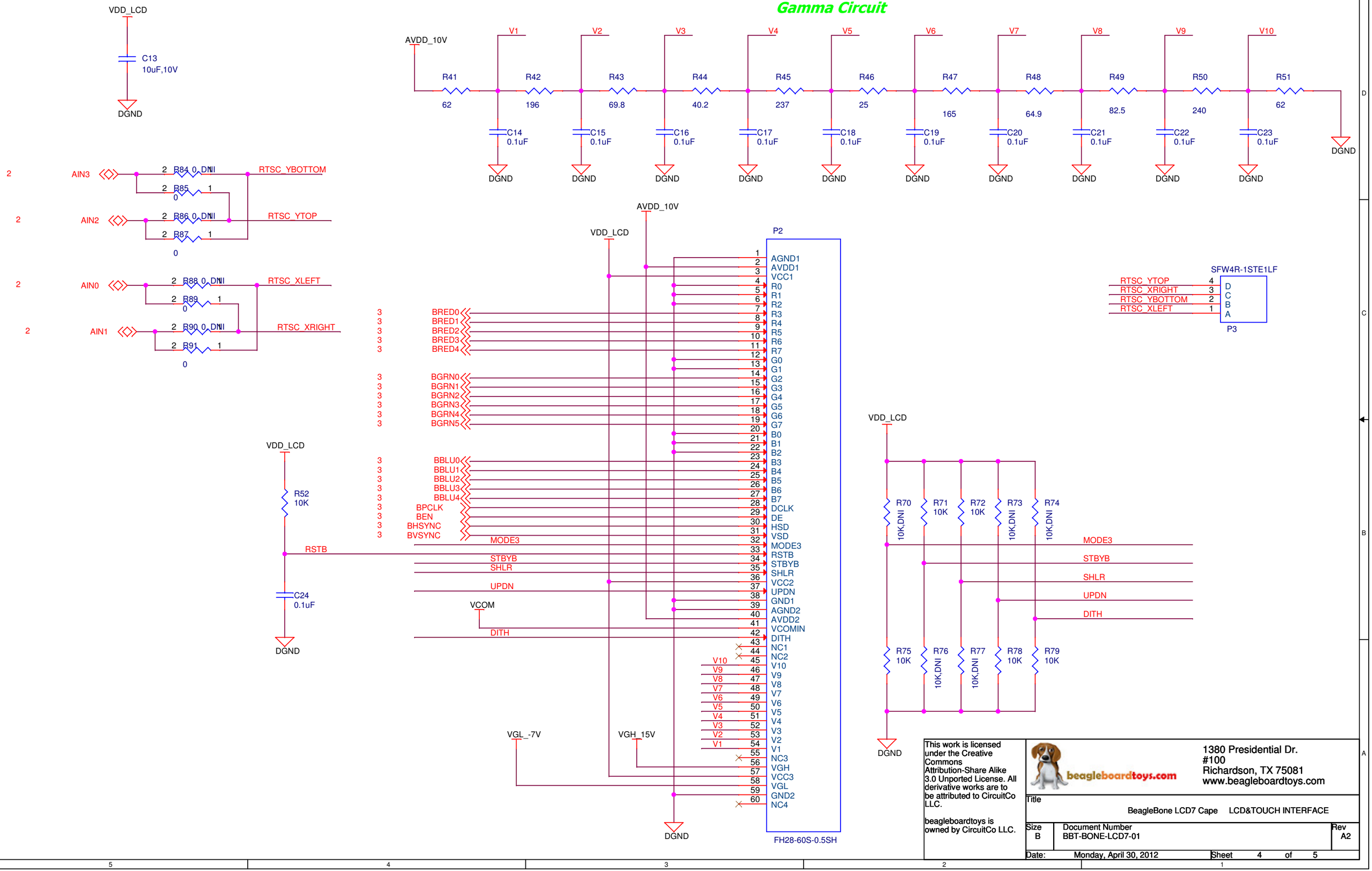
Gamma Circuit



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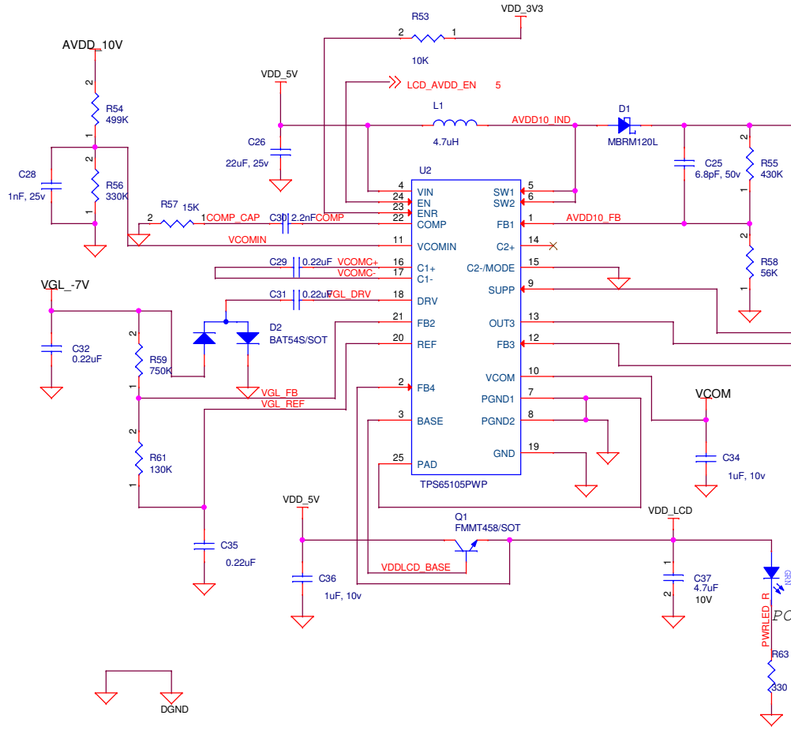

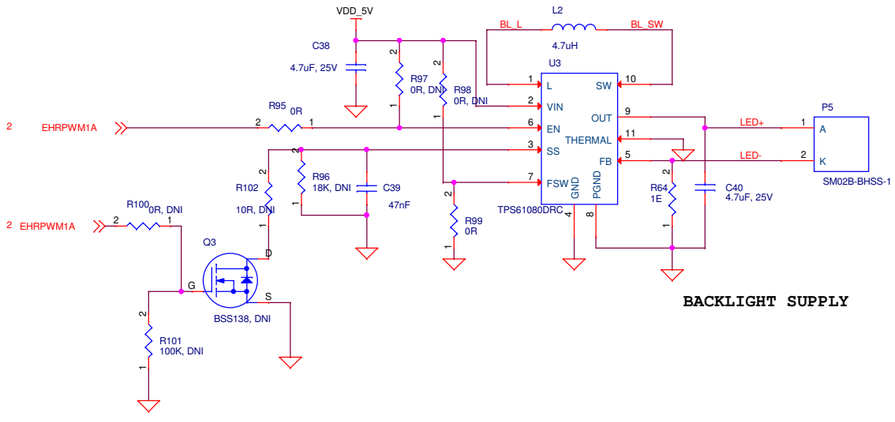

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