	REV	DATE	BY	CHANGE
	A	07/16/12	GLS	Release to Proto Fab
A	В	08/1/12	GLS	Fixed drill on mount of Mindstorms Jack and BOM for 6P pheonix
	С	08/12/12	GLS	Add JP to disconnect VM for SW debug, add screw term for Power Add 10pin battery connector as pass through header for stacking Narrow Bd by 2-3 mm on each wing to fit along side bone on LCD7 cape Swap pin 3,4 on external connectors Change GPIO used to support other capes, GPIO1_30 swapped with GPIO_27 GPIO1_30 now Motor6_Dir, GPIO0_27 now the mode 2 option for MOTOR6_PWM (pin 3 on jumper J1) Pin 3, J2 from GPIO0_3(P9_21) to GPIO1_3 (P8_6) for compatibility with LCD7 A3 release Move P8_20 net to P9_27
В	A1	10/21/12	GLS	Changed 2 pos dip Switch part number and foot print change motor8 dir back to GPIO1_31 with 0 ohm for option to use GPIO3_19 A4 ULCD7 schematic uses GPIO3_19 for cap touch int. Fix name of GPIO0_27 from incorrect GPIO1_27 Changed Motor DC jack to through hole

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Use stackable headers Add power connector

Changes to board-am335x.c: Disable 1-wire bus on gpio1_3 Disable spidev

Need to swap P8_20 (which is currently used by DVI_PDn on latest version of DVI cape). We could move it to P9_27 which is the only pin available while remaining compatible with latest version sof LCD7 and DVID capes.

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