



REVISION RECORD		
REV.	DESCRIPTION:	DATE:
0.1	FIRST PROTOTYPE RELEASE	03-30-2012
0.3	ADD R50 R53, CHANGE U2 TO CAT24C256Y-GT3	04-09-2012
	CHANGE U6 7 L2 4 5 R50 53 41 42, ADD J4 C27 28, DELETE S1	05-02-2012
0.4	ADD R54-57 J10, CHANGE J8 J9, DELETE MNT2	05-09-2012
1.0	SECOND PROTOTYPE RELEASE	05-22-2012
1.1	REWIRE FOR LCD CAPES, DELETE U4 C15 C16 R34 R35	06-22-2012
2.0	REVIEW RELEASE	06-25-2012
3.0	REPLACE J4 WITH S1, ADD TP12 TP13 R34, DELETE JMP1 2	06-28-2012
3.1	NP R26, ADD J6 J11, CONNECT R17 TO J8 PIN 17, CHANGE U2	08-28-2012
3.2	DELETE R54-R57, REWIRE J9 PINS 21 22, DISCONT J10 PINS 3 4	09-05-2012
A	SECOND PROTOTYPE RELEASE	09-19-2012

NOTES:
UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE 0402 SIZE SMT 5%, VALUES IN OHMS
2. ALL NON-POLARIZED CAPACITORS 0402 SIZE CERAMIC CHIP.

PCB1
BARE PRINTED CIRCUIT BOARD
750-00430

DRAWN BY: MIKE HENNIG	DATE: 02-06-2012	TITLE: SCHEMATIC	
CHECKED BY: TIM MONTGOMERY	DATE: 09-18-2012	PROJECT: BEAGLEBONE TIMBLE ADAPTER	
APPROVED BY: RYAN ERICKSON	DATE: 09-18-2012	SIZE: B	DRAWING NO: 332-00430
FILENAME: 332-00430_A.sch		SCALE:	REV: A
		SHEET: 01	1

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