

REV	DATE	BY	CHANGE
A	06/19/2013	DJA	Initial release.
A1	09/24/2013	DJA	CHANGES AS PER TDK ANTENNA GROUP AND TESTING in the LAB. 1.) SWAPPING J1 and J8 (match original BB CAPE REF DES EXP HDR) 2.) SWAPPING J12 and J9 (match original BB CAPE REF DES EXP HDR) 3.) Changing ANT1 MF components - C1=OPEN, C2=2.2pF 4.) Changing ANT2 MF components - C23=OPEN, C24=2.2pF. 5.) DELETE J4,J5 - NOT NEEDED. 6.) SWAP GPIO1_5 with GPIO1_29 so that Buffer Enable is GPIO1_29. 7.) Add Zero OHM resistors and DNI 1K PD resistors on UART4_CTS and UART4_RTSN lines and Swapping input pins on 3.3V Side of U2 Buffer. 8.) DELETE TP2,TP3,TP5,TP6. 9.) DELETE J3 and Connect Directly to DNI R1 10.) DELETE R47,R48,R50. 11.) CHANGE TP1/TP4 to UNCONNECTED 0 OHM RESISTOR. 12.) DNI R2 and R3. 13.) CHANGE R66 and R67 to Current SENSE Resistors. 14.) MMC Detection FAIL - Change R45,R46 to 150K 15.) WL_EN/IRQ route to pins (Pin14 GPIO0_26 driving WL_EN & Pin17 GPIO0_27 driving WL_IRQ) Through BUFFER U1 16) SDIO_DAT3 route to pins (Pin 6 GPIO1_3 driving SDIO_DAT3 through Buffer U2. 17.) REMOVE PD on BT_HCI_RTS (R44). 18.) CHANGE R19 PU from 1K0 to 10K. 19.) DELETE R44 RESISTOR. 20.) PCB FIX - ADJUST M1 PCB FOOTPRINT TO BETTER MATCH MODULE SPEC and COM BOARD REF DESIGN. 21.) ASSY FIX - ADJUST SOLDER APERATURE GERBERS on M1-(G1-G36) to be smaller in area. to match SIGNAL PIN SIZE.

UNLESS NOTED OTHERWISE, the PASSIVE COMPONENTS WILL HAVE THE FOLLOWING STANDARDS:

RESISTORS - 0402, 5%, 1/16W

CAPACITORS - 0402, 10%, 50V

INDUCTORS - 0402, 20%, 500mA

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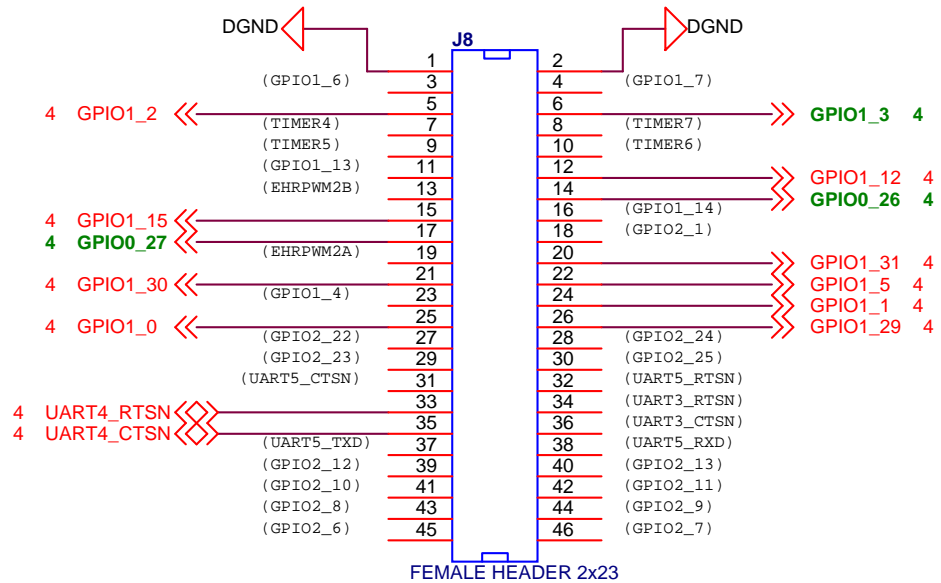


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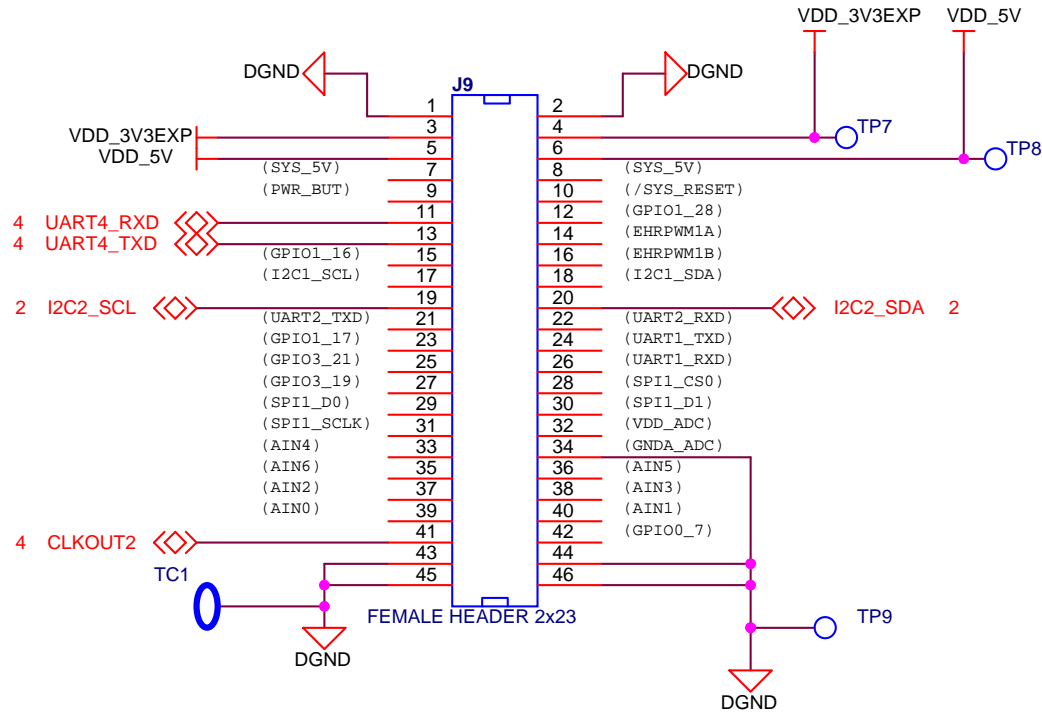
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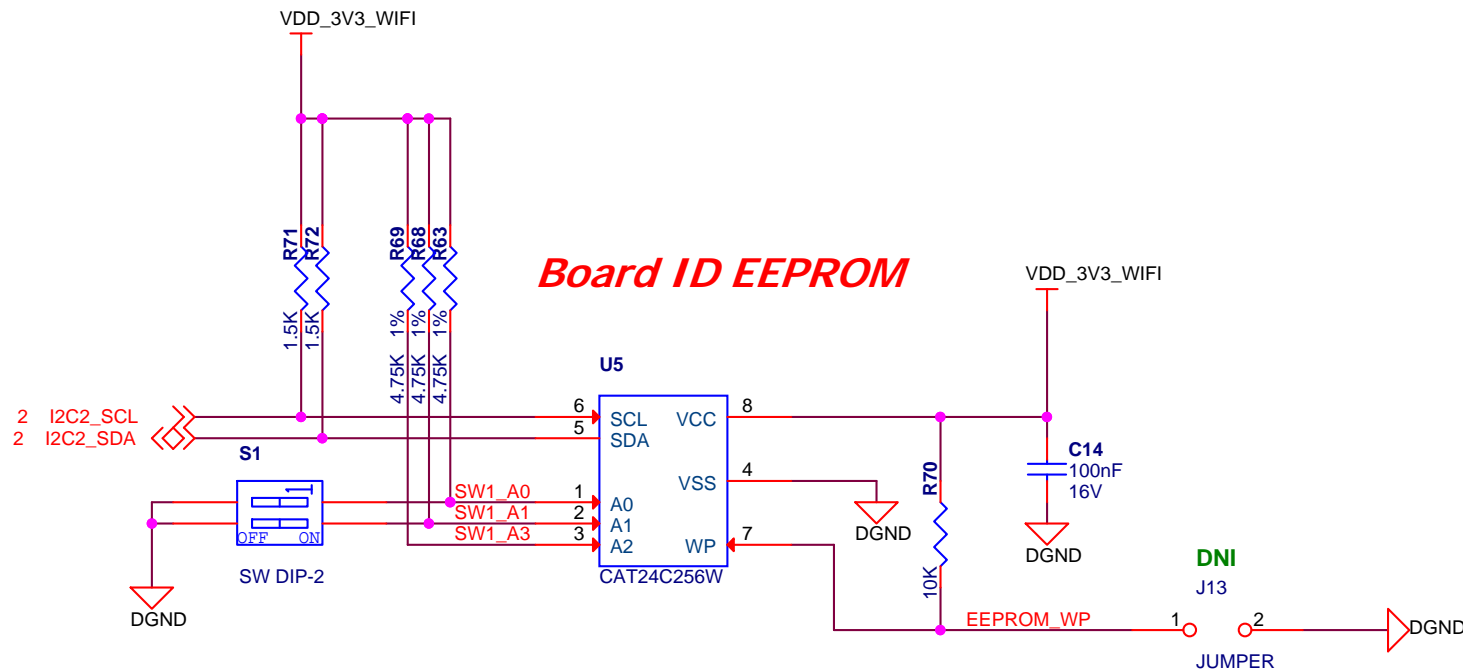
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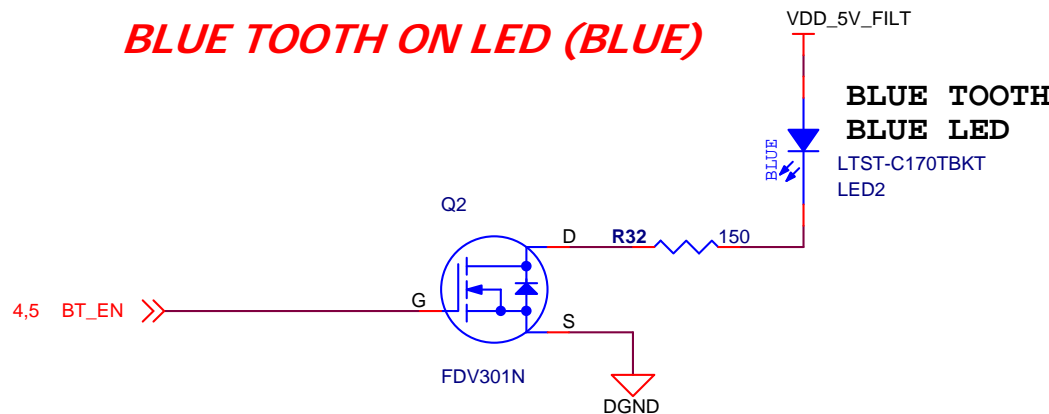
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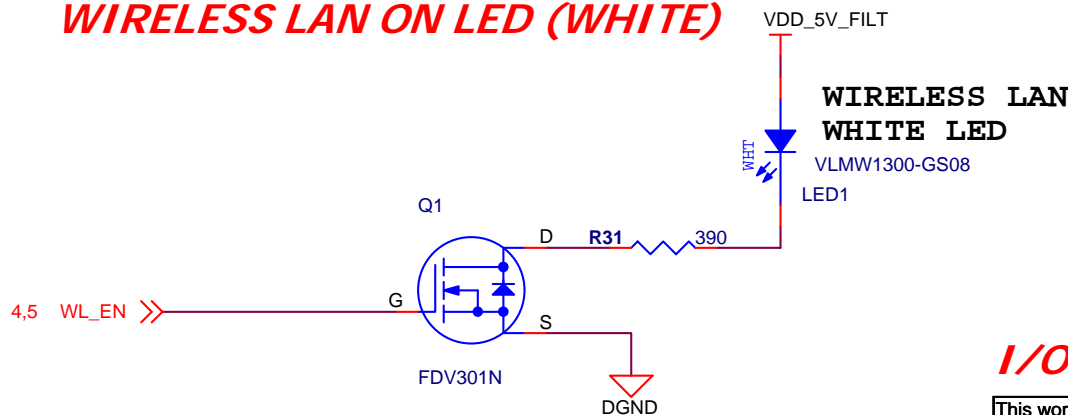
Board ID EEPROM




BLUE TOOTH ON LED (BLUE)

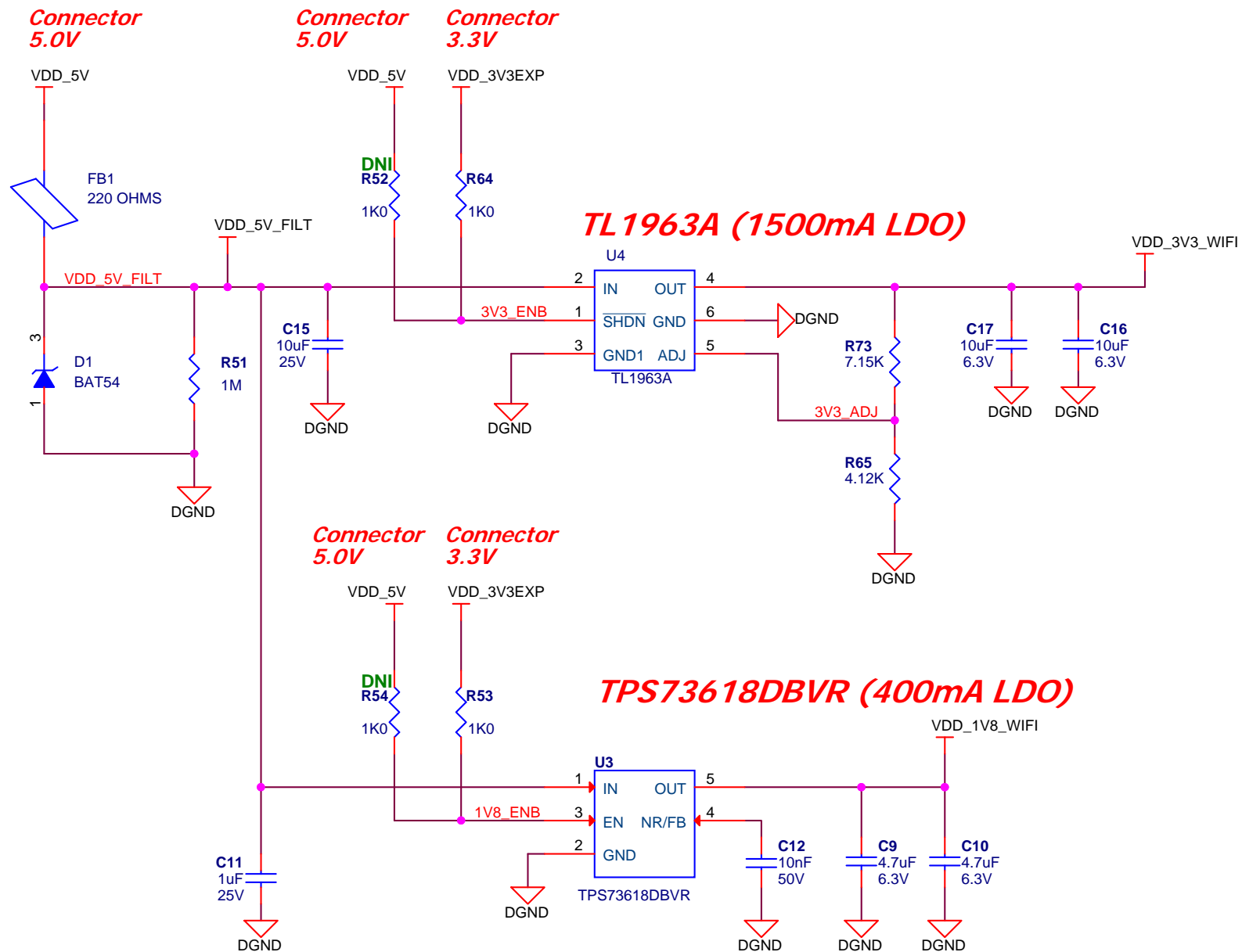


WIRELESS LAN ON LED (WHITE)




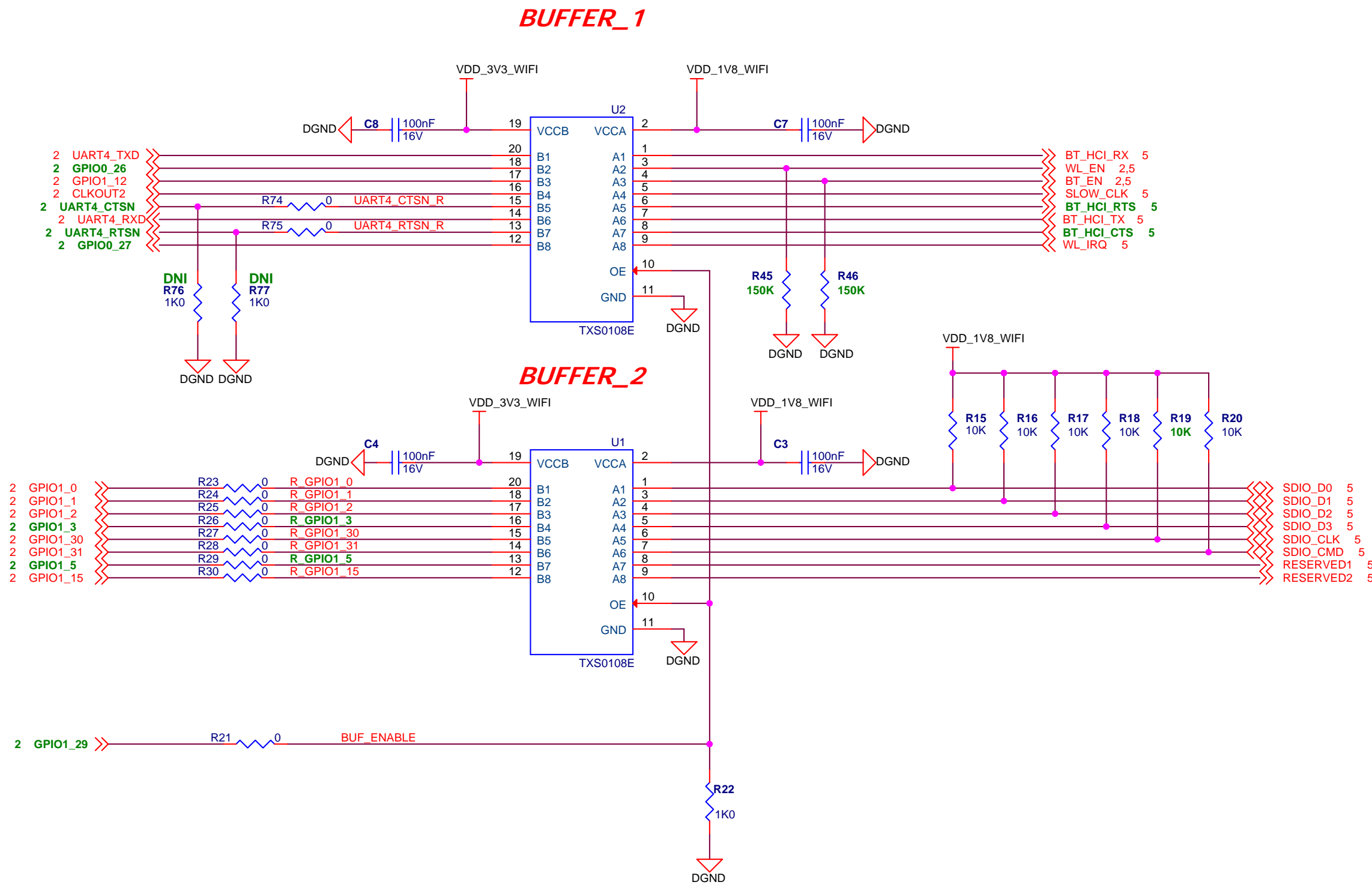
I/O CONNECTOR / ID EEPROM / LEDS

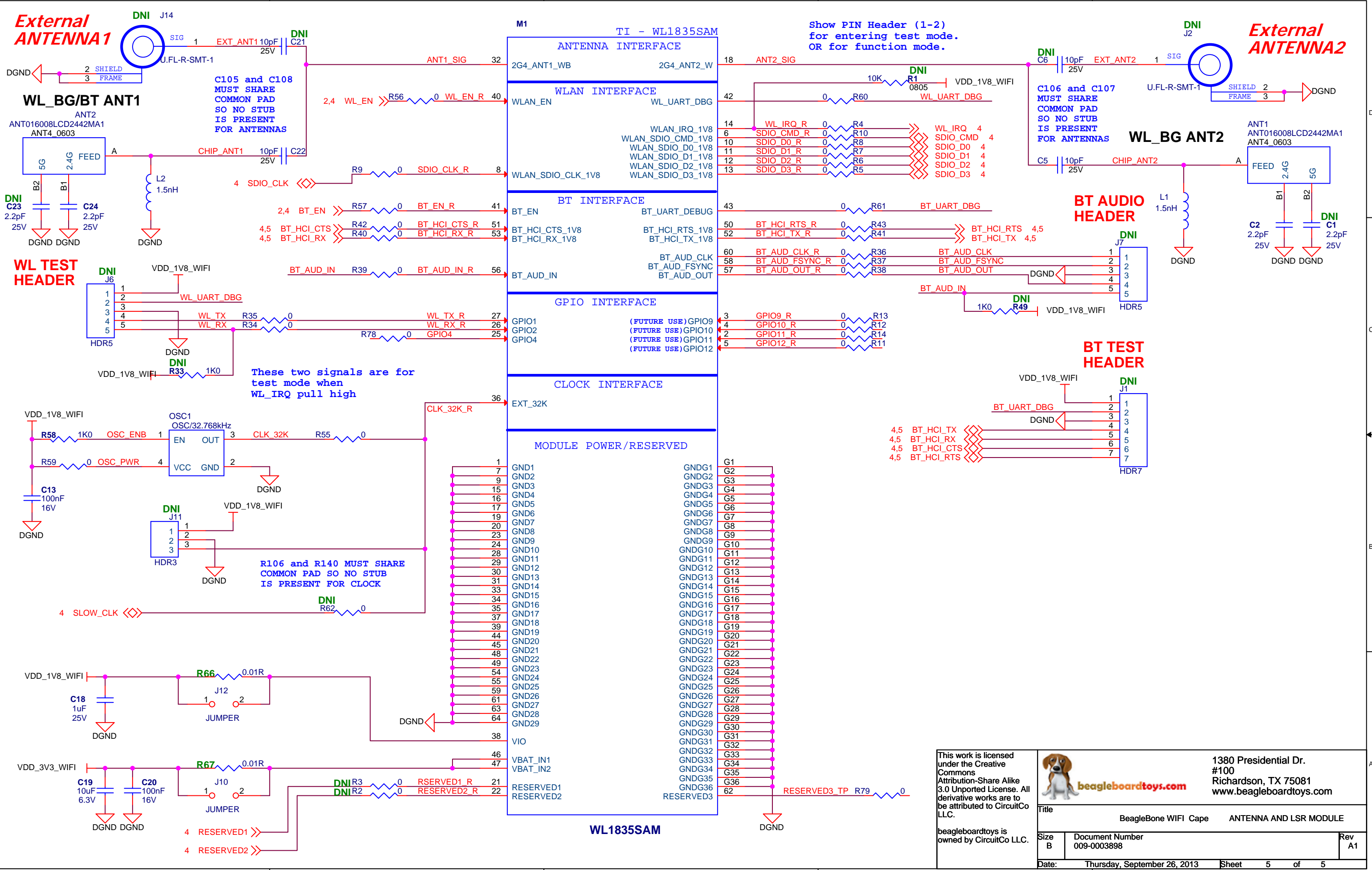
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POWER GENERATION - 3.3V and 1.8V

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