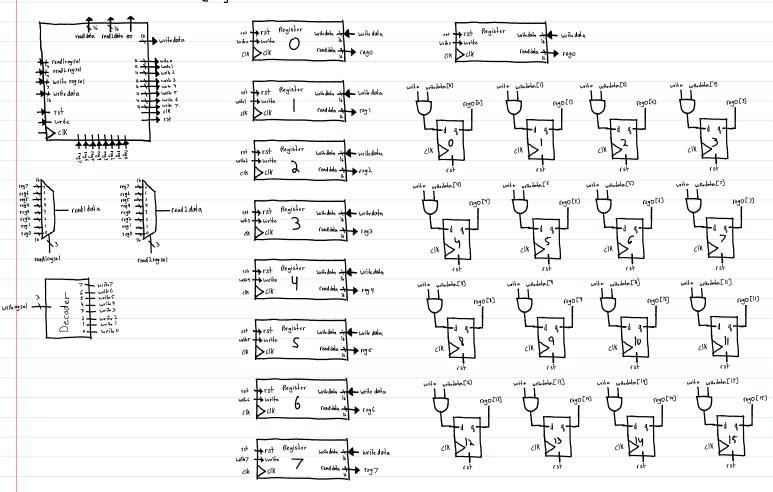
read regsel [2:0] CIK read regsel [2:0]

Write reg sel [2:0] write





## Problem 2

Inputs: clock cst\_rst

