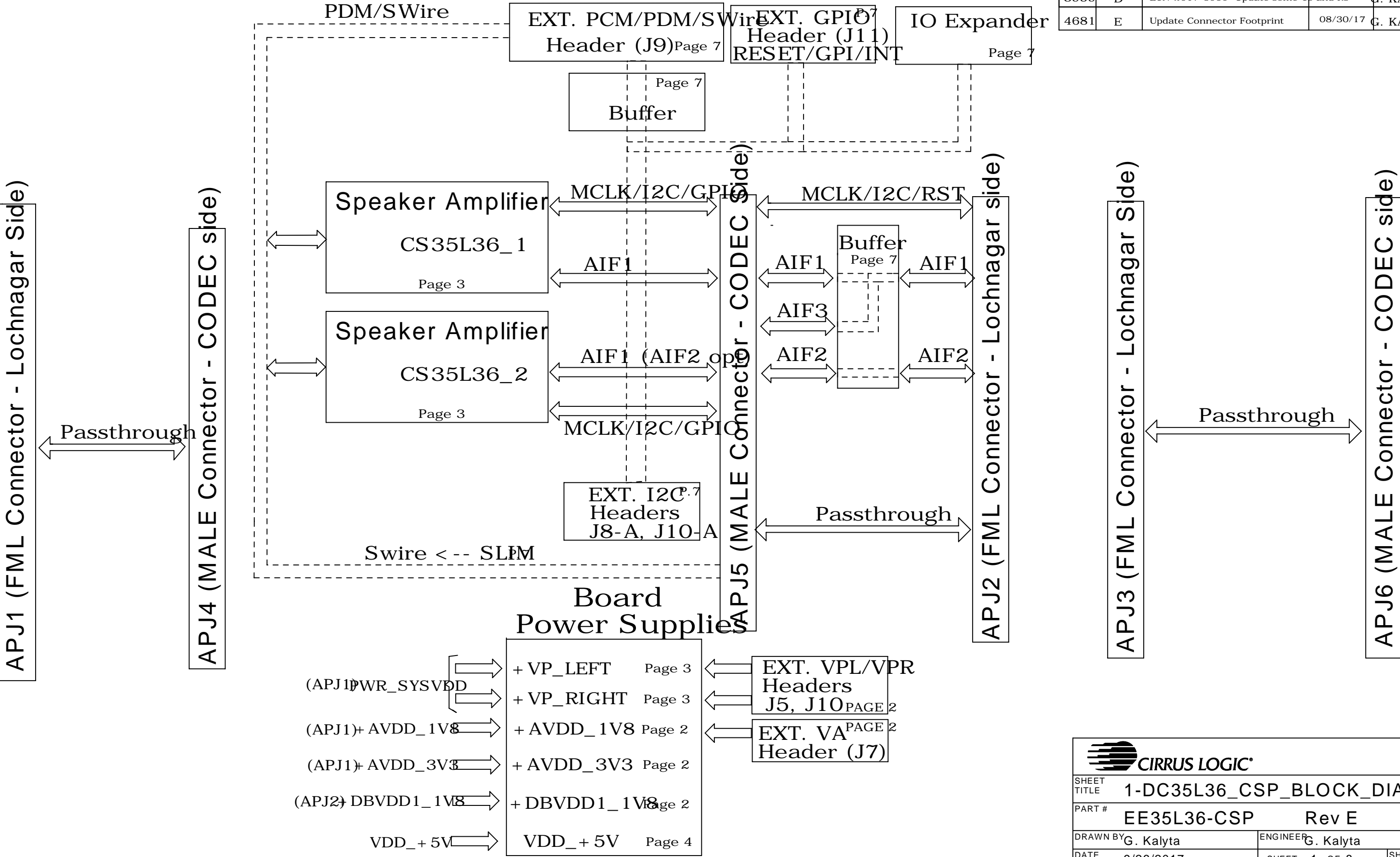
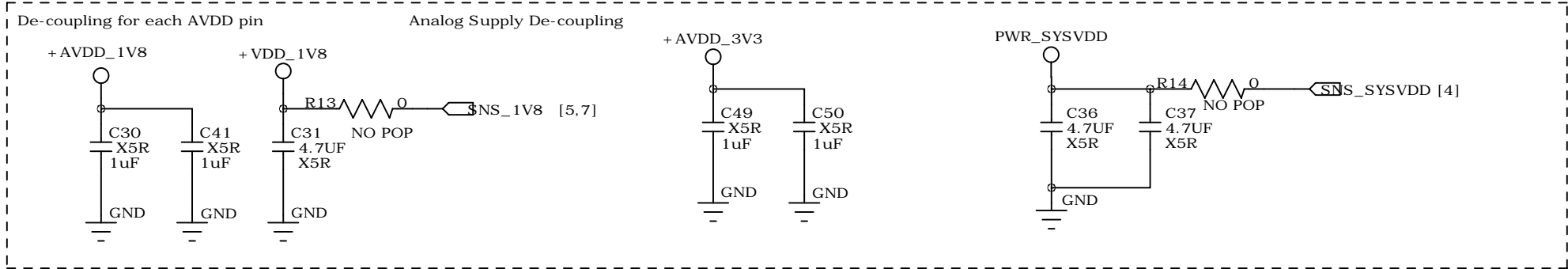
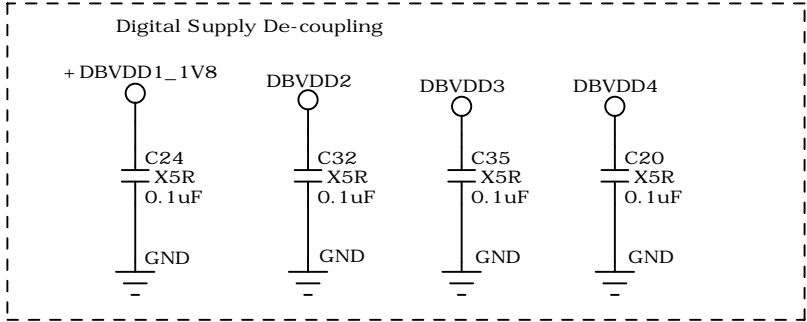


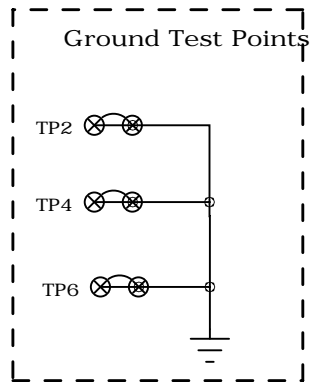
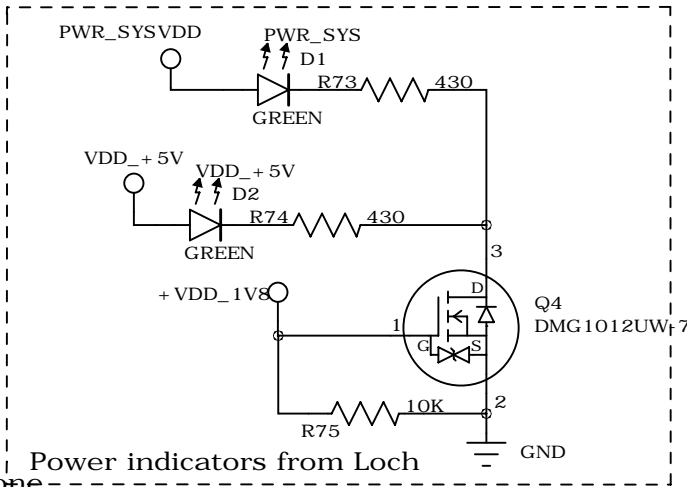
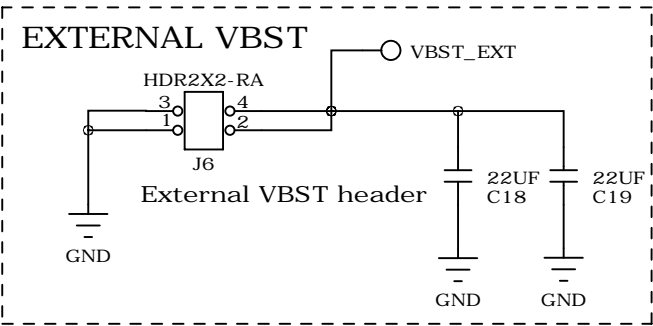
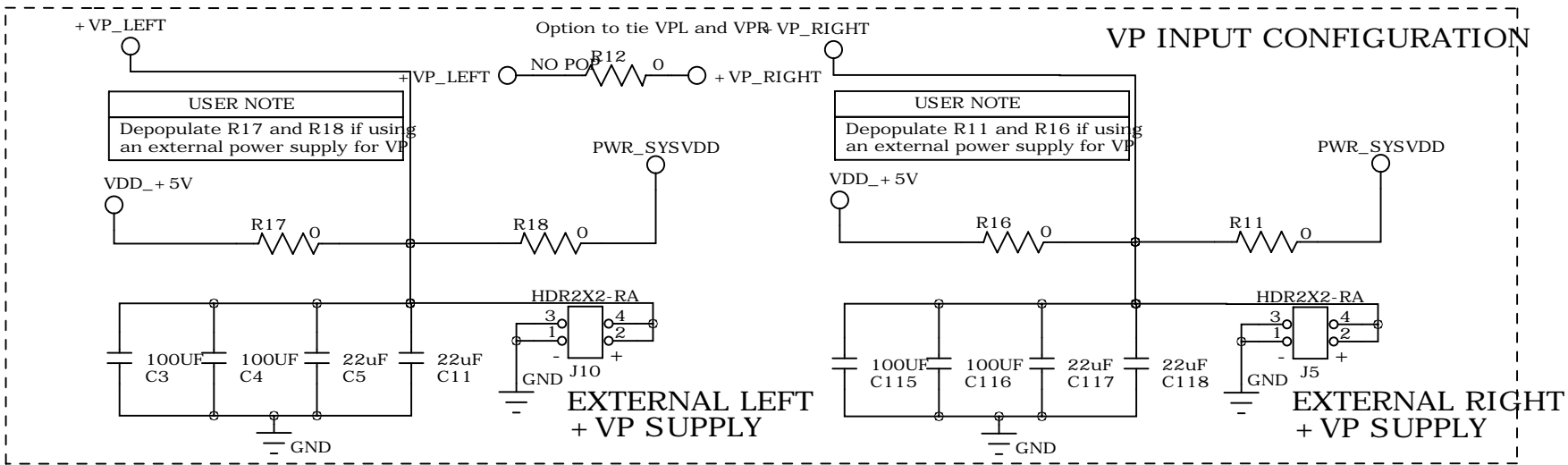
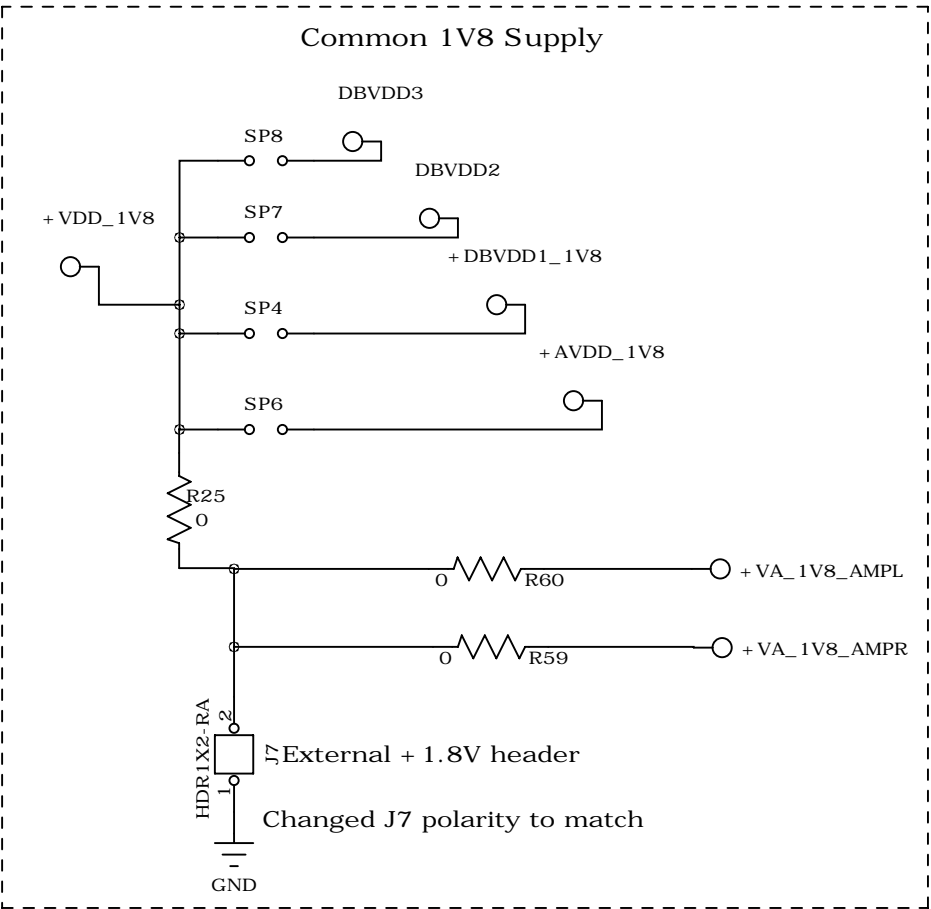
DC35L36-CSP Board Block Diagram

| ECO # | REVISION | DESCRIPTION OF CHANGE | DATE | ENGINEER |
|-------|----------|-------------------------------------|----------|-----------|
| | A | INITIAL DESIGN | 10/21/16 | G. KALYTA |
| 3218 | B | Update some Cs and Rs | 02/13/17 | G. KALYTA |
| 3776 | C | ECN-2017-3776 Update some Cs and Rs | 05/01/17 | G. KALYTA |
| 3959 | D | ECN-2017-3959 Update some Cs and Rs | 05/23/17 | G. KALYTA |
| 4681 | E | Update Connector Footprint | 08/30/17 | G. KALYTA |





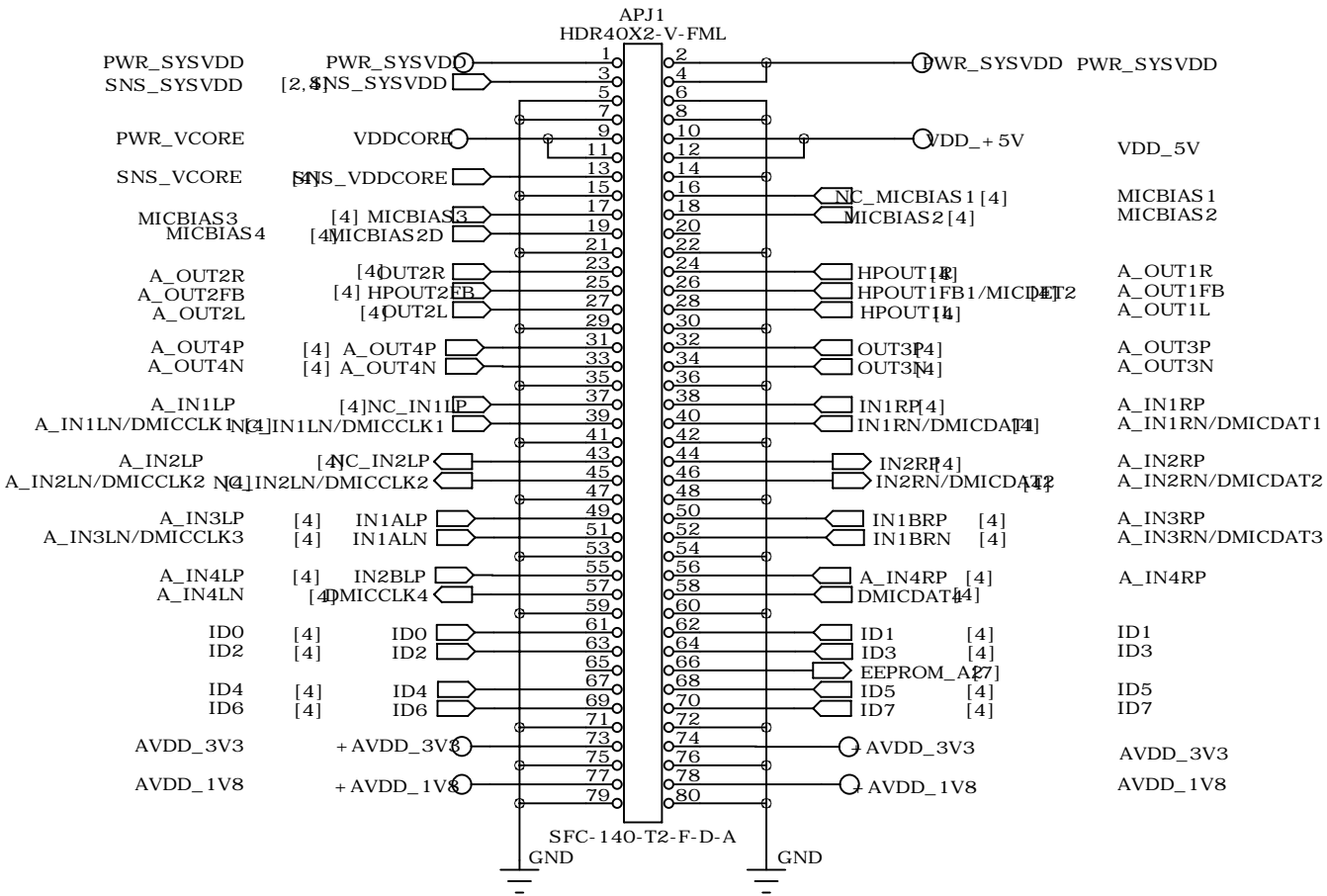
NOTE: VDD_+ 5V and PWR_SYSVDD are shorted if R17, R18; R16, R11 are populated (default condition)



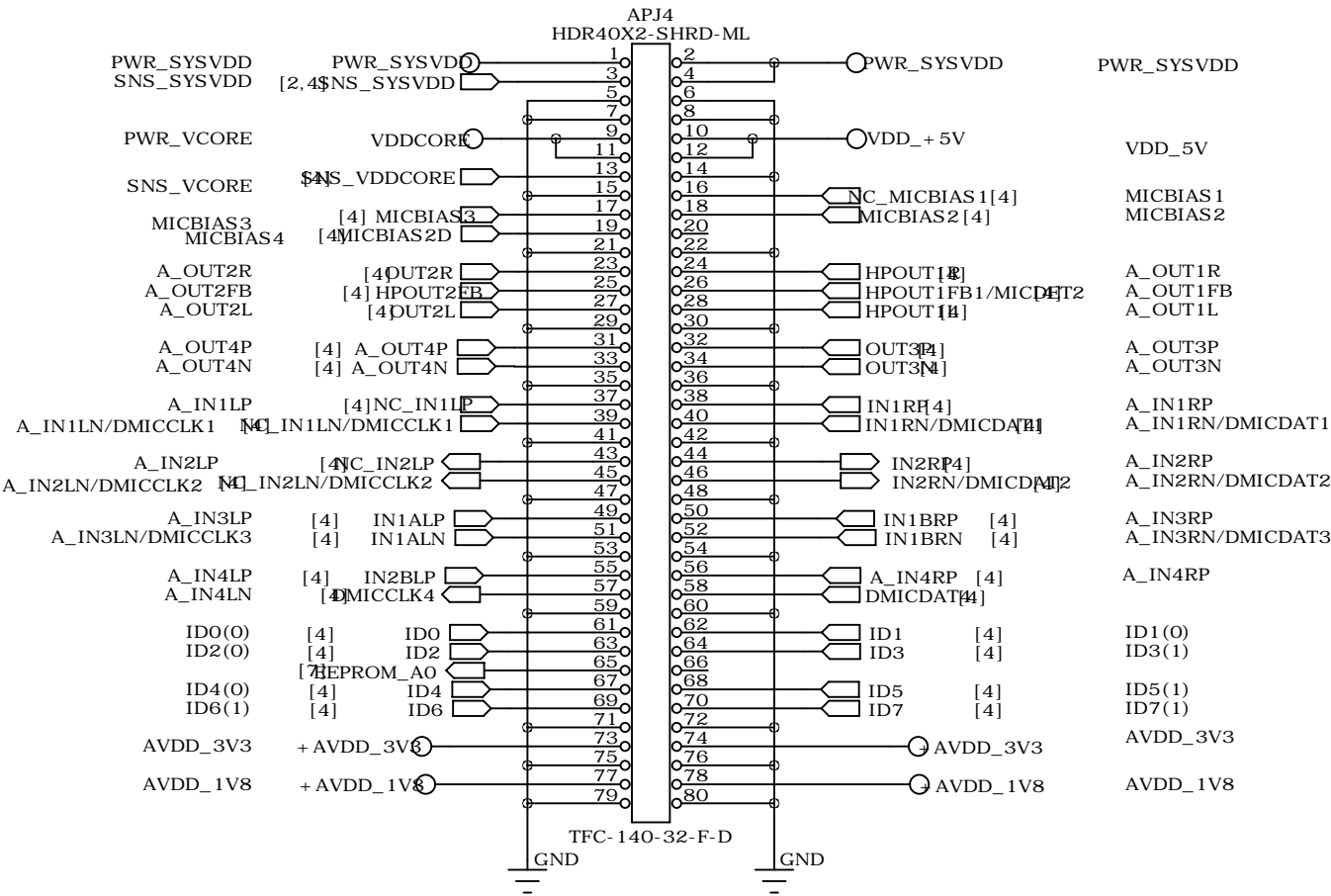
NOTES: UNLESS OTHERWISE SPECIFIED;
1. ALL RESISTOR VALUES ARE OHMS.

| | | | |
|-------------|--|-------------|--------------------|
| | | | |
| SHEET TITLE | | 2-POWER | |
| PART # | | EE35L36-CSP | Rev E |
| DRAWN BY | | G. Kalyta | ENGINEER G. Kalyta |
| DATE | | 9/26/2017 | SHEET 2 OF 8 |
| | | | SHEET SIZE B |

Female connector (Lochnagar side)



Male connector (CODEC side)



ID0...ID7 left floating to pass ID code from Codec DCard to Lochnagar



CIRRUS LOGIC®

SHEET
TITLE 4-CONN1

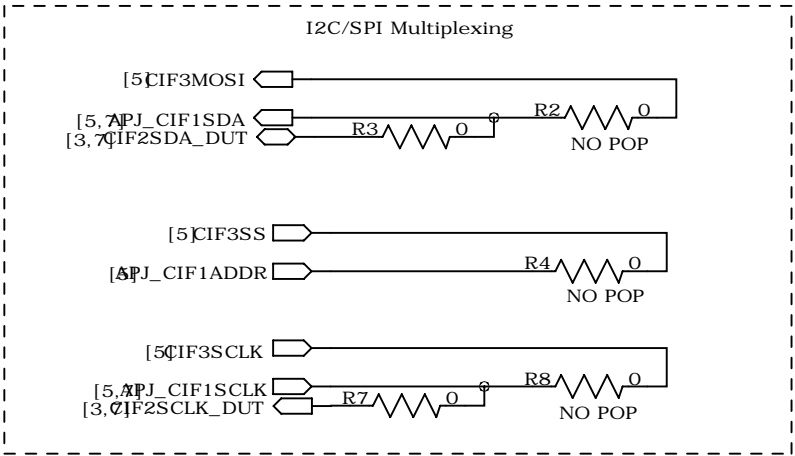
PART # EE35L36-CSP Rev E

DRAWN BY G. Kalyta ENGINEER G. Kalyta

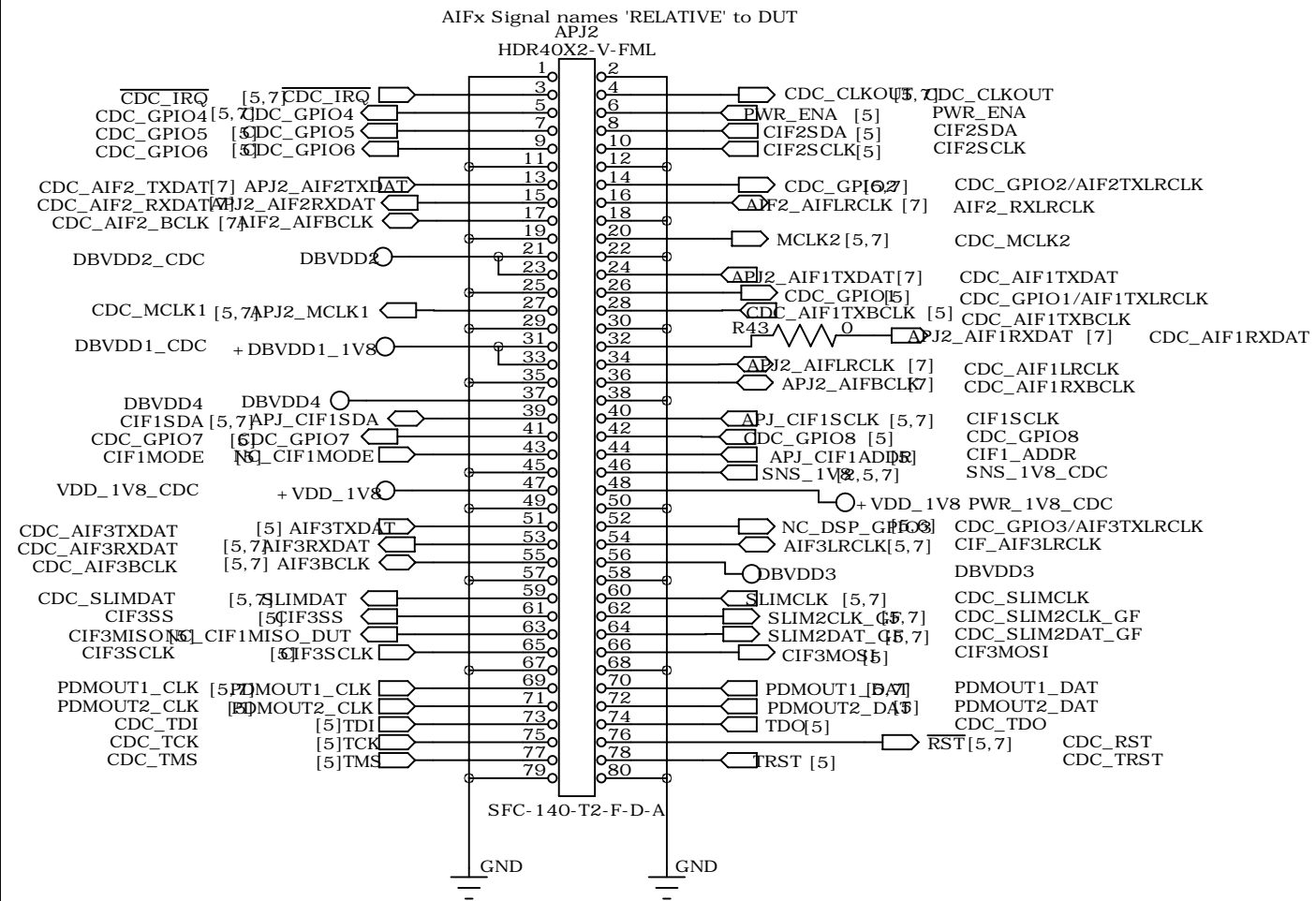
DATE 9/26/2017

SHEET 4 OF 8

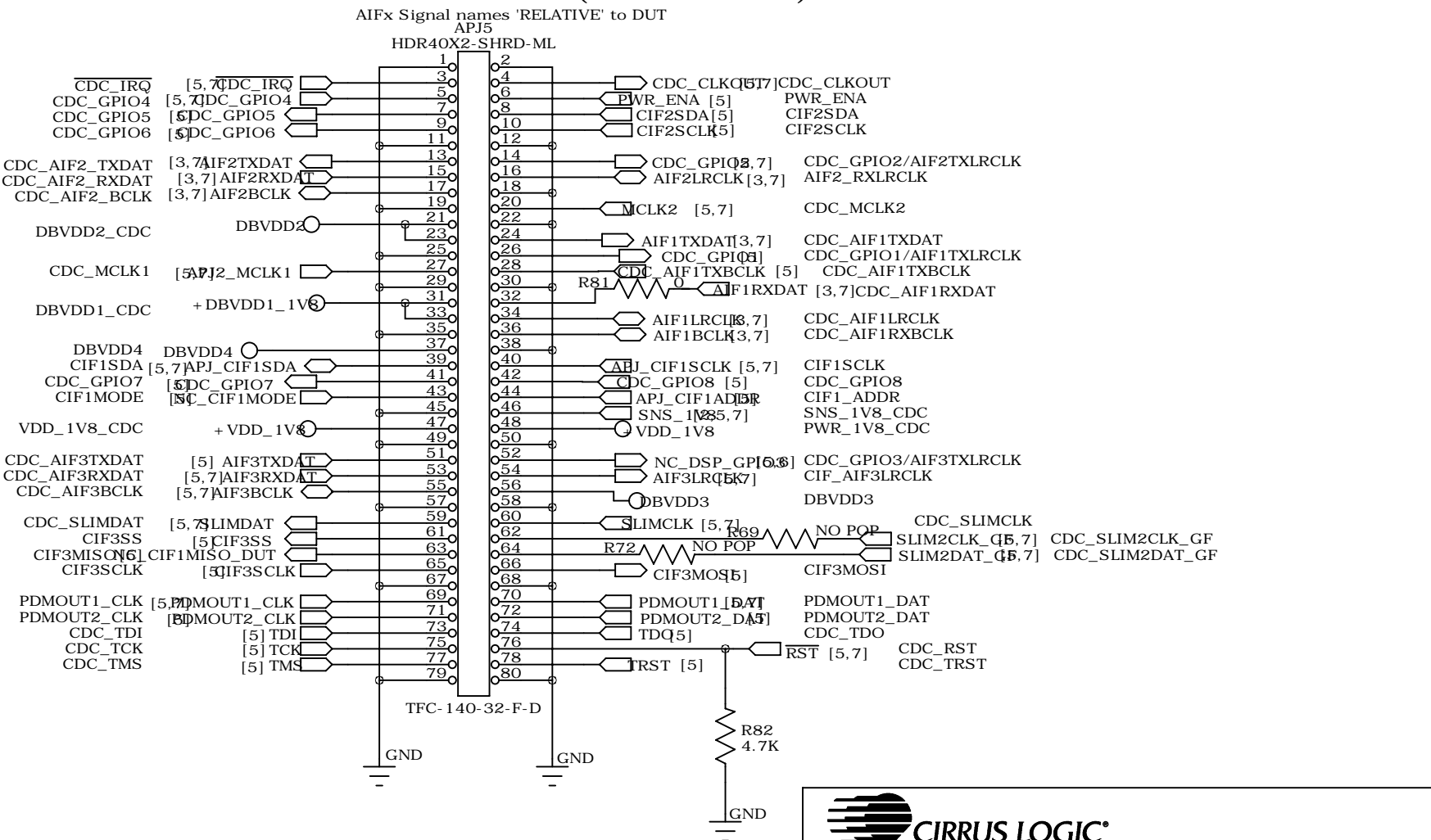
SHEET
SIZE B



Female connector (Lochnagar side)

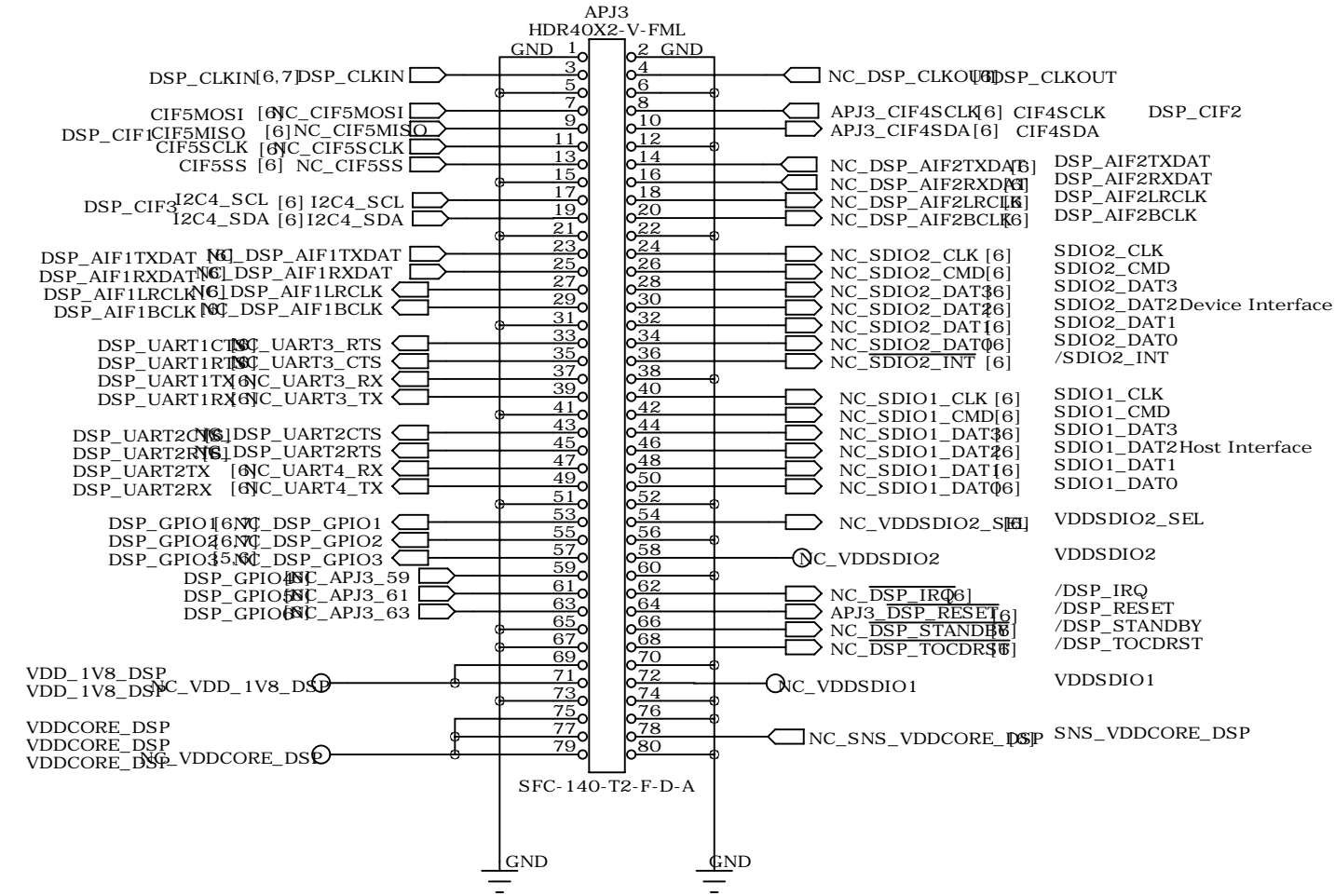


Male connector (CODEC side)

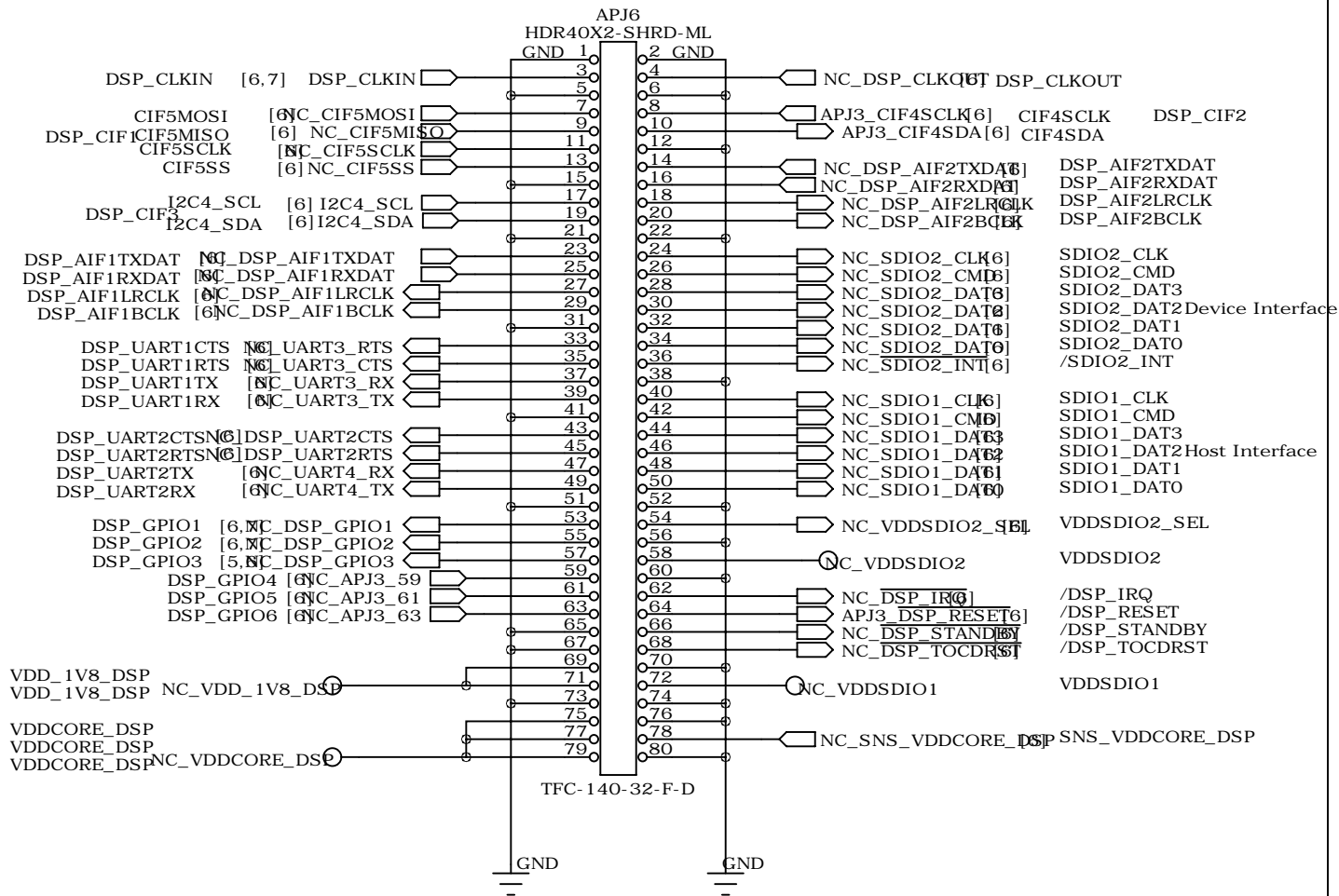


| | | | | | |
|-------------|--|--|-------------|--|--|
| SHEET TITLE | | | 5-CONN2 | | |
| PART # | | | EE35L36-CSP | | |
| DRAWN BY | | | G. Kalyta | | |
| DATE | | | 9/26/2017 | | |
| ENGINEER | | | G. Kalyta | | |
| SHEET | | | 5 OF 8 | | |
| SIZE | | | B | | |

AIFx Signal names 'RELATIVE' to DUT



AIFx Signal names 'RELATIVE' to DUT



SHEET
TITLE

6-CONN3

PART #
EE35L36-CSP

Rev E

DRAWN BY
G. Kalyta

ENGINEER
G. Kalyta

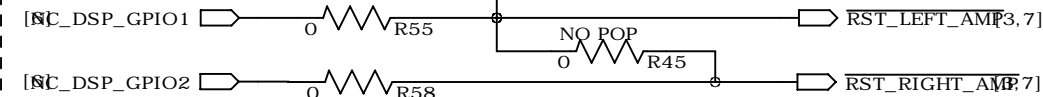
DATE
9/26/2017

SHEET
6 OF 8

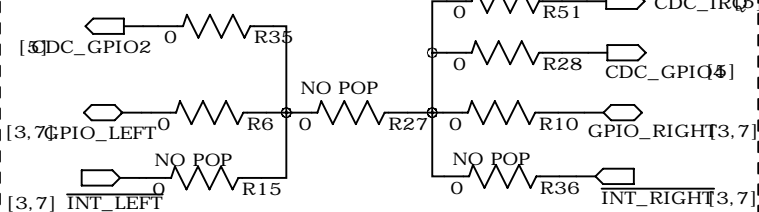
SHEET
SIZE B

AMP RESET

Resets MUST be tied together in Shared Boost mode

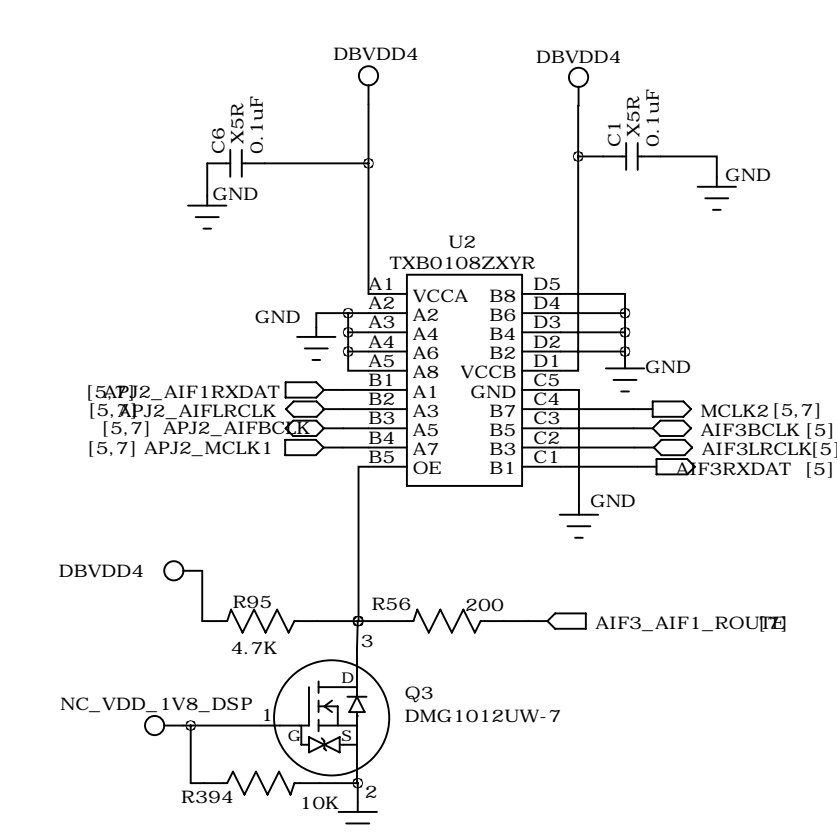
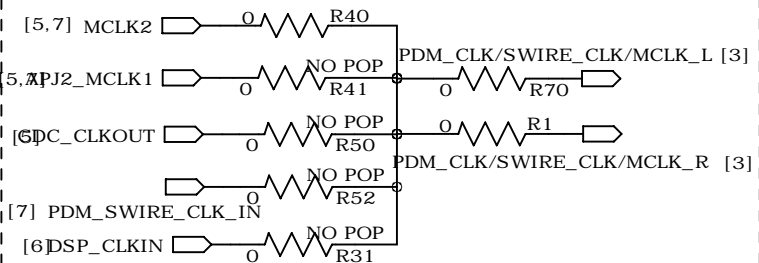


AMP INTERRUPTS

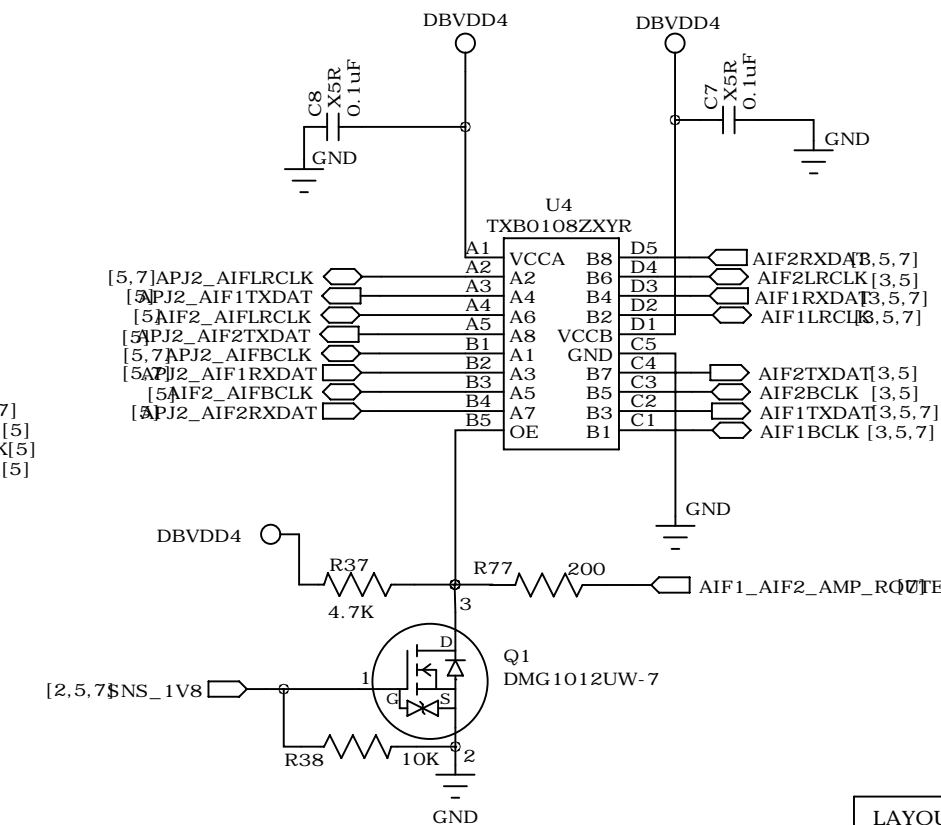
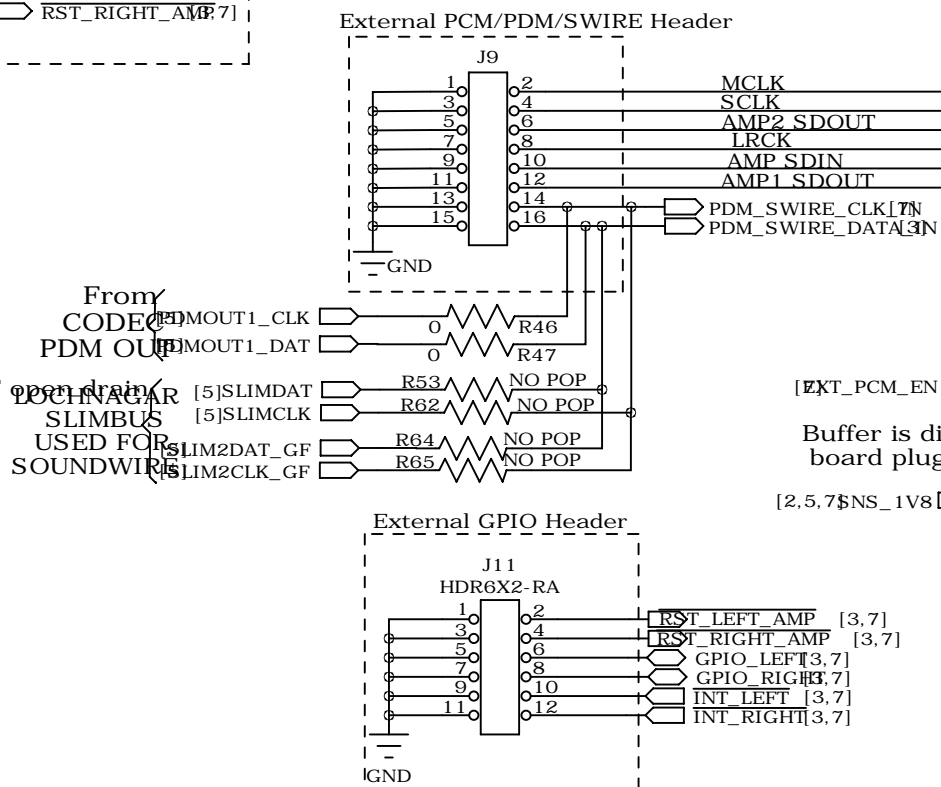


Default config: GPIO signals are used as Interrupts
DO NOT install R27, if /INT signals are used - they are NOT open drain!

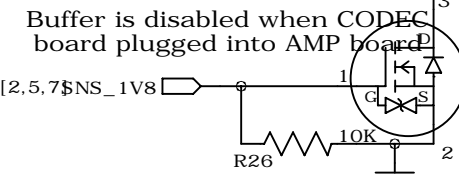
MCLK ROUTING



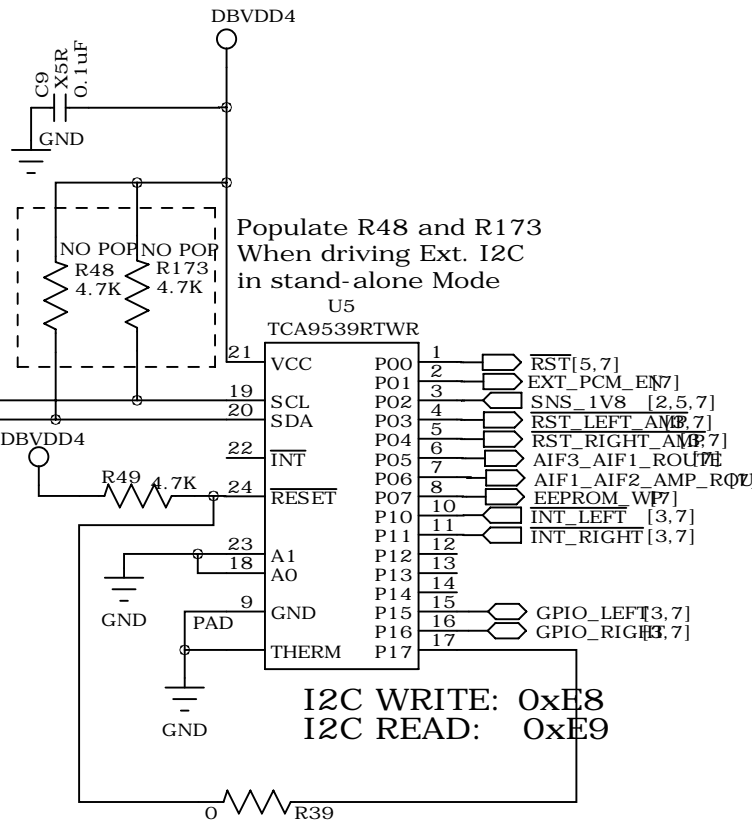
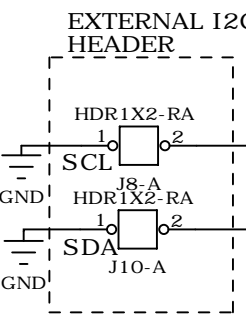
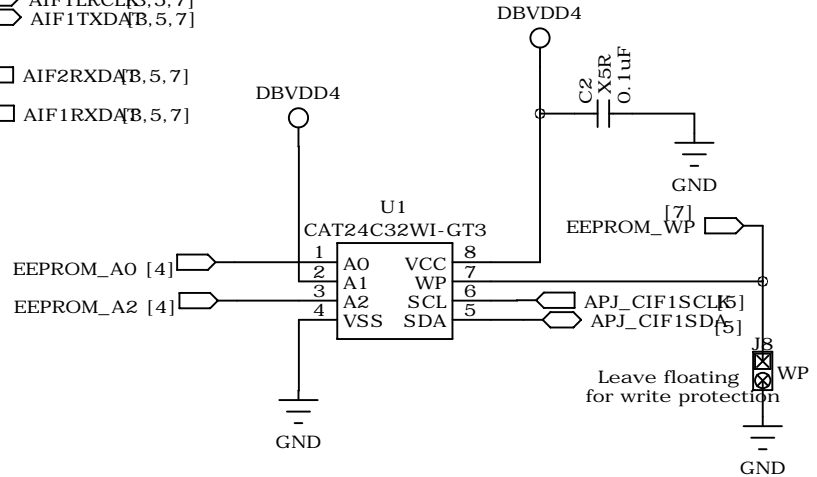
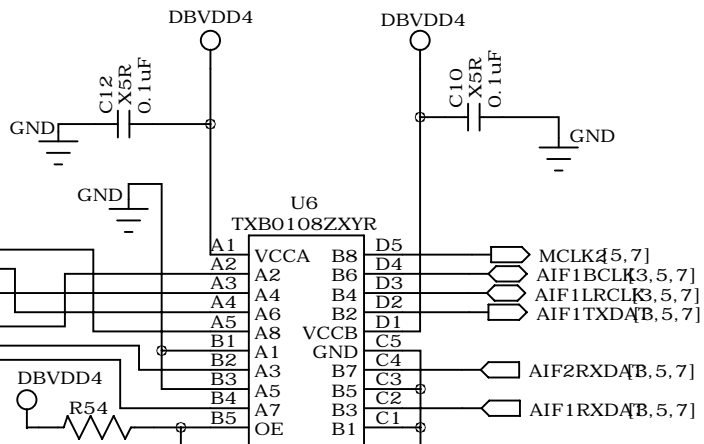
Buffer is disabled when plugged into Lochnagar board
Buffer is enabled when plugged into Reliability board
AIF1 signals routed from Rel. Board to CODEC's AIF3



Buffer is disabled when CODEC board plugged into AMP board
Buffer is enabled when CODEC board is not present
AIF1 and AIF2 signals routed from Lochnagar to AMPs



LAYOUT NOTES:
Use controlled 50ohm characteristic impedance for digital signals
Place series termination resistors close to the DUT.
Route analogue speaker outputs differentially.



Populate R48 and R173
When driving Ext. I2C
in stand-alone Mode

I2C WRITE: 0xE8
I2C READ: 0xE9



| | | | |
|------------------------|--|--------------------|--|
| SHEET TITLE 7-BUFF_I/O | | | |
| PART# EE35L36-CSP | | Rev E | |
| DRAWN BY G. Kalyta | | ENGINEER G. Kalyta | |
| DATE 9/26/2017 | | SHEET 7 OF 8 | |
| | | SHEET SIZE B | |

PCB DWG-240-10133/EO

LBL SUBASSY PROD ID AND REV

REVISION HISTORY

REV A:

- Initial release

REV B:

- R22 changed from 100k to 0 Ohm
- R66 changed from 20k to 4.99k
- R40 connection changed
- R83, R395, R396 changed from 0 Ohm 0201 to 0 Ohm 0402
- R397 changed from 22 Ohm 0201 to 22 Ohm 0402
- L3, L7 changed type and package from 2520 to 2016

REV D:


- Added R29, R79, R80, R87, C33, C34

REV C: wasn't manufactured

- Removed R79, R80, R29, R31, C33, C34
- Changed R23, R24 to 0402 NO POP
- Changed R84, R85 to 10kOhm
- Added R31, R86
- Changed C14, C227 to 4.7uF 16V X5R
- Changed C13, C16, C226, C26 to 10uF 16V X5R
- Changed R76 to 22 Ohm
- Changed R397 to NO POP

REV E:

- Removed R29, R79
- Changed C33, C34 to NO POP and 0603
- Changed R84, R85 to NO POP
- Changed U3, U7 to B0
- Changed APJ2, 4, 6 footprint

| | | | |
|---|-------------|--------------|--------------|
|  | | | |
| SHEET TITLE | 8-DOC_MECH | | |
| PART # | EE35L36-CSP | Rev E | |
| DRAWN BY | G. Kalyta | ENGINEER | G. Kalyta |
| DATE | 9/26/2017 | SHEET 8 OF 8 | SHEET SIZE B |