

Linux Kit Core

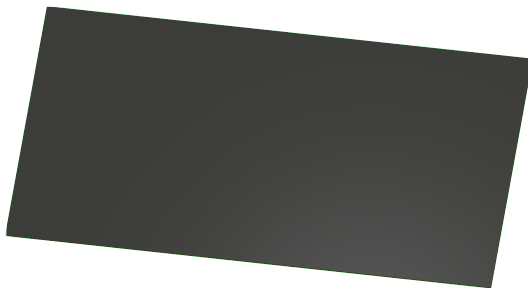
Variant: DRAFT

2025-02-21

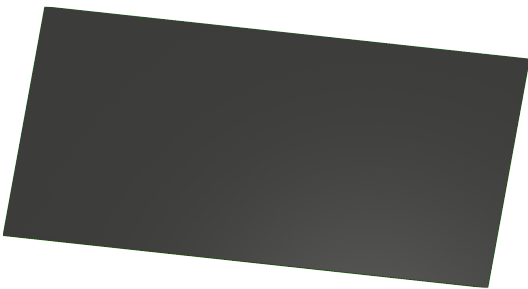
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as **X**

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 21-Feb-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

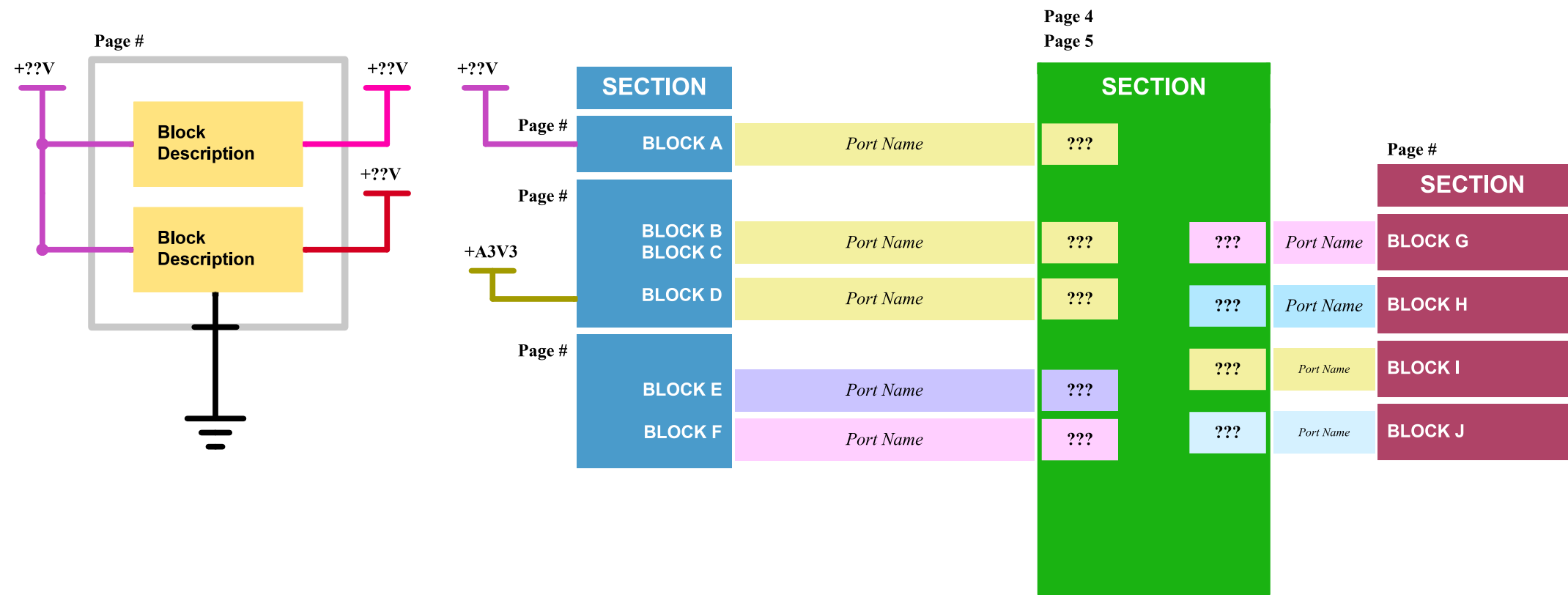
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: FoxIO Tech	Variant: DRAFT	Git Hash: f091b53
	Board Name: Linux Kit Core	Project Name: Linux Kit		
	Sheet Title:	File Name: Linux-Kit-Core-Hardware.kicad_sch	Designer: Cjio	Date: 2025-01-12
	Sheet Path: /		Revision: + (Unreleased)	
		Reviewer:	Size: A3	Sheet: 1 of 7

[2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company: FoxIO Tech		Variant: DRAFT	Git Hash: f091b53
		Board Name: Linux Kit Core		Project Name: Linux Kit	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Cjiio	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Block Diagram/		Reviewer:	Size: A3	Sheet: 2 of 7

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F	1	2	3	4	5	6	7	8

Section A – Title A

Page 4

File: Section A - Title A.kicad_sch

Section B – Title B

Page 5

File: Section B - Title B.kicad_sch

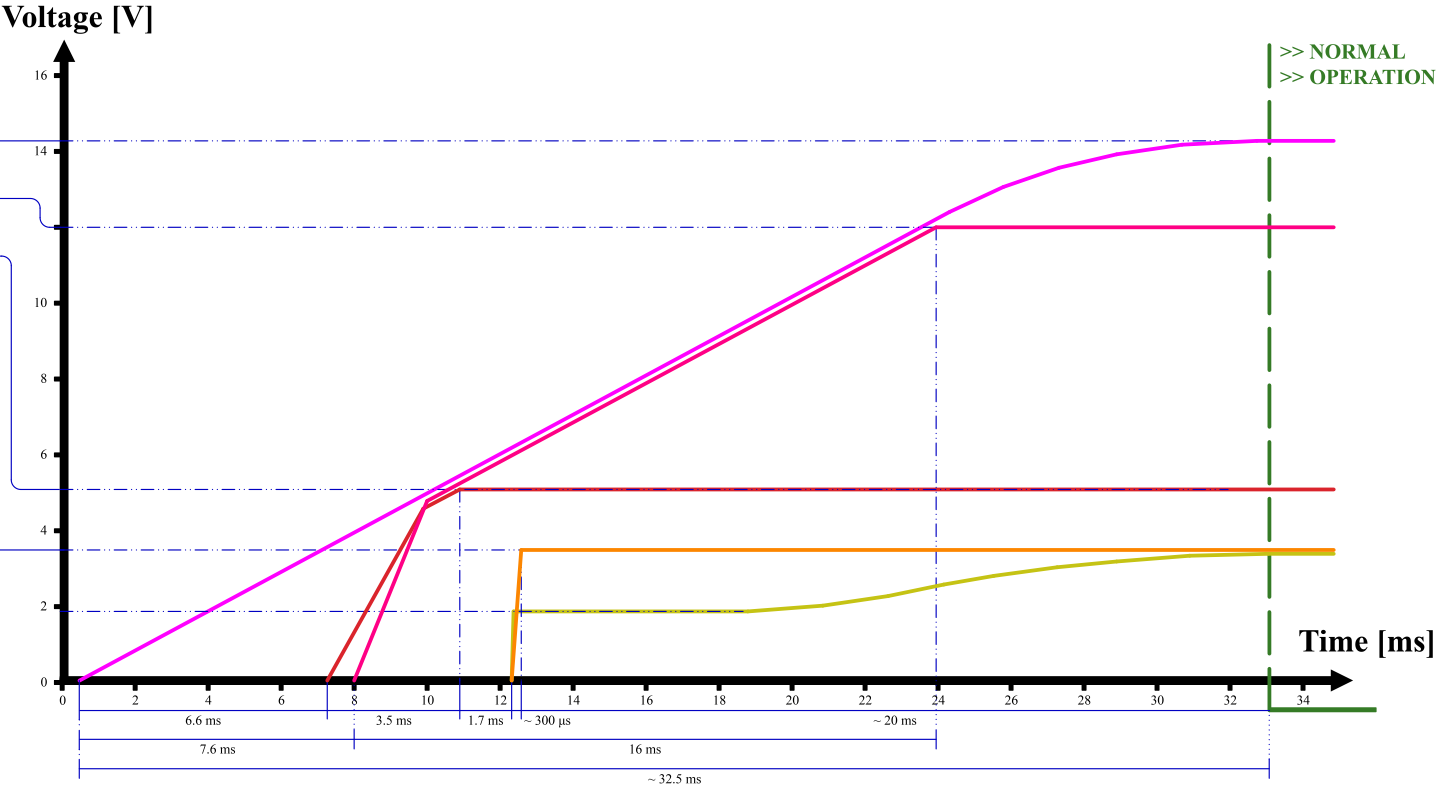
	Comments:	Company: FoxIO Tech		Variant: DRAFT	Git Hash: f091b53
		Board Name: Linux Kit Core		Project Name: Linux Kit	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Cjiio	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3	Sheet: 3 of 7

1	2	3	4	5	6
A	[4] Sheet Title A				
B	<div></div> <div>LAYOUT NOTE: blablabla</div> <div>DESIGN NOTE: blablabla</div> <div></div> <div>Block Description</div>				
C					
D					
			Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation		Company: FoxIO Tech
			Board Name: Linux Kit Core		Variant: DRAFT
			Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Project Name: Linux Kit
			Sheet Path: /Project Architecture/Section A - Title A/	Designer: Cjiiio	Git Hash: f091b53
				Date: 2025-01-12	Revision: + (Unreleased)
				Size: A4	Sheet: 4 of 7
1	2	3	4	5	6

[illegible]

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company: FoxIO Tech		Variant: DRAFT	Git Hash: f091b53
		Board Name: Linux Kit Core		Project Name: Linux Kit	
	Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Cjio	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Power - Sequencing/		Reviewer:	Size: A4	Sheet: 6 of 7

[7] Revision History

	Comments:	Company: FoxIO Tech		Variant: DRAFT	Git Hash: f091b53
		Board Name: Linux Kit Core		Project Name: Linux Kit	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Cjiiio	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7