



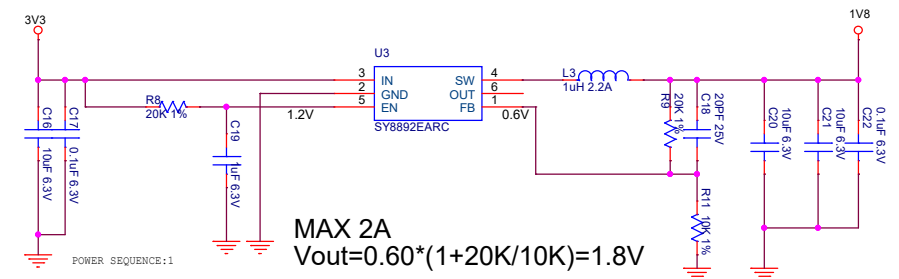
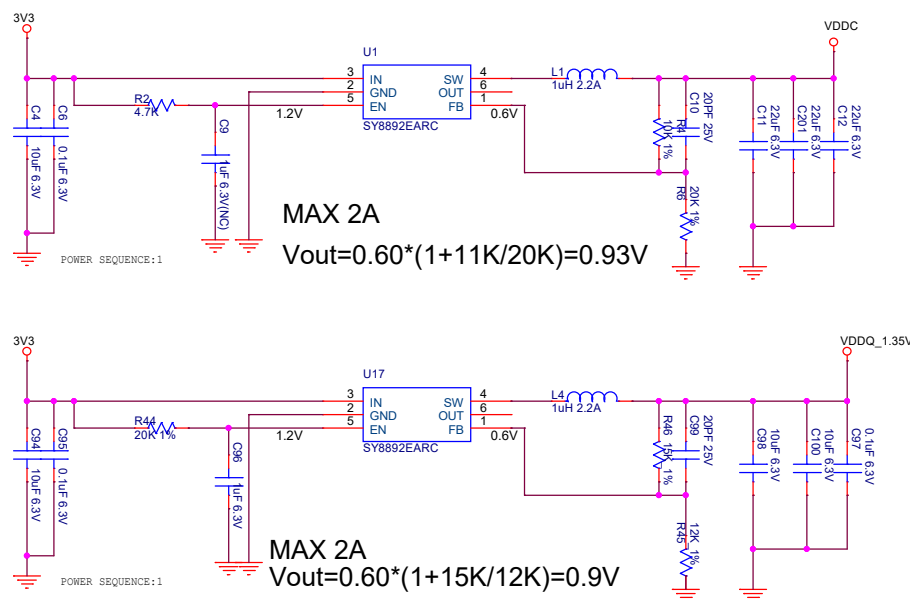
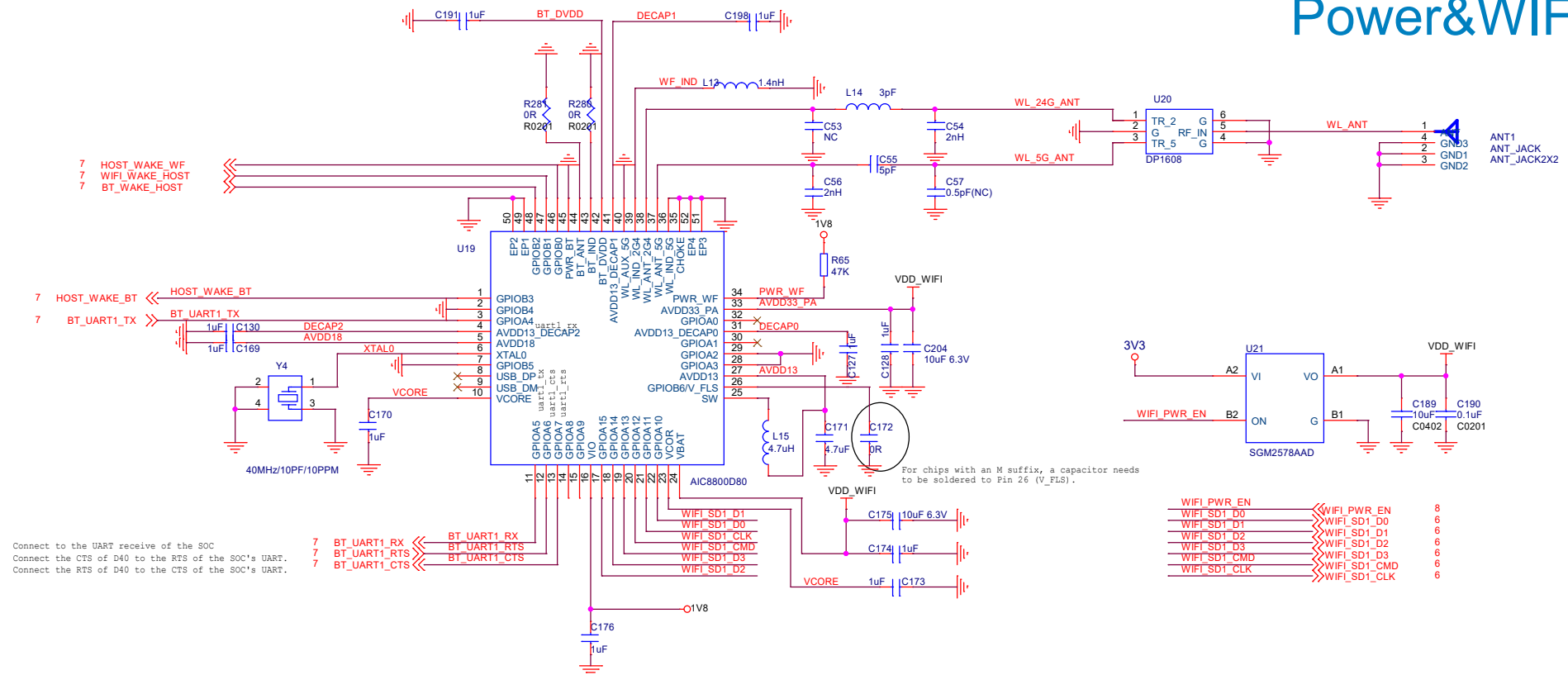
Milk-V Duo Module schematic

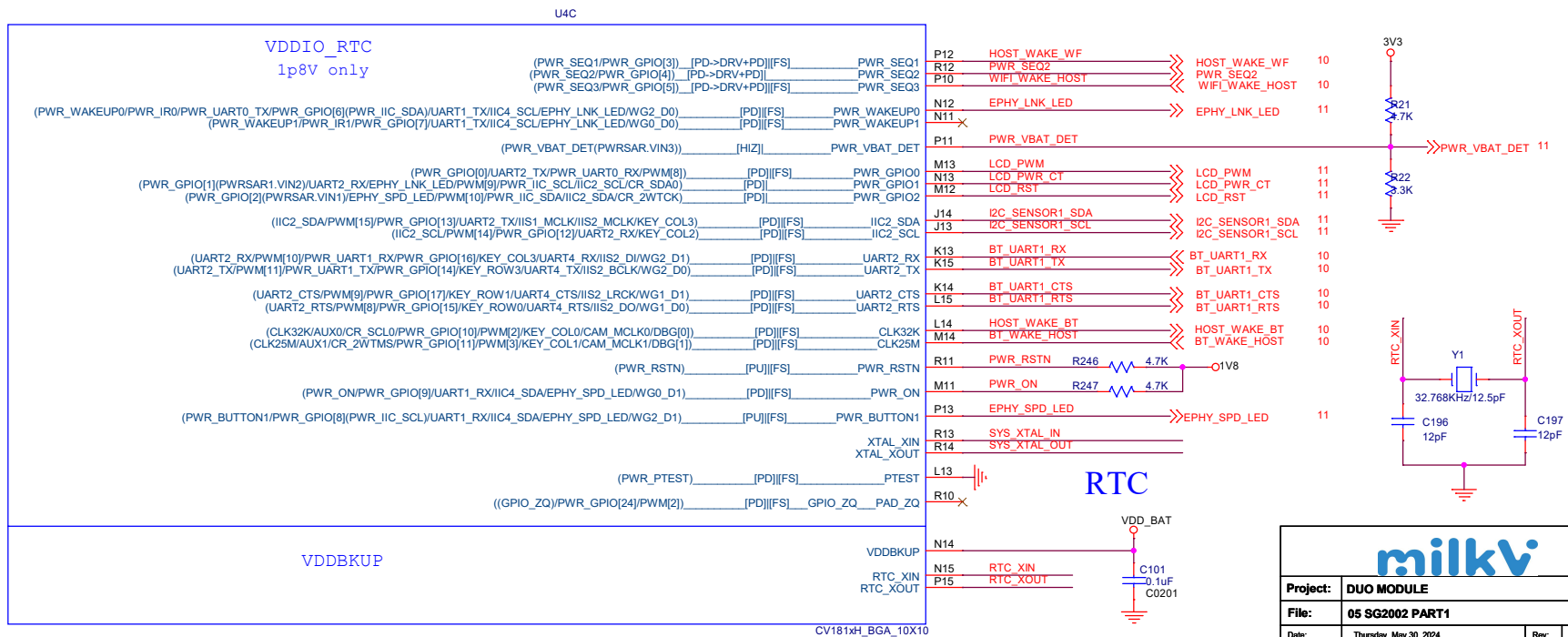
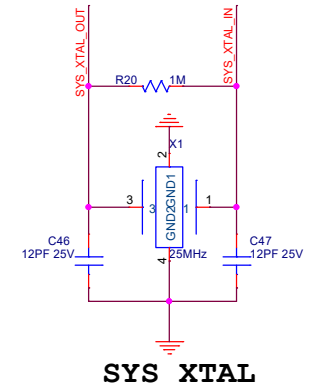
V1.1

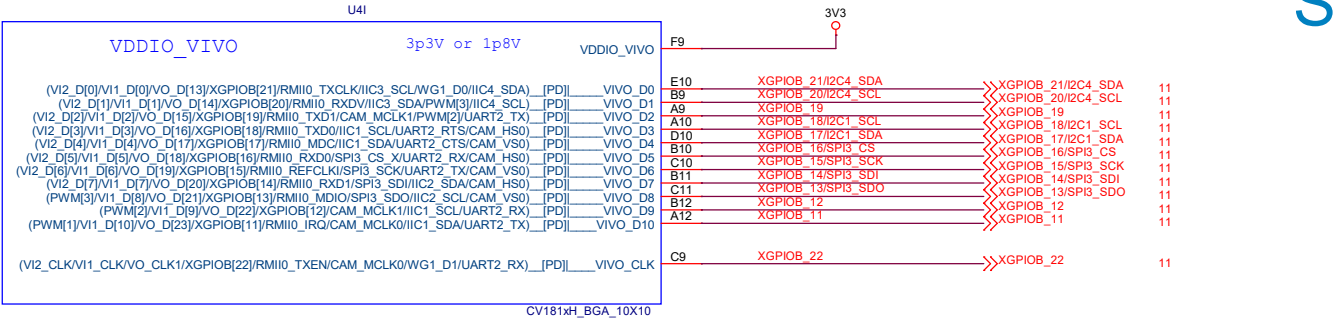
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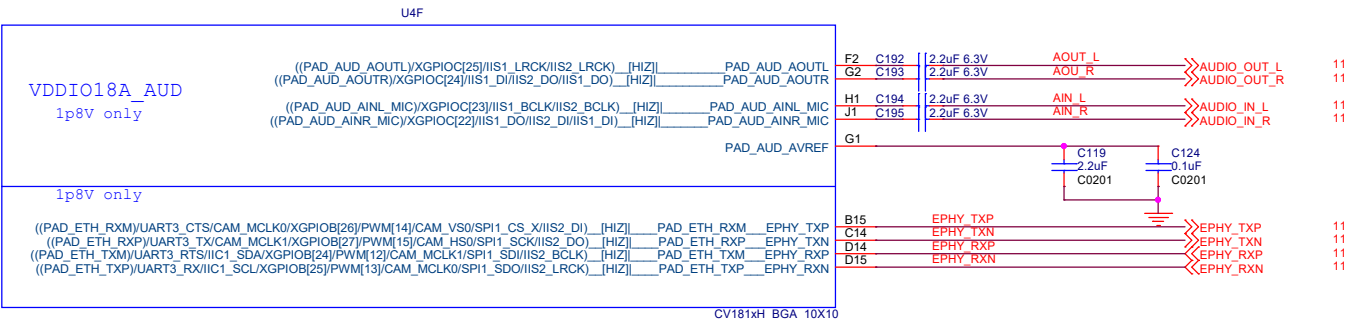
Power&WIFI BT



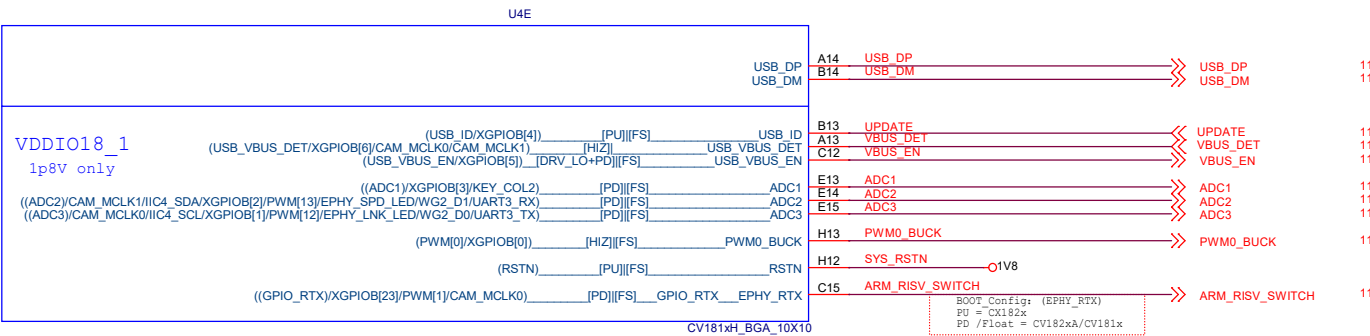




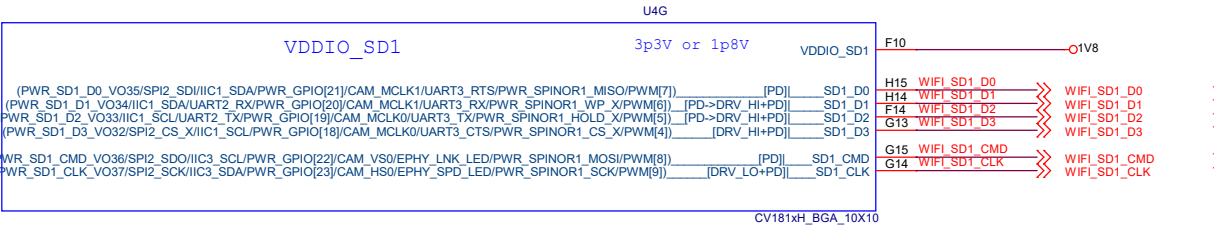
AUDIO&EPHY



USB&VDDIO18

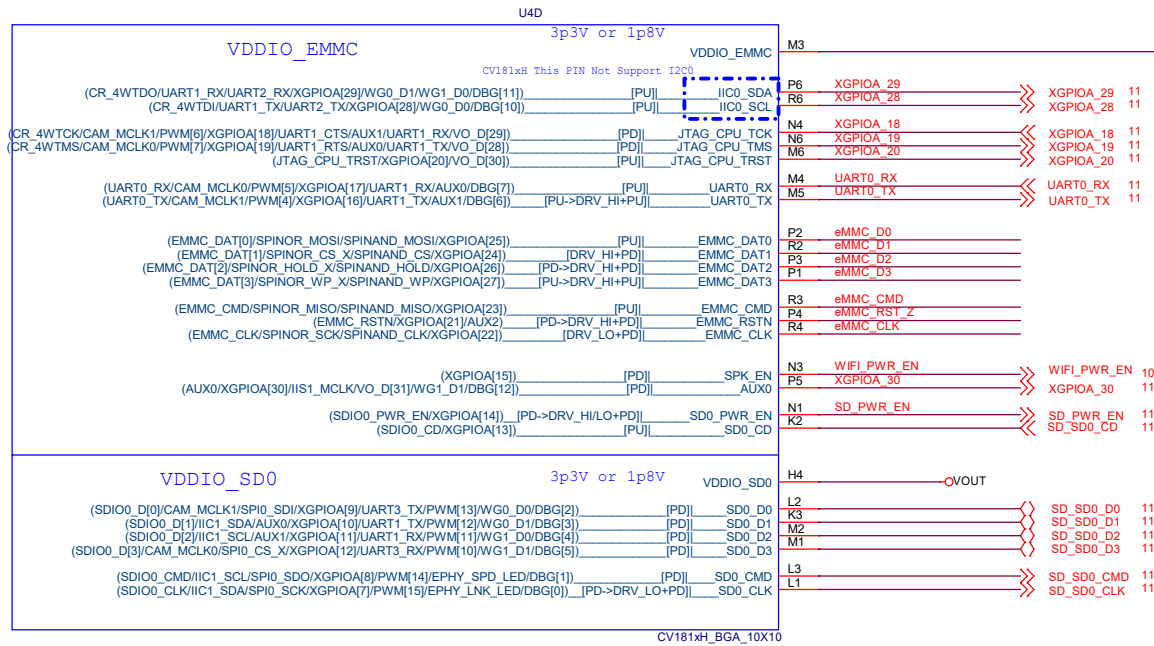


VDDIO_SD1

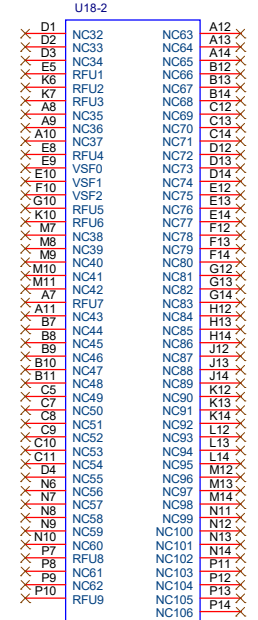
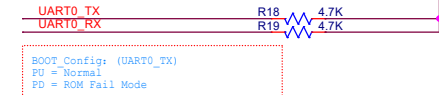


milkV			
Project:	DUO MODULE		
File:	06 SG2002 PART2		
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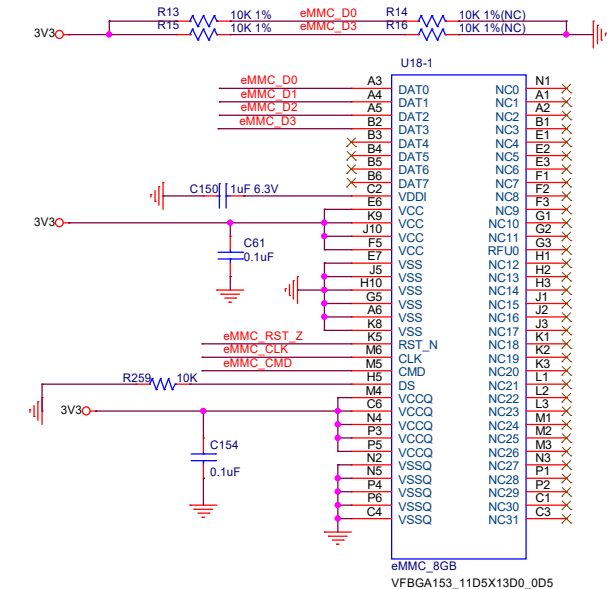
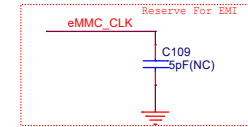
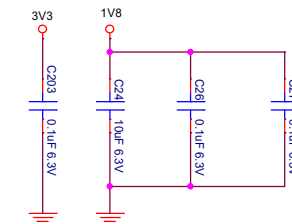
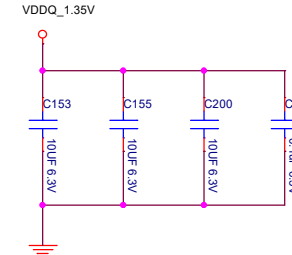
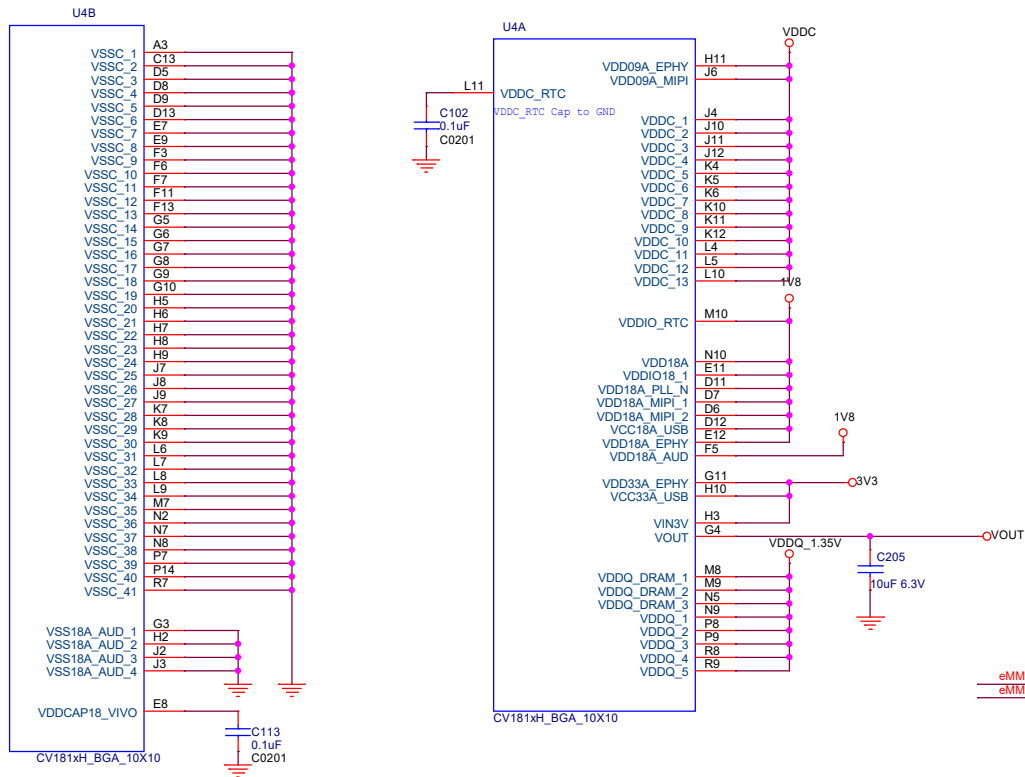
SG2000 PART III



eMMC_D0	eMMC_D3	boot device selection
1	1	eMMC
1	0	Serial NOR
0	0	Serial NAND

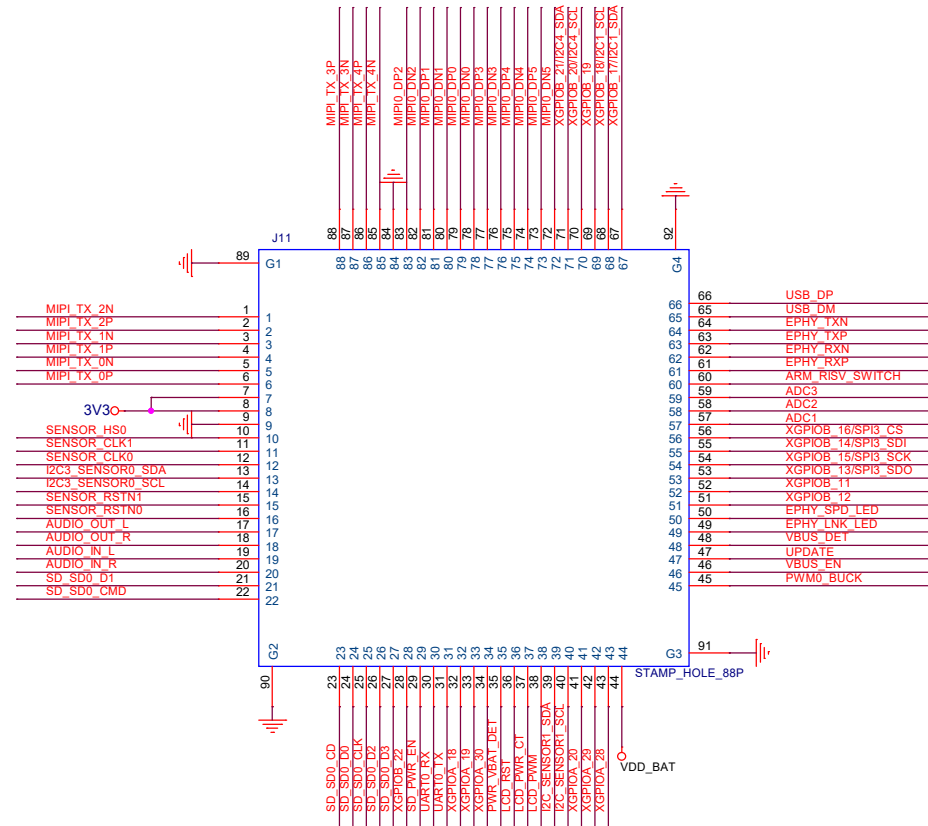


EMMC




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File:	07 SG2000 PART III&EMMC		
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STAMP_PINS



MIP1 TX_0N		MIP1 TX_0P	6
MIP1 TX_0N	>>	MIP1 TX_0N	6
MIP1 TX_1P		MIP1 TX_1P	6
MIP1 TX_1N		MIP1 TX_1N	6
MIP1 TX_2P		MIP1 TX_2P	6
MIP1 TX_2N		MIP1 TX_2N	6
MIP1 TX_3P		MIP1 TX_3P	6
MIP1 TX_3N		MIP1 TX_3N	6
MIP1 TX_4P		MIP1 TX_4P	6
MIP1 TX_4N		MIP1 TX_4N	6
SSENSOR HS0		SSENSOR HS0	6
SSENSOR RSTN1	>>	SSENSOR HS0 SSENSOR_RSTN1	6 6
SSENSOR_CLK0		SSENSOR_CLK0	6
SSENSOR_CLK1		SSENSOR_CLK1	6
SSENSOR_RSTN0	>>	SSENSOR_RSTN0	6
MIP0_DP0		MIP0_DP0	6
MIP0_DN0		MIP0_DN0	6
MIP0_DP1		MIP0_DP1	6
MIP0_DP2		MIP0_DP2	6
MIP0_DP2		MIP0_DP2	6
MIP0_DP3		MIP0_DP3	6
MIP0_DP3		MIP0_DP3	6
MIP0_DP4		MIP0_DP4	6
MIP0_DP4		MIP0_DP4	6
MIP0_DP5		MIP0_DP5	6
MIP0_DN5		MIP0_DN5	6
I2C3_SENSOR0_SCL		I2C3_SENSOR0_SCL	6
I2C3_SENSOR0_SDA		I2C3_SENSOR0_SDA	6
I2C_SENSOR1_SCL		I2C_SENSOR1_SCL	7
I2C_SENSOR1_SDA		I2C_SENSOR1_SDA	9
SD_PWR_EN		SD_PWR_EN	9
SD_SD0_CLK		SD_SD0_CLK	9
SD_SD0_CMD		SD_SD0_CMD	9
SD_SD0_D0		SD_SD0_D0	9
SD_SD0_D1		SD_SD0_D1	9
SD_SD0_D2		SD_SD0_D2	9
SD_SD0_D3		SD_SD0_D3	9
SD_SD0_CD		SD_SD0_CD	9
AUDIO_OUT_L		AUDIO_OUT_L	8
AUDIO_OUT_R		AUDIO_OUT_R	8
AUDIO_IN_L		AUDIO_IN_L	8
AUDIO_IN_R		AUDIO_IN_R	8
EPHY_LNK_LED		EPHY_LNK_LED	7
EPHY_SPD_LED		EPHY_SPD_LED	8
EPHY_RXN		EPHY_RXN	8
EPHY_RXP		EPHY_RXP	8
EPHY_TXN		EPHY_TXN	8
EPHY_TXP		EPHY_TXP	8
USB_DP		USB_DP	8
USB_DM		USB_DM	8
UPDATE		UPDATE	8
VBUS_DET		VBUS_DET	8
VBUS_EN		VBUS_EN	8
ARM_RISV_SWITCH		ARM_RISV_SWITCH	8
PWR_VBAT_DET	>>	PWR_VBAT_DET	7
XGPIOB_21/I2C4_SDA		XGPIOB_21/I2C4_SDA	8
XGPIOB_20/I2C4_SCL		XGPIOB_20/I2C4_SCL	8
XGPIOB_19		XGPIOB_19	8
XGPIOB_18/I2C1_SCL		XGPIOB_18/I2C1_SCL	8
XGPIOB_17/I2C1_SDA		XGPIOB_17/I2C1_SDA	8
XGPIOB_16/SPI3_CS		XGPIOB_16/SPI3_CS	8
XGPIOB_15/SPI3_SCK		XGPIOB_15/SPI3_SCK	8
XGPIOB_14/SPI3_SDI		XGPIOB_14/SPI3_SDI	8
XGPIOB_13/SPI3_SDO		XGPIOB_13/SPI3_SDO	8
XGPIOB_12		XGPIOB_12	8
XGPIOB_11		XGPIOB_11	8
XGPIOA_30		XGPIOA_30	9
ADC1		ADC1	8
ADC2		ADC2	8
ADC3		ADC3	8
XGPIOA_20		XGPIOA_20	9
LCD_PWM		LCD_PWM	7
LCD_PWR_CT		LCD_PWR_CT	7
LCD_RST		LCD_RST	7
UART0_TX		UART0_TX	9
UART0_RX		UART0_RX	9
PWM0_BUCK		PWM0_BUCK	8
XGPIOA_19		XGPIOA_19	9
XGPIOA_18		XGPIOA_18	9
XGPIOA_29		XGPIOA_29	11
XGPIOA_28		XGPIOA_28	11
XGPIOB_22		XGPIOB_22	11

			
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File:	08_STAMP_HOLE		
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