



## 1. Description

### 1.1. Project

|                 |                   |
|-----------------|-------------------|
| Project Name    | pcb_fft           |
| Board Name      | custom            |
| Generated with: | STM32CubeMX 6.9.0 |
| Date            | 10/23/2023        |

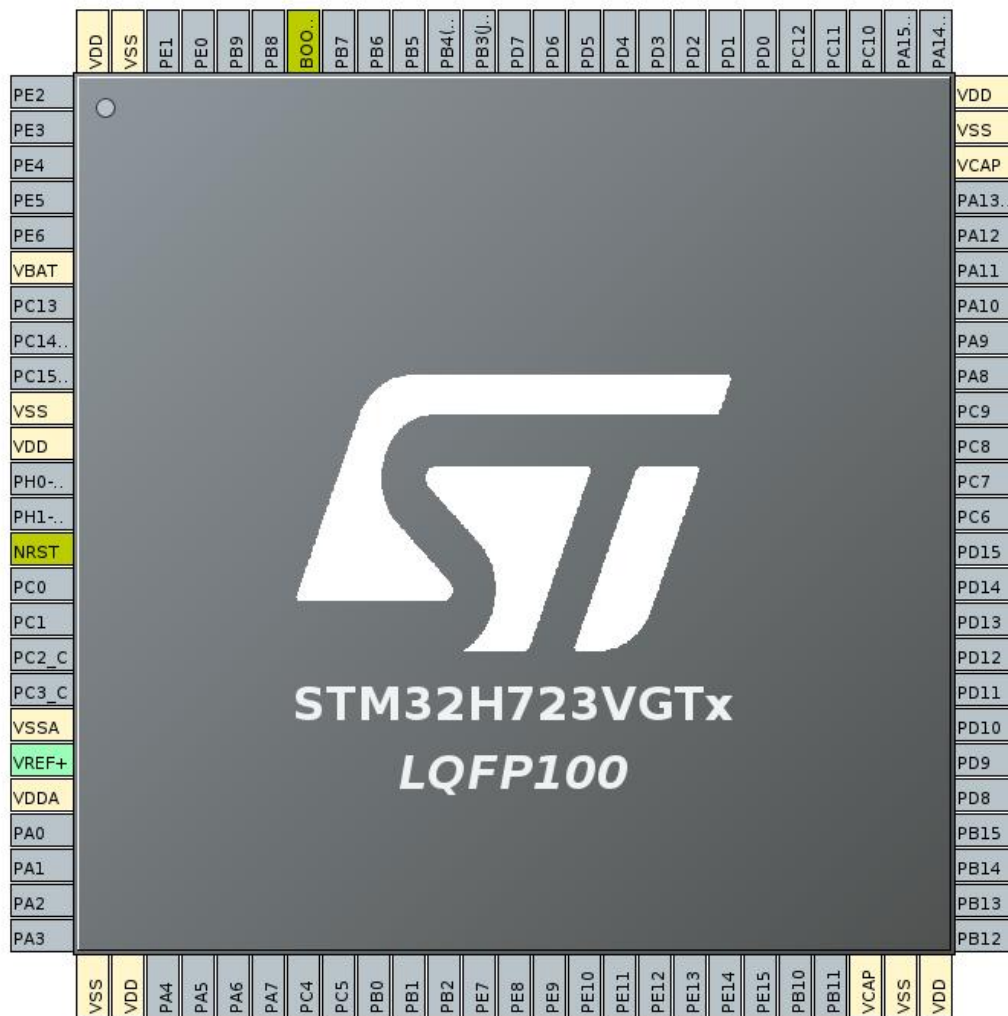
### 1.2. MCU

|                |               |
|----------------|---------------|
| MCU Series     | STM32H7       |
| MCU Line       | STM32H723/733 |
| MCU name       | STM32H723VGTx |
| MCU Package    | LQFP100       |
| MCU Pin number | 100           |

### 1.3. Core(s) information

|         |               |
|---------|---------------|
| Core(s) | Arm Cortex-M7 |
|---------|---------------|

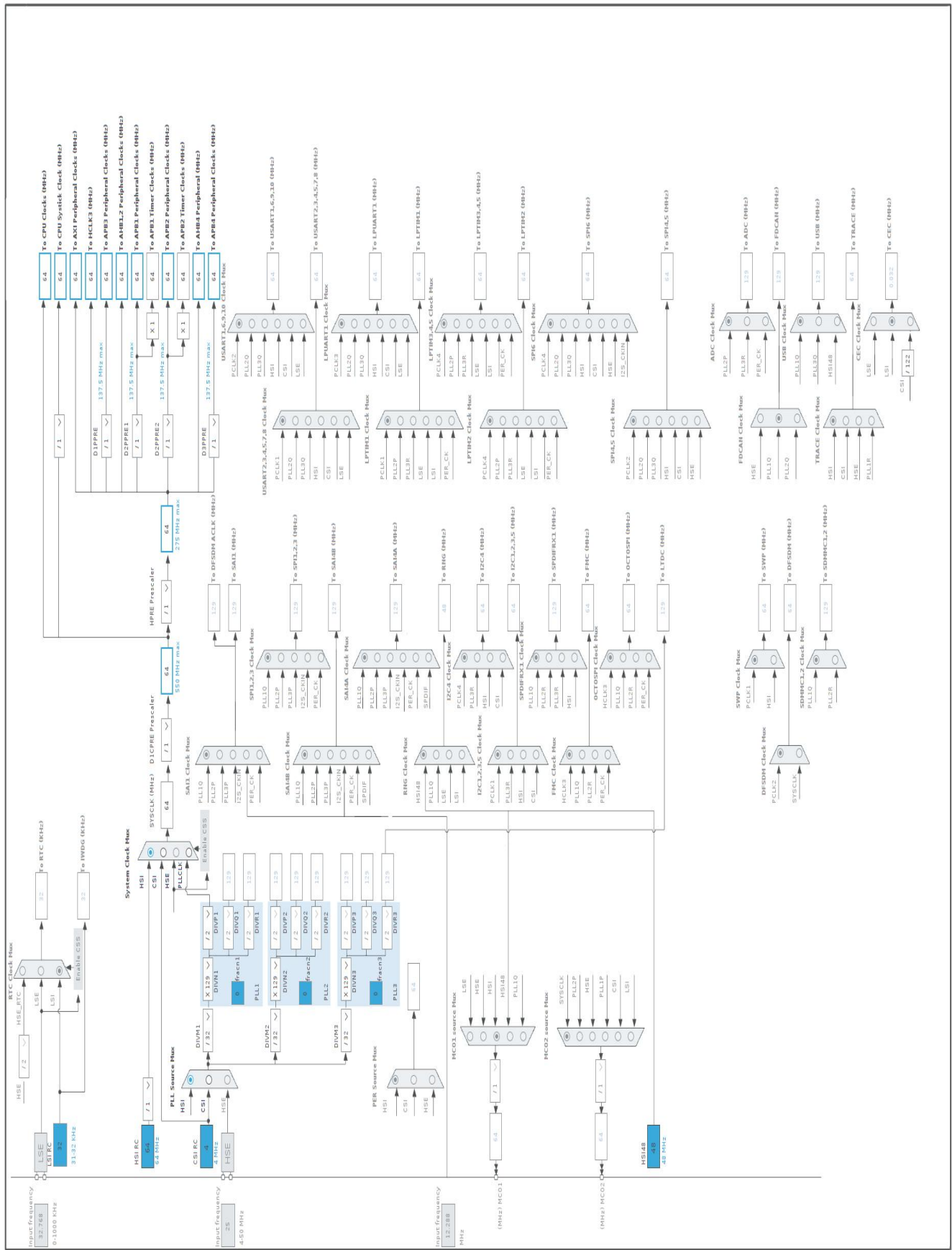
## 2. Pinout Configuration



### 3. Pins Configuration

| Pin Number<br>LQFP100 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label |
|-----------------------|---------------------------------------|----------|--------------------------|-------|
| 6                     | VBAT                                  | Power    |                          |       |
| 10                    | VSS                                   | Power    |                          |       |
| 11                    | VDD                                   | Power    |                          |       |
| 14                    | NRST                                  | Reset    |                          |       |
| 19                    | VSSA                                  | Power    |                          |       |
| 21                    | VDDA                                  | Power    |                          |       |
| 26                    | VSS                                   | Power    |                          |       |
| 27                    | VDD                                   | Power    |                          |       |
| 48                    | VCAP                                  | Power    |                          |       |
| 49                    | VSS                                   | Power    |                          |       |
| 50                    | VDD                                   | Power    |                          |       |
| 73                    | VCAP                                  | Power    |                          |       |
| 74                    | VSS                                   | Power    |                          |       |
| 75                    | VDD                                   | Power    |                          |       |
| 94                    | BOOT0                                 | Boot     |                          |       |
| 99                    | VSS                                   | Power    |                          |       |
| 100                   | VDD                                   | Power    |                          |       |

## 4. Clock Tree Configuration



## 5. Software Project

### 5.1. Project Settings

| Name                              | Value   |
|-----------------------------------|---|
| Project Name                      | pcb_fft   |
| Project Folder                    | /home/clayton/Documents/university/fifth_year/second_semester/fyp-nubots- |
| Toolchain / IDE                   | STM32CubeIDE  |
| Firmware Package Name and Version | STM32Cube FW_H7 V1.11.1   |
| Application Structure             | Advanced  |
| Generate Under Root               | Yes   |
| Do not generate the main()        | No  |
| Minimum Heap Size                 | 0x200   |
| Minimum Stack Size                | 0x400   |

### 5.2. Code Generation Settings

| Name  | Value                                 |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software                    | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files   | No                                    |
| Backup previously generated files when re-generating            | No                                    |
| Keep User Code when re-generating                               | Yes                                   |
| Delete previously generated files when not re-generated         | Yes                                   |
| Set all free pins as analog (to optimize the power consumption) | No                                    |
| Enable Full Assert  | No                                    |

### 5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name      | Peripheral Instance Name |
|------|--------------------|--------------------------|
| 1    | SystemClock_Config | RCC                      |

## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

|           |               |
|-----------|---------------|
| Series    | STM32H7       |
| Line      | STM32H723/733 |
| MCU       | STM32H723VGTx |
| Datasheet | DS13313_Rev1  |

### 1.2. Parameter Selection

|             |     |
|-------------|-----|
| Temperature | 25  |
| Vdd         | 3.0 |

### 1.3. Battery Selection

|                   |              |
|-------------------|--------------|
| Battery           | Alkaline(9V) |
| Capacity          | 625.0 mAh    |
| Self Discharge    | 0.3 %/month  |
| Nominal Voltage   | 9.0 V        |
| Max Cont Current  | 200.0 mA     |
| Max Pulse Current | 0.0 mA       |
| Cells in series   | 1            |
| Cells in parallel | 1            |

## 1.4. Sequence

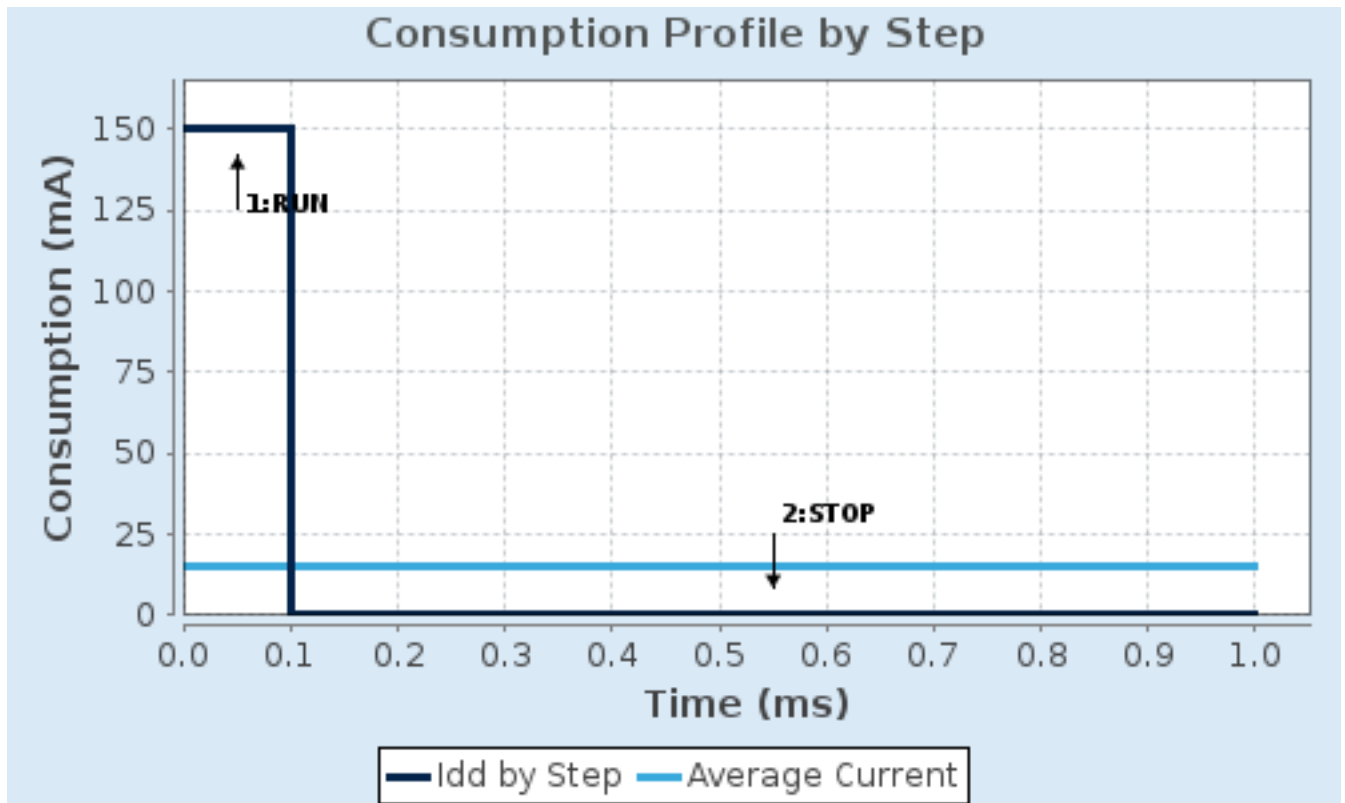
|                               |                          |                      |
|-------------------------------|--------------------------|----------------------|
| <b>Step</b>                   | Step1                    | Step2                |
| <b>Mode</b>                   | RUN                      | STOP                 |
| <b>Vdd</b>                    | 3.0                      | 3.0                  |
| <b>Voltage Source</b>         | Battery                  | Battery              |
| <b>Range</b>                  | VOS0: Scale0/Boost       | SVOS5: System-Scale5 |
| <b>D1 Mode</b>                | DRUN                     | DSTANDBY             |
| <b>D2 Mode</b>                | DRUN                     | DSTANDBY             |
| <b>D3 Mode</b>                | DRUN                     | DSTOP                |
| <b>Fetch Type</b>             | SRAM1/FlashMode-ON/Cache | NA                   |
| <b>CPU Frequency</b>          | 550 MHz                  | 0 Hz                 |
| <b>Clock Configuration</b>    | HSE BYP PLL              | ALL CLOCKS OFF       |
| <b>Clock Source Frequency</b> | 8 MHz                    | 0 Hz                 |
| <b>Peripherals</b>            |                          |                      |
| <b>Additional Cons.</b>       | 0 mA                     | 0 mA                 |
| <b>Average Current</b>        | 150 mA                   | 94.5 $\mu$ A         |
| <b>Duration</b>               | 0.1 ms                   | 0.9 ms               |
| <b>DMIPS</b>                  | 1177.0                   | 0.0                  |
| <b>Ta Max</b>                 | 104.75                   | 124.99               |
| <b>Category</b>               | In DS Table              | In DS Table          |

## 1.5. Results

|               |                 |                 |              |
|---------------|-----------------|-----------------|--------------|
| Sequence Time | 1 ms            | Average Current | 15.09 mA     |
| Battery Life  | 1 day, 17 hours | Average DMIPS   | 1177.0 DMIPS |

## 1.6. Chart





## 2. Peripherals and Middlewares Configuration

### 2.1. CORTEX\_M7

#### 2.1.1. Parameter Settings:

##### Speculation default mode Settings:

Speculation default mode Enabled

##### Cortex Interface Settings:

CPU ICache Disabled

CPU DCache Disabled

##### Cortex Memory Protection Unit Control Settings:

MPU Control Mode Background Region Privileged accesses only + MPU Disabled during hard fault, NMI and FAULTMASK handlers

##### Cortex Memory Protection Unit Region 0 Settings:

MPU Region Enabled

MPU Region Base Address **0x0 \***

MPU Region Size 4GB

MPU SubRegion Disable **0x87 \***

MPU TEX field level level 0

MPU Access Permission ALL ACCESS NOT PERMITTED

MPU Instruction Access DISABLE

MPU Shareability Permission ENABLE

MPU Cacheable Permission DISABLE

MPU Bufferable Permission DISABLE

##### Cortex Memory Protection Unit Region 1 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 2 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 3 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 4 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 5 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 6 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 7 Settings:

MPU Region Disabled

##### Cortex Memory Protection Unit Region 8 Settings:

MPU Region Disabled

**Cortex Memory Protection Unit Region 9 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 10 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 11 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 12 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 13 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 14 Settings:**

MPU Region Disabled

**Cortex Memory Protection Unit Region 15 Settings:**

MPU Region Disabled

## 2.2. RCC

### 2.2.1. Parameter Settings:

**Power Parameters:**

|                               |                                 |
|-------------------------------|---------------------------------|
| SupplySource                  | PWR_LDO_SUPPLY                  |
| Power Regulator Voltage Scale | Power Regulator Voltage Scale 3 |

**RCC Parameters:**

|                                |          |
|--------------------------------|----------|
| TIM Prescaler Selection        | Disabled |
| HSE Startup Timeout Value (ms) | 100      |
| LSE Startup Timeout Value (ms) | 5000     |
| CSI Calibration Value          | 16       |
| HSI Calibration Value          | 64       |

**System Parameters:**

|                   |                    |
|-------------------|--------------------|
| VDD voltage (V)   | 3.3                |
| Flash Latency(WS) | 1 WS (2 CPU cycle) |

\* User modified value

## 3. System Configuration

### 3.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull<br>down | Max<br>Speed | User Label |
|----|-----|--------|-----------|---------------------------|--------------|------------|
|----|-----|--------|-----------|---------------------------|--------------|------------|

### 3.2. DMA configuration

nothing configured in DMA service

### 3.3. BDMA configuration

nothing configured in DMA service

### 3.4. MDMA configuration

nothing configured in DMA service

### 3.5. NVIC configuration

#### 3.5.1. NVIC

| Interrupt Table                               | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt                        | true   | 0                    | 0           |
| Hard fault interrupt                          | true   | 0                    | 0           |
| Memory management fault                       | true   | 0                    | 0           |
| Pre-fetch fault, memory access fault          | true   | 0                    | 0           |
| Undefined instruction or illegal state        | true   | 0                    | 0           |
| System service call via SWI instruction       | true   | 0                    | 0           |
| Debug monitor                                 | true   | 0                    | 0           |
| Pendable request for system service           | true   | 0                    | 0           |
| System tick timer                             | true   | 15                   | 0           |
| PVD/AVD through EXTI Line detection Interrupt | unused |                      |             |
| Flash global interrupt                        | unused |                      |             |
| RCC global interrupt                          | unused |                      |             |
| FPU global interrupt                          | unused |                      |             |
| HSEM1 global interrupt                        | unused |                      |             |

#### 3.5.2. NVIC Code generation

| Enabled interrupt Table                 | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt                  | false                             | true                 | false            |
| Hard fault interrupt                    | false                             | true                 | false            |
| Memory management fault                 | false                             | true                 | false            |
| Pre-fetch fault, memory access fault    | false                             | true                 | false            |
| Undefined instruction or illegal state  | false                             | true                 | false            |
| System service call via SWI instruction | false                             | true                 | false            |
| Debug monitor                           | false                             | true                 | false            |
| Pendable request for system service     | false                             | true                 | false            |
| System tick timer                       | false                             | true                 | true             |

\* User modified value

## 4. System Views

### 4.1. Category view

#### 4.1.1. Current

Middleware

System Core      Analog      Timers      Connectivity      Multimedia      Security      Computing      Trace and Debugger and Thermal

- BDMA
- CORTEX\_M7 ✓
- DMA
- MDMA
- NVIC ✓
- RCC ✓

## 5. Docs & Resources

| Type                    | Link  |
|-------------------------|---|
| BSDL files              | <a href="https://www.st.com/resource/en/bsdl_model/stm32h7_bsd1.zip">https://www.st.com/resource/en/bsdl_model/stm32h7_bsd1.zip</a>   |
| IBIS models             | <a href="https://www.st.com/resource/en/ibis_model/stm32h7_ibis.zip">https://www.st.com/resource/en/ibis_model/stm32h7_ibis.zip</a>   |
| System View Description | <a href="https://www.st.com/resource/en/svd/stm32h7-svd.zip">https://www.st.com/resource/en/svd/stm32h7-svd.zip</a>   |
| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/microcontrollers_stm32h7_series_product_overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers_stm32h7_series_product_overview.pdf</a>   |
| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>   |
| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>   |
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| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf</a>   |
| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/microcontrollers_stm32h72x-3x_line_product-overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers_stm32h72x-3x_line_product-overview.pdf</a>                                     |
| Brochures               | <a href="https://www.st.com/resource/en/brochure/brstm32h7.pdf">https://www.st.com/resource/en/brochure/brstm32h7.pdf</a>   |
| Brochures               | <a href="https://www.st.com/resource/en/brochure/brstm32h7vl.pdf">https://www.st.com/resource/en/brochure/brstm32h7vl.pdf</a>   |
| Brochures               | <a href="https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf">https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf</a>   |
| Flyers                  | <a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>   |
| Flyers                  | <a href="https://www.st.com/resource/en/flyer/flstm32trust.pdf">https://www.st.com/resource/en/flyer/flstm32trust.pdf</a>   |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf</a> |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a> |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf</a>     |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an2639-soldering-">https://www.st.com/resource/en/application_note/an2639-soldering-</a>   |

recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf

- Application Notes [https://www.st.com/resource/en/application\\_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf)
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- Application Notes [https://www.st.com/resource/en/application\\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf)
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| Technical Notes<br>& Articles                        | <a href="https://www.st.com/resource/en/technical_note/tn1206-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-qfp-packages-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1206-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-qfp-packages-stmicroelectronics.pdf</a>   |
| Technical Notes<br>& Articles                        | <a href="https://www.st.com/resource/en/technical_note/tn1207-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-so-packages-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1207-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-so-packages-stmicroelectronics.pdf</a>   |

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| Technical Notes<br>& Articles | <a href="https://www.st.com/resource/en/technical_note/tn1208-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-tssop-and-ssop-packages-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1208-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-tssop-and-ssop-packages-stmicroelectronics.pdf</a> |
| Technical Notes<br>& Articles | <a href="https://www.st.com/resource/en/technical_note/tn1433-reference-device-marking-schematics-for-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1433-reference-device-marking-schematics-for-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf</a>                   |